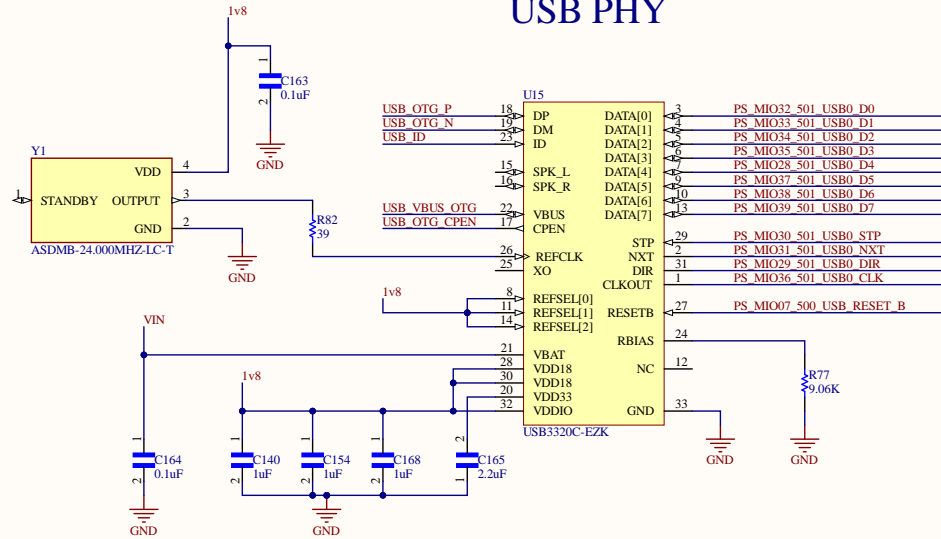


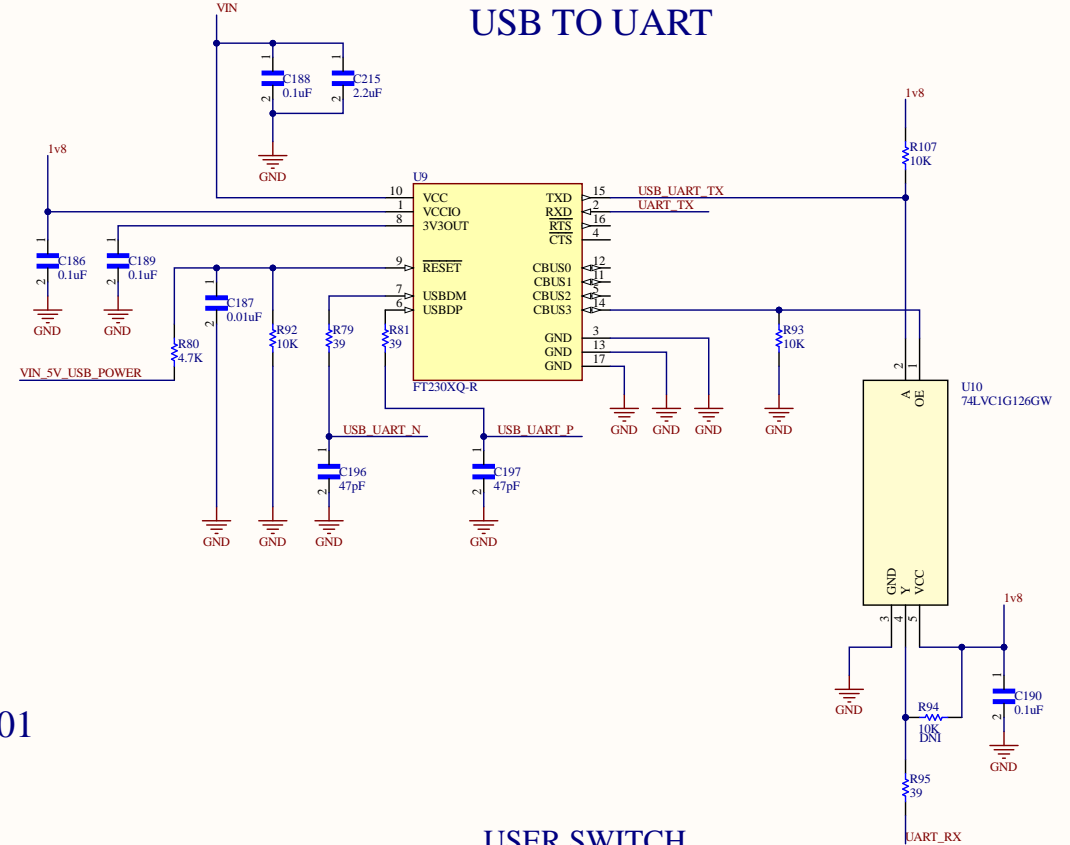
	1	2	3	4	5	6	7	8
A								
B								
C								
D								
	1	2	3	4	5	6	7	8

Title				PlutoV2 Schematic				Revision	
Size		Number						Revision	
A3								A	
Date:		6/06/2022						Sheet of	
File:		D:\EEET 2022\...\Cover.SchDoc						Drawn By: R.Pender	

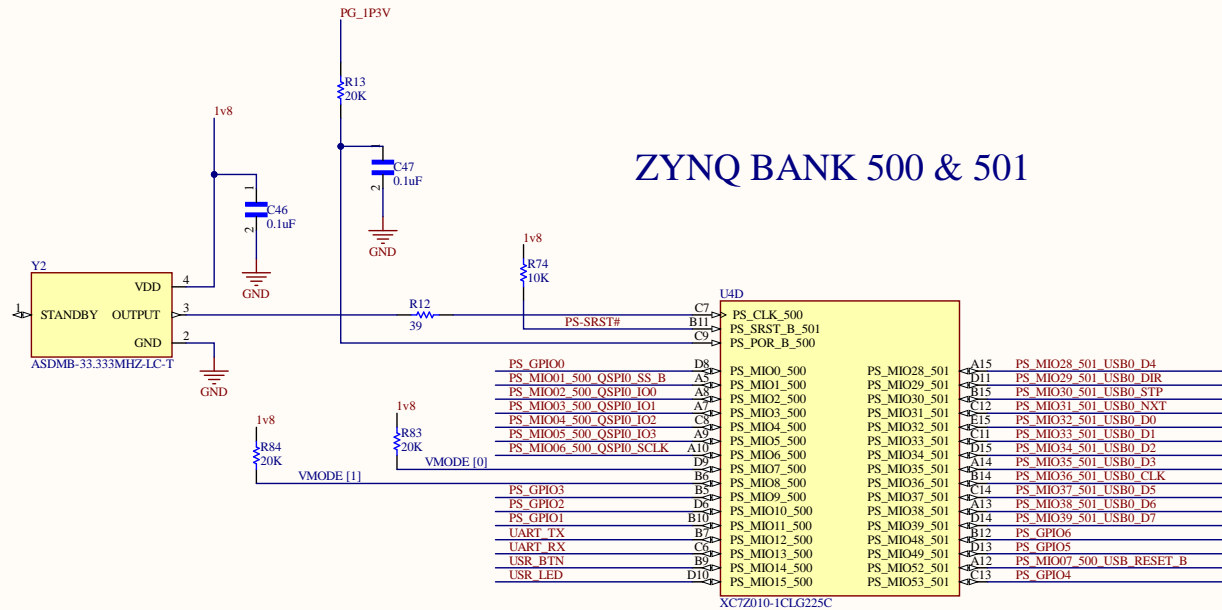
USB PHY



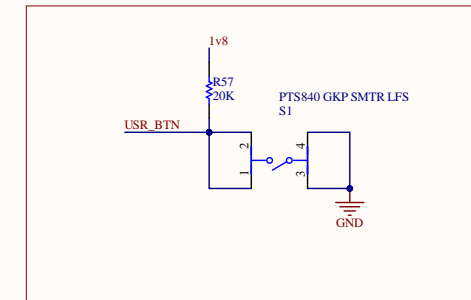
USB TO UART



ZYNQ BANK 500 & 501

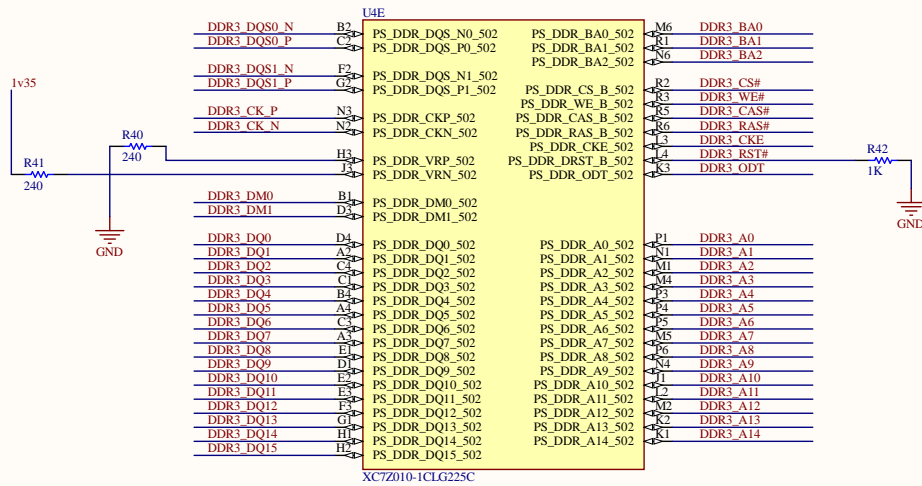


USER SWITCH

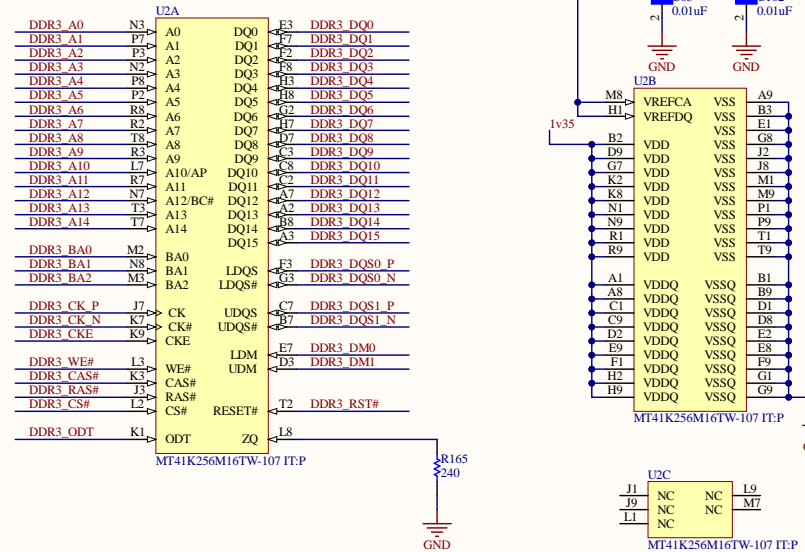


Title PlutoV2 Schematic		
Size A3	Number	Revision A
Date: 6/06/2022	Sheet of	Revision
File: D:\EET 2022\USB.SchDoc	Drawn By:	R.Pender

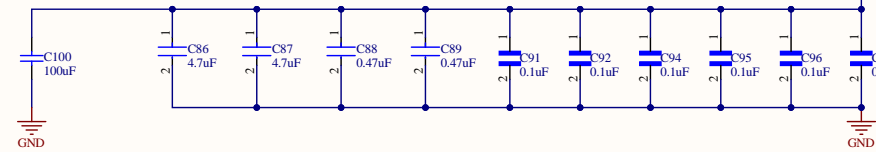
ZYNQ BANK 502 DDR3



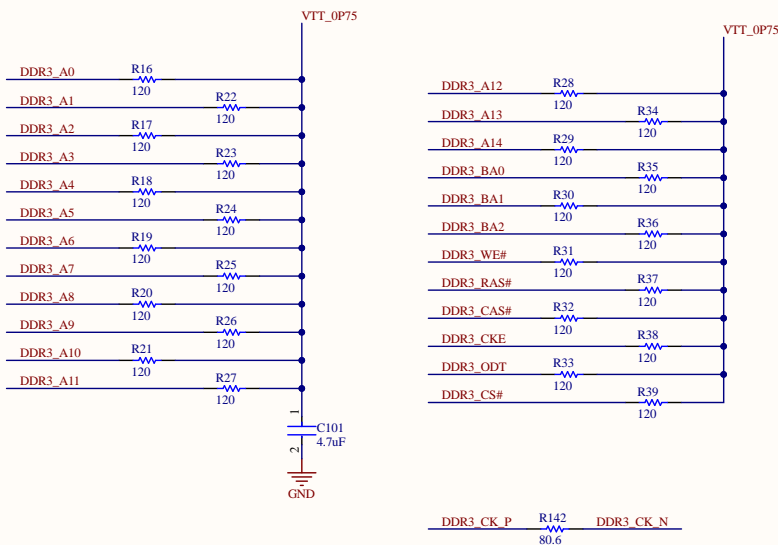
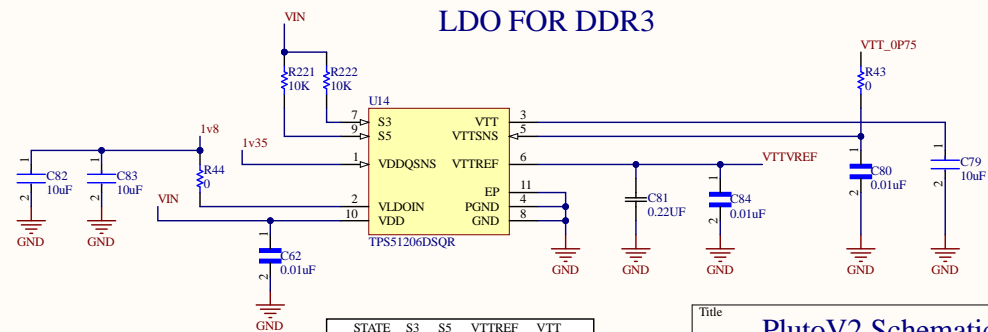
DDR3



DDR3 DECOUPLING - PLACE NEAR DDR3

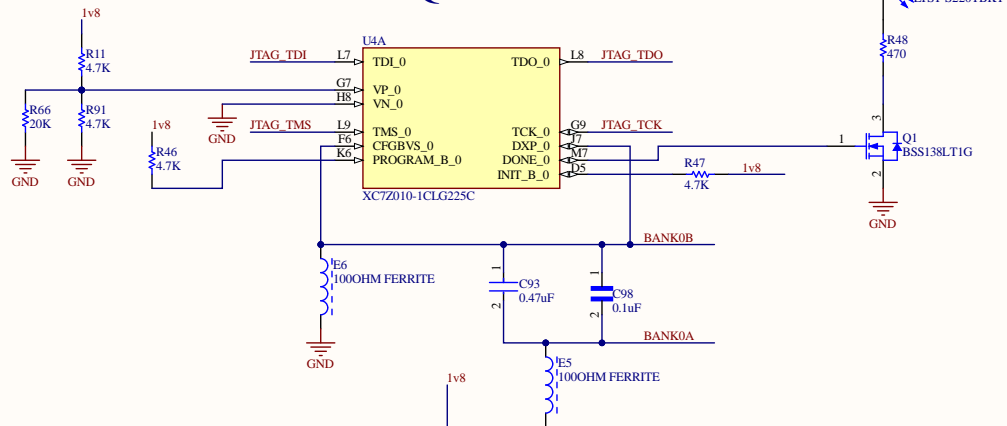


LDO FOR DDR3

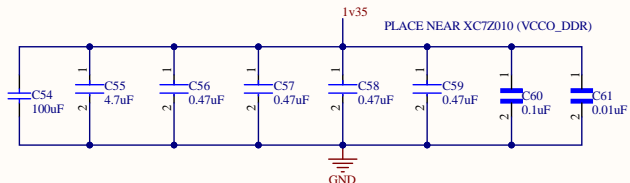


Title		
PlutoV2 Schematic		
Size	Number	Revision
A3		A
Date:	6/06/2022	Sheet of
File:	D:\EEET 2022\...\DDR3.SchDoc	Drawn By: R.Pender

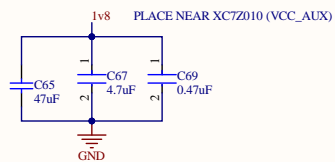
ZYNQ BANK 0



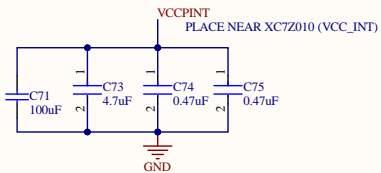
VCCO DDR DECOUPLING



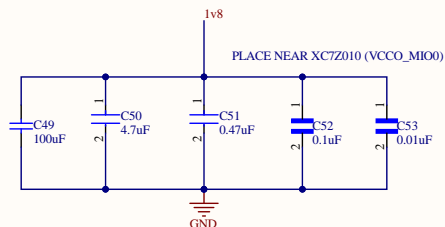
VCC AUX DECOUPLING



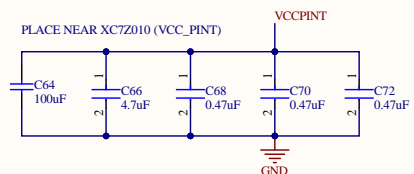
VCC INT DECOUPLING



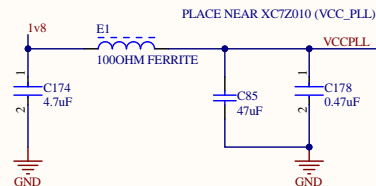
VCCO PAUX DECOUPLING



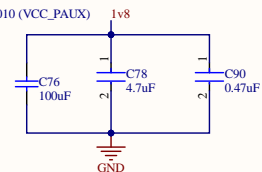
VCC PINT DECOUPLING



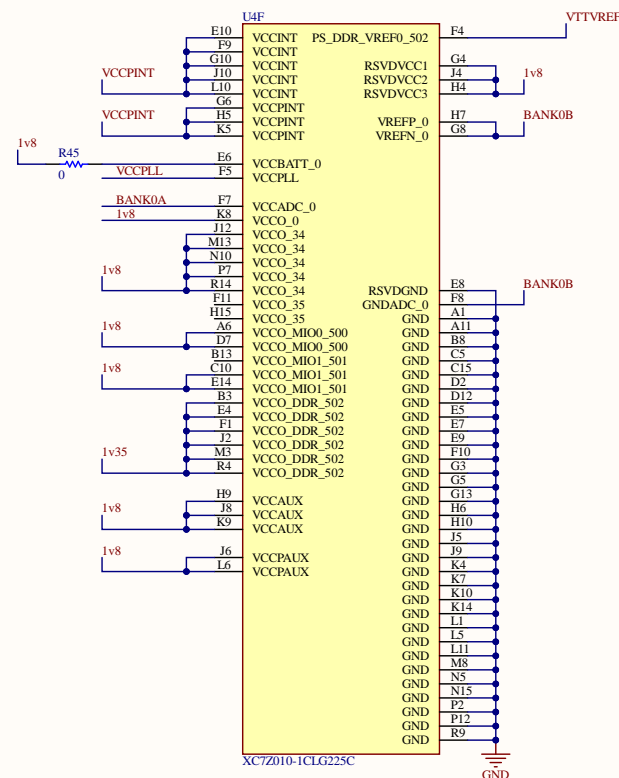
VCC PLL FILTERING



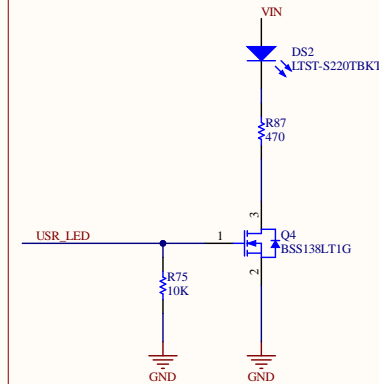
VCC PAUX DECOUPLING



ZYNQ POWER

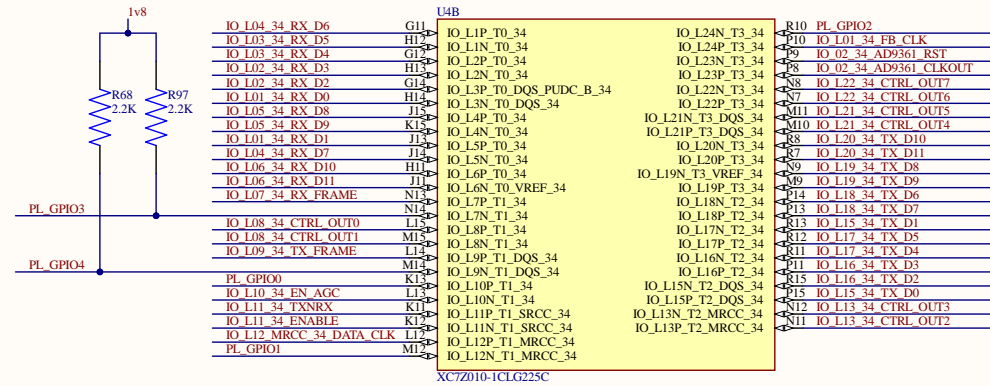


NOTE CHANGE LED PACKAGE

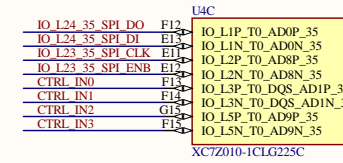


Title PlutoV2 Schematic			
Size A3	Number	Revision A	
Date:	6/06/2022	Sheet of	1
File:	D:\EET 2022\ZYNQ JTAG and Decoupling\BOM\R.Pender		

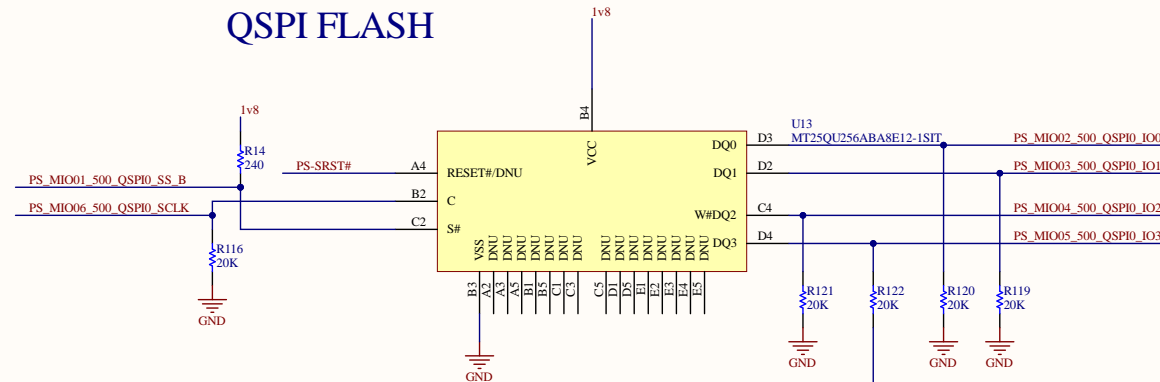
ZYNQ BANK 35



ZYNQ BANK 35



QSPI FLASH



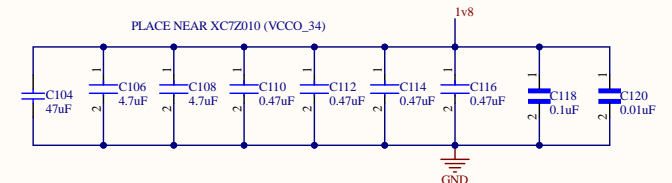
BOOT MODE	MIO5	MIO4
CASCADED JTAG	0	0
QSPI	1	0

MIO5 CAN BE SHORTED TO GND BY INSERTING A JUMPER ON THE JTAG ADAPTER

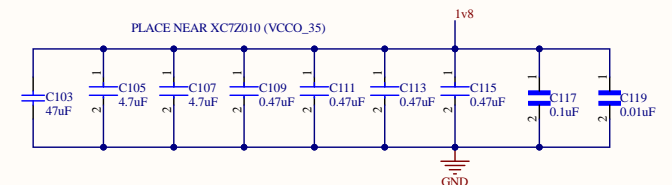
WHEN USING THE ADAPTER, AND WANTING TO HAVE JTAG BOOT (DEVICE DOESN'T BOOT U-BOOT, OR KERNEL):

- INSERT THE JUMPER ON THE ADAPTER (JTAG BOOT)ext
- POWER UP PLUTO
- REMOVE THE JUMPER ON THE ADAPTER, SO YOU CAN TALK TO THE SPI FLASH
- DEBUG AS NORMAL.

ZYNQ BANK 34 DECOUPLING

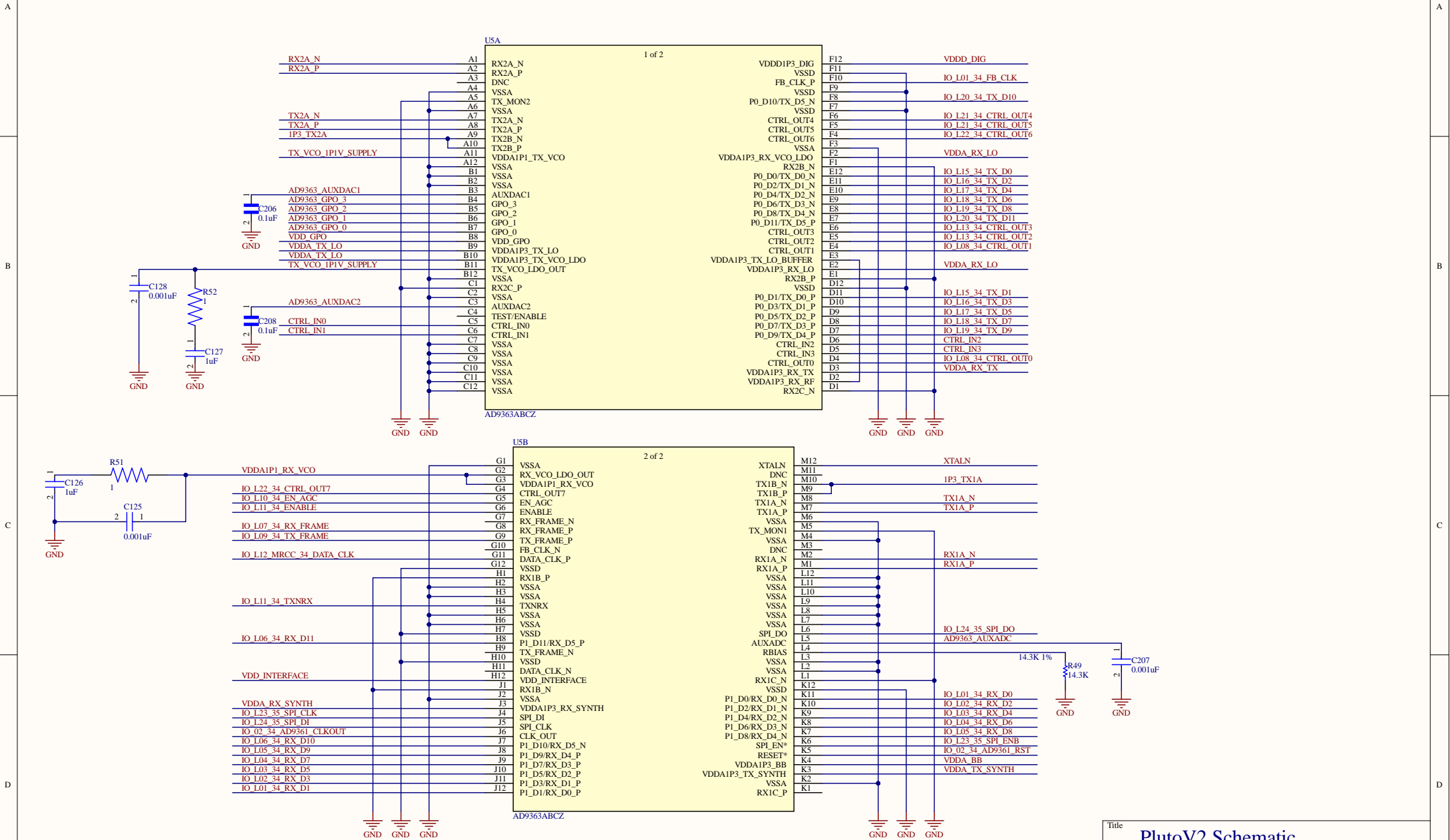


ZYNQ BANK 35 DECOUPLING



Title PlutoV2 Schematic		
Size A3	Number	Revision A
Date: 6/06/2022	Sheet of 8	Drawn By: R.Pender
File: D:\EET 2022\ZYNQ 34_35 and QSPI		

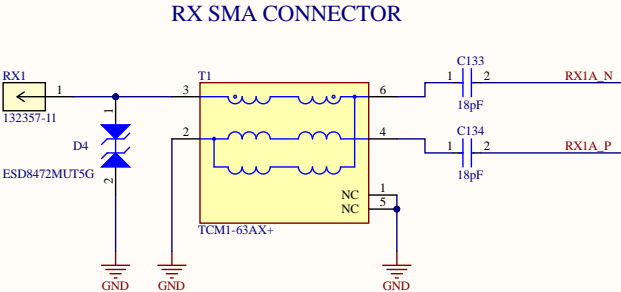
AD9363



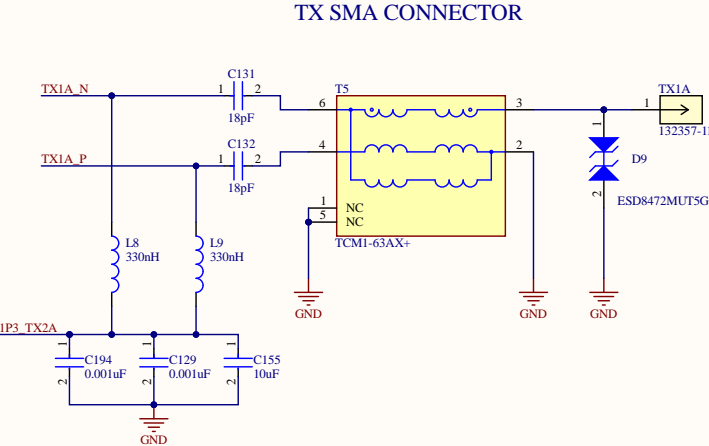
Title PlutoV2 Schematic			
Size A3	Number		Revision A
Date:	6/06/2022	Sheet of	
File:	D:\JEET 2022_\AD9363.SchDoc	Drawn By:	R.Pender

RF FRONTEND

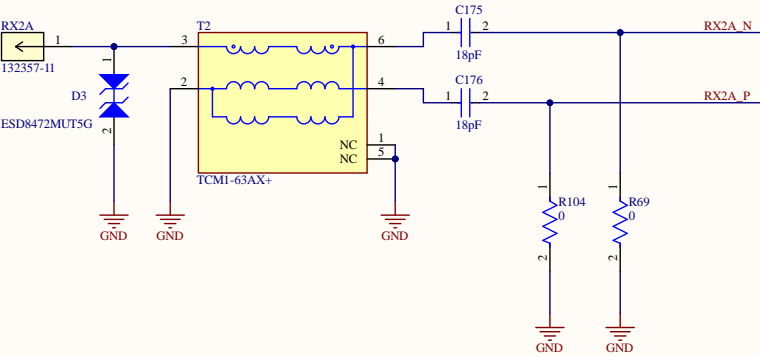
ADD RECEIVER LNA IF REQUIRED



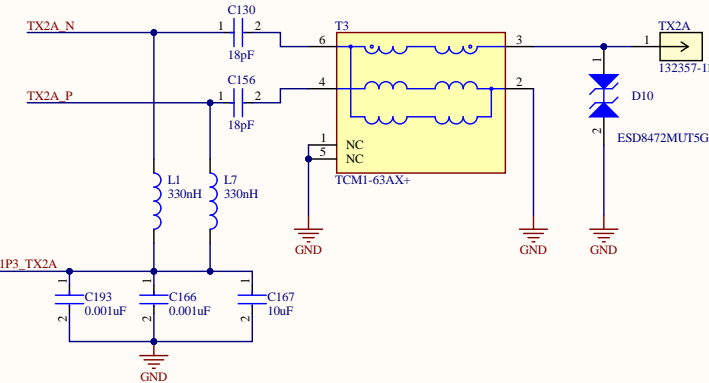
ADD TRANSMITTER PA IF REQUIRED



RX U.FL CONNECTOR



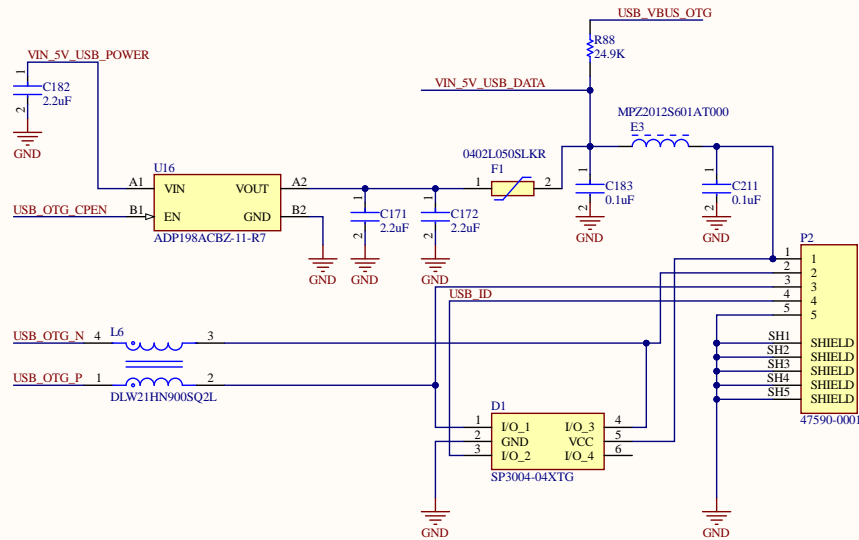
TX U.FL CONNECTOR



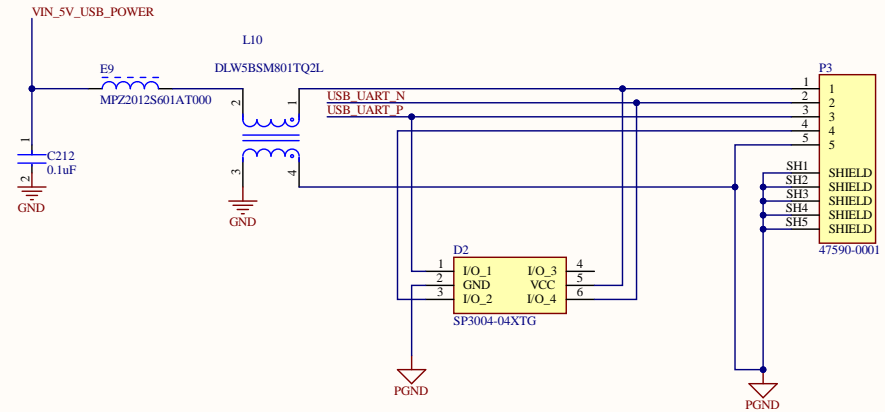
Title		
PlutoV2 Schematic		
Size	Number	Revision
A3		A
Date:	6/06/2022	Sheet of
File:	D:\EET 2022\RF FRONTEND.SchDoc	Drawn By: R.Pender

USB CONNECTORS

USB OTG

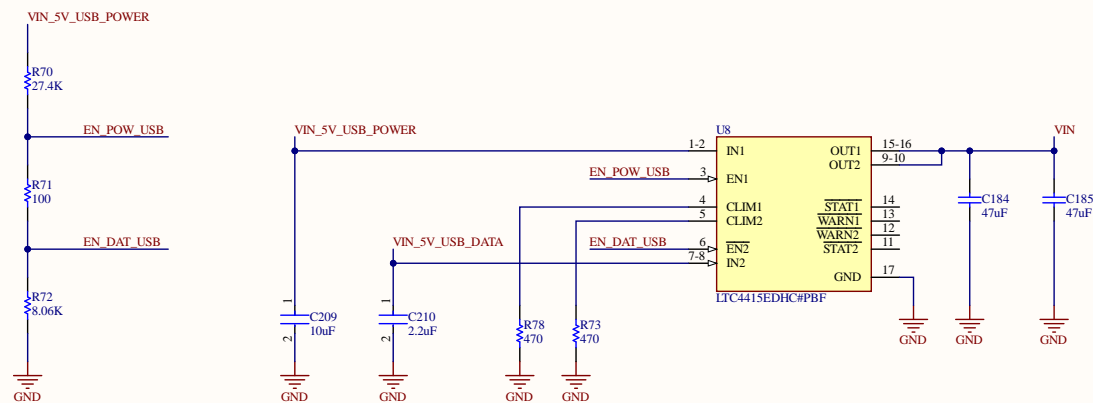


USB POWER ONLY

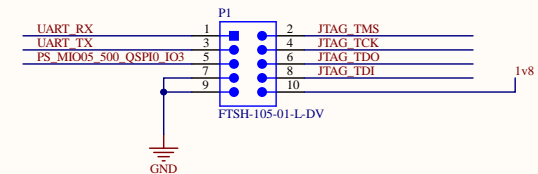


CHOOSE SHIELDING GROUNDING SCHEME

USB POWER SELECTOR

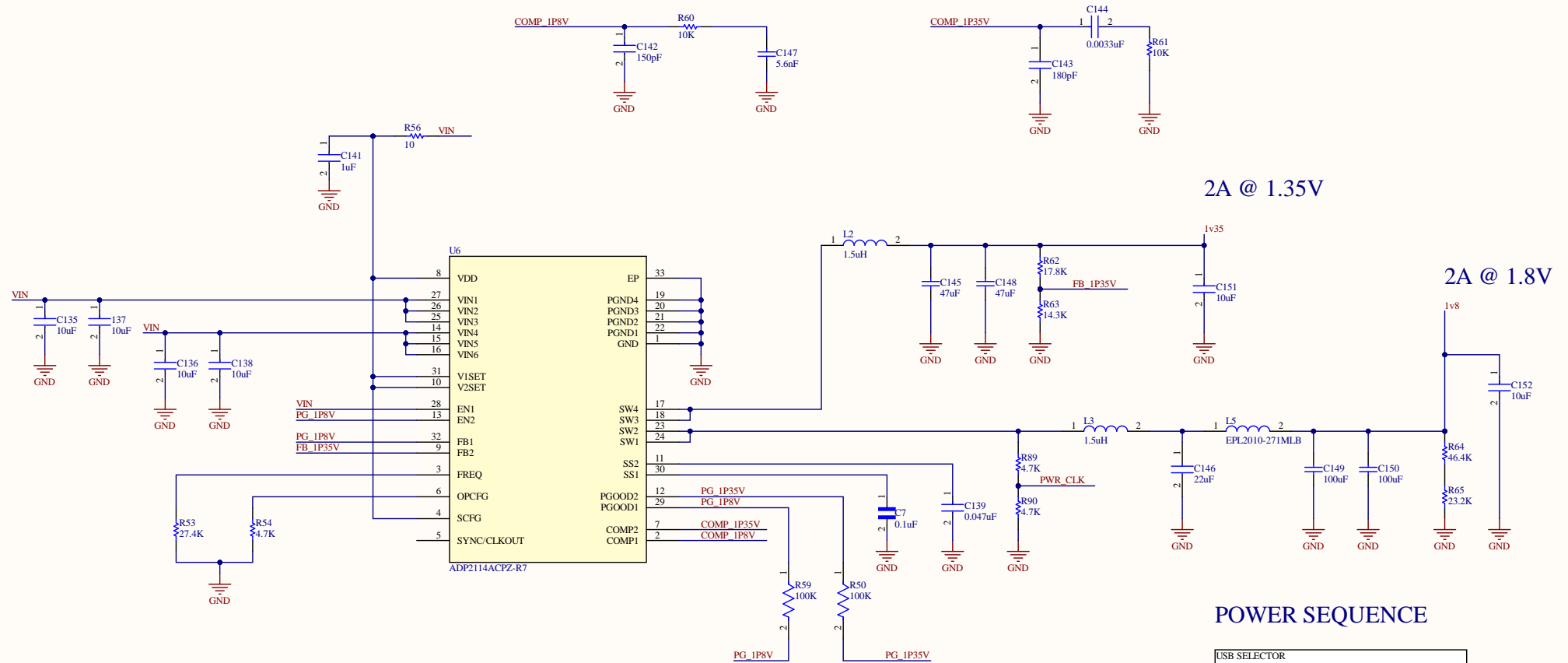


0.05 PITCH JTAG AND UART CONNECTOR



Title			
PlutoV2 Schematic			
Size A3	Number		Revision
			A
Date:	6/06/2022	Sheet of	
File:	D:\EET 2022\CONNECTIONS SELECTED	Trans.SchDoc	R.Pender

1.35V & 1.8V SUPPLY



USB SELECTOR
SLEW LIMIT CURRENT MONITOR

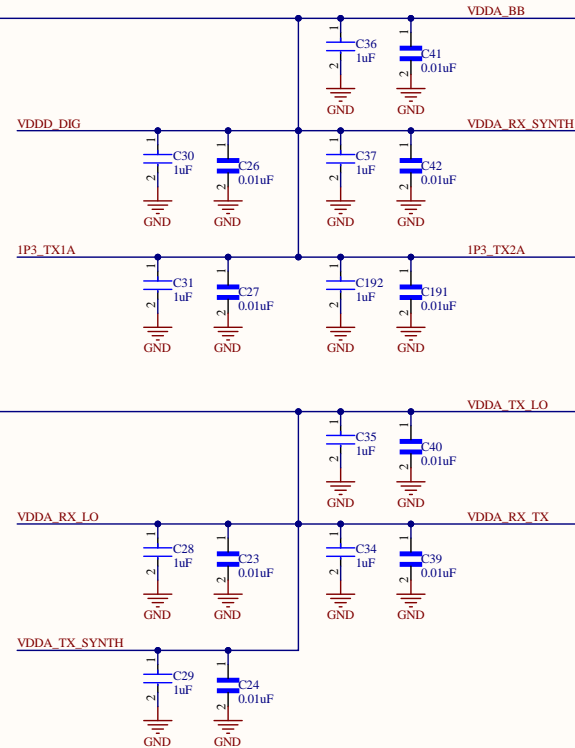
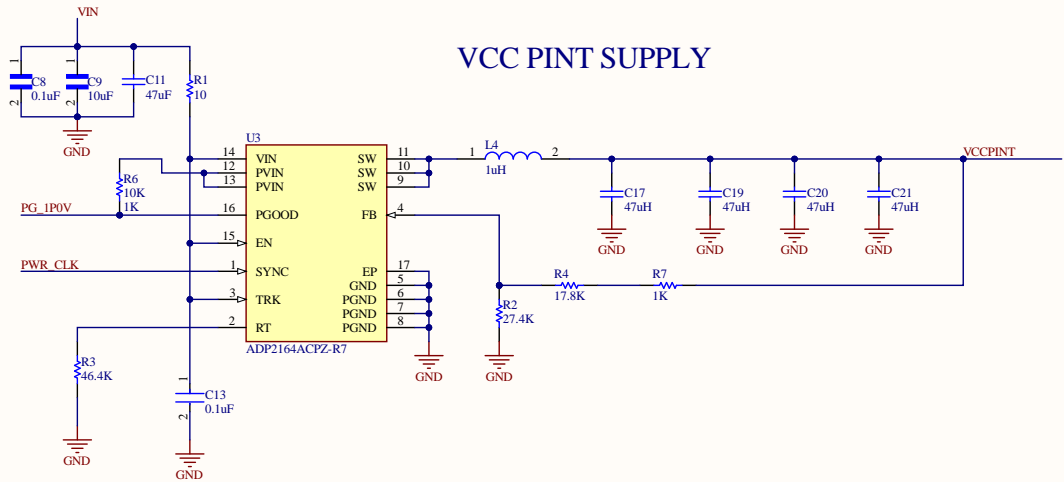
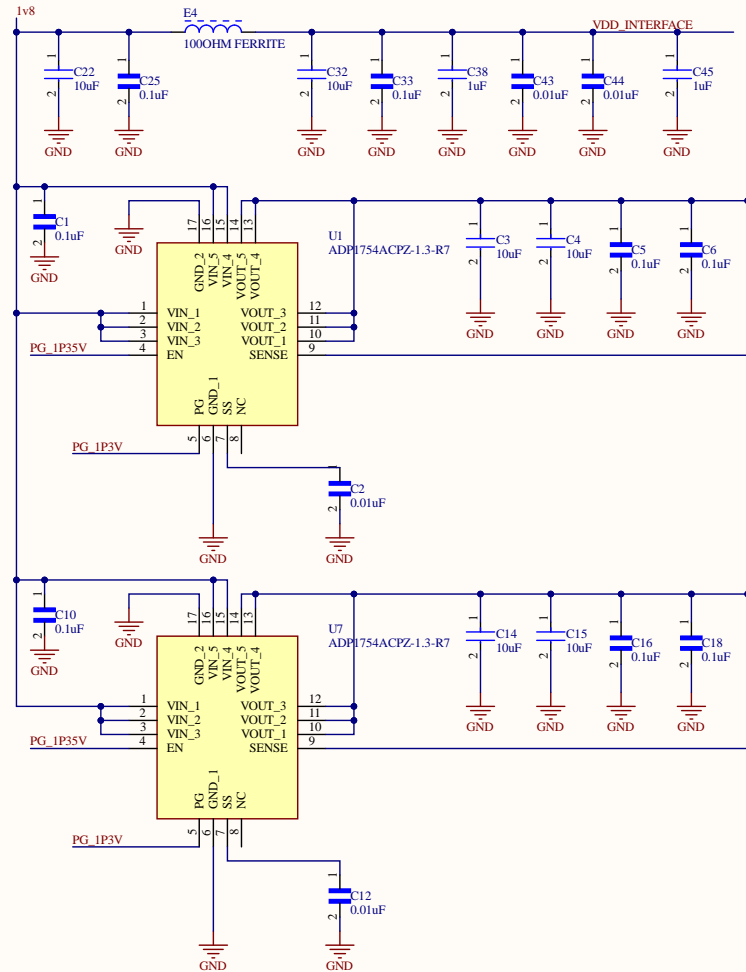
1ST. 1V0
PS: VCCPINT
PL: VCCINT
AD9363: ---

2ND. 1V8 AND 1V3
PS: VCCPAUX, VCC_PLL, VCCO_MIO0, VCC_MIO1
PL: VCCPAUX, VCCO_0, VCCO_34, VCCO_35
AD9363: VDD_INTERFACE (1V8), RF (1V3)

3RD. 1V35
PS: VCCO_DDR
PL: ---
AD9363: ---

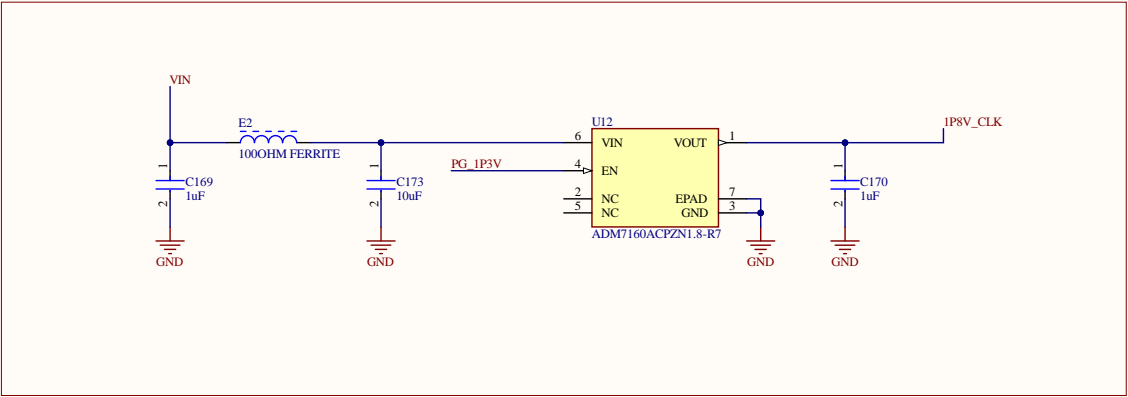
Title		
PlutoV2 Schematic		
Size	Number	Revision
A3		A
Date:	6/06/2022	Sheet of
File:	D:\EET 2022\POWERREG.SchDoc	Drawn By: R.Pender

1.3V SUPPLY

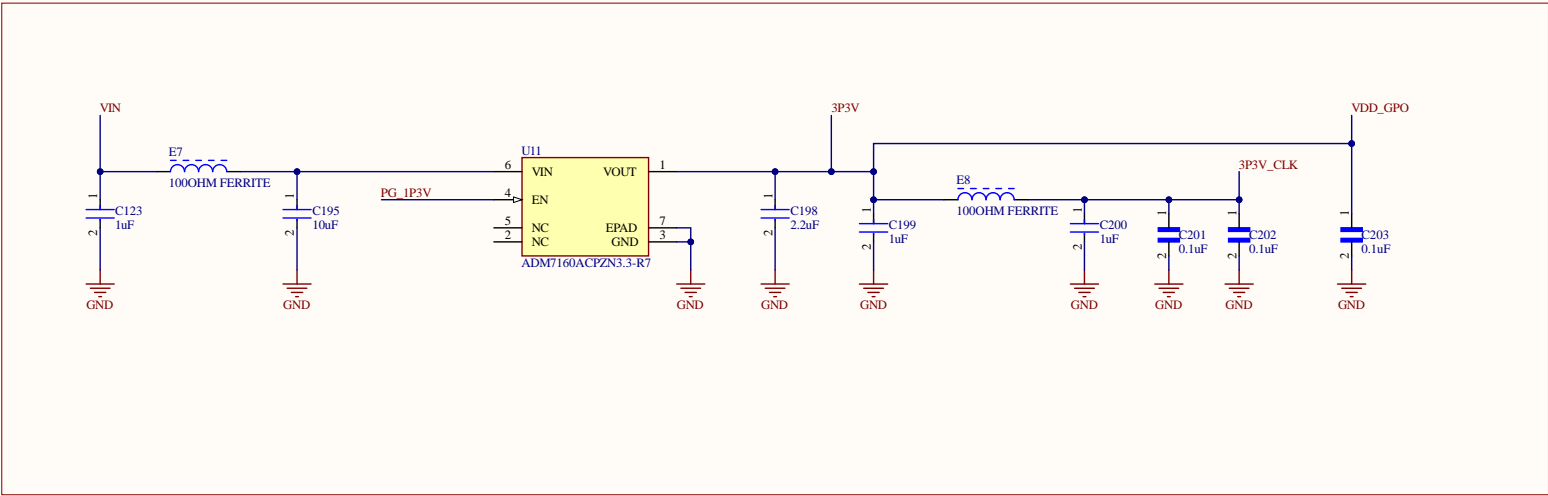


Title		
PlutoV2 Schematic		
Size	Number	Revision
A3		A
Date:	6/06/2022	Sheet of
File:	D:\EET 2022\POWERMANAGE.Sch	Drawn By: R.Pender

CLK 1.8V SUPPLY



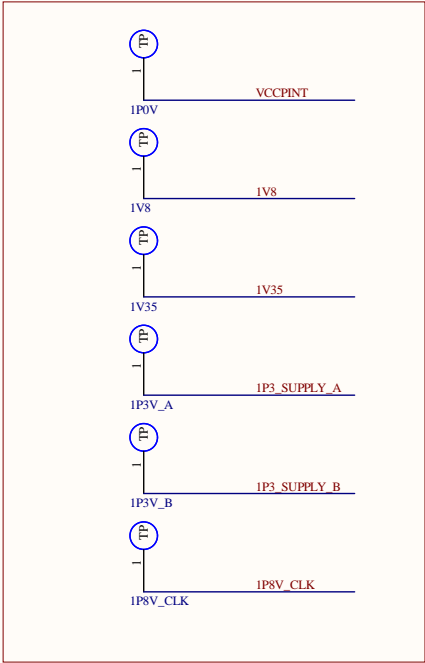
3.3V SUPPLY



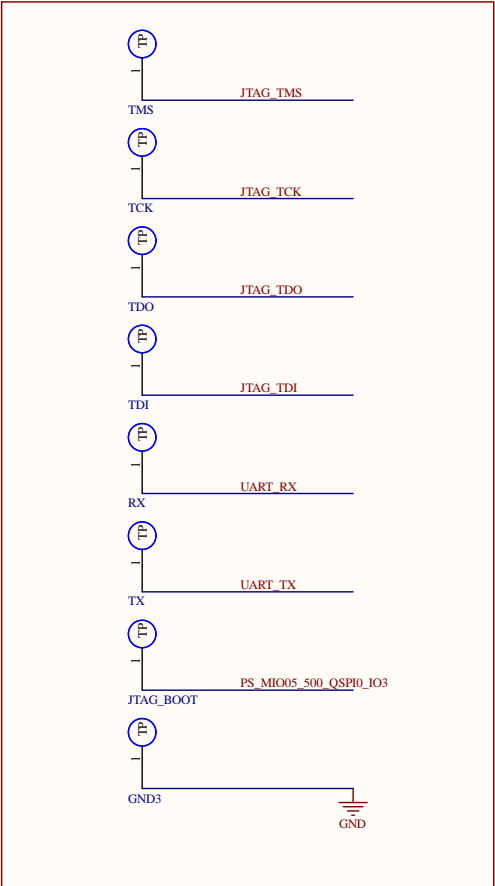
Title		
PlutoV2 Schematic		
Size	Number	Revision
A3		A
Date:	6/06/2022	Sheet of
File:	D:\EET 2022\POWERMANAGE2.Sch	Drawn By: R.Pender

TEST POINTS

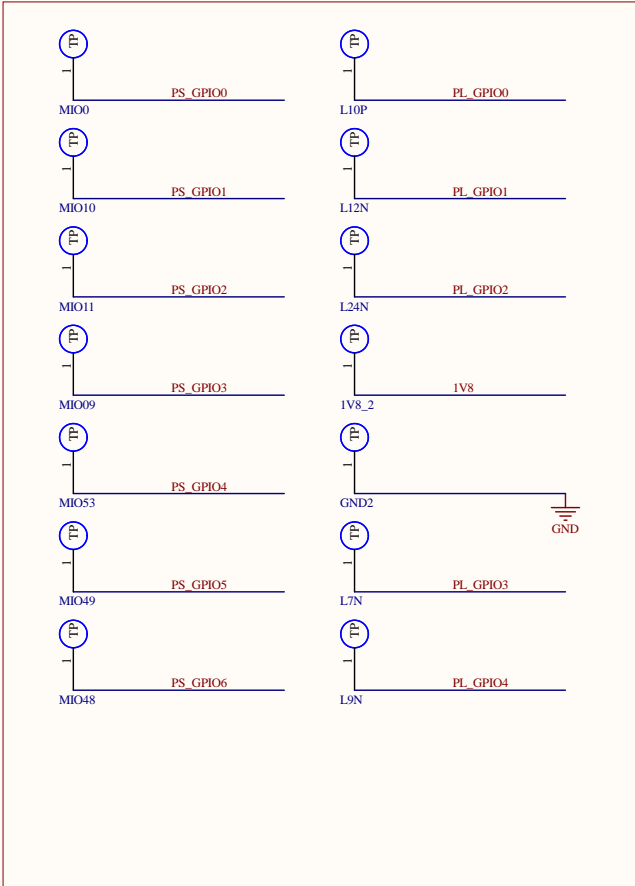
POWER TEST



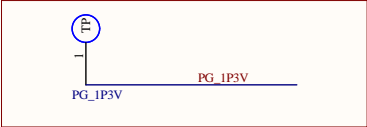
JTAG & UART



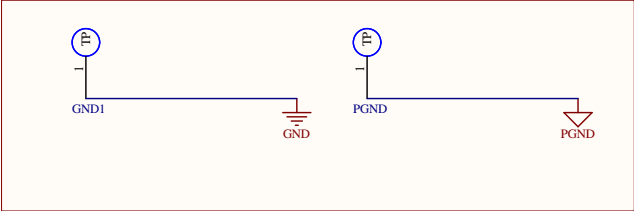
ZYNQ



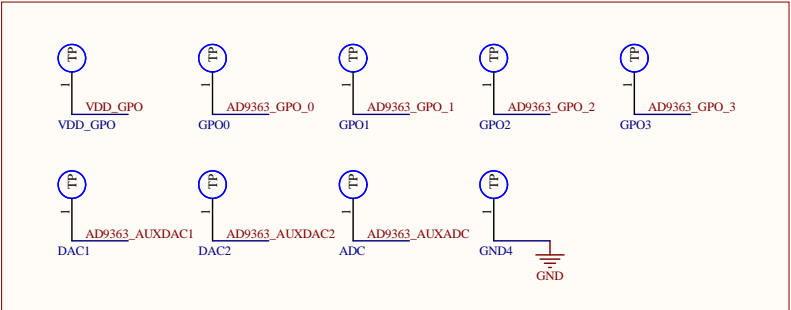
RESET ZYNQ



GROUND

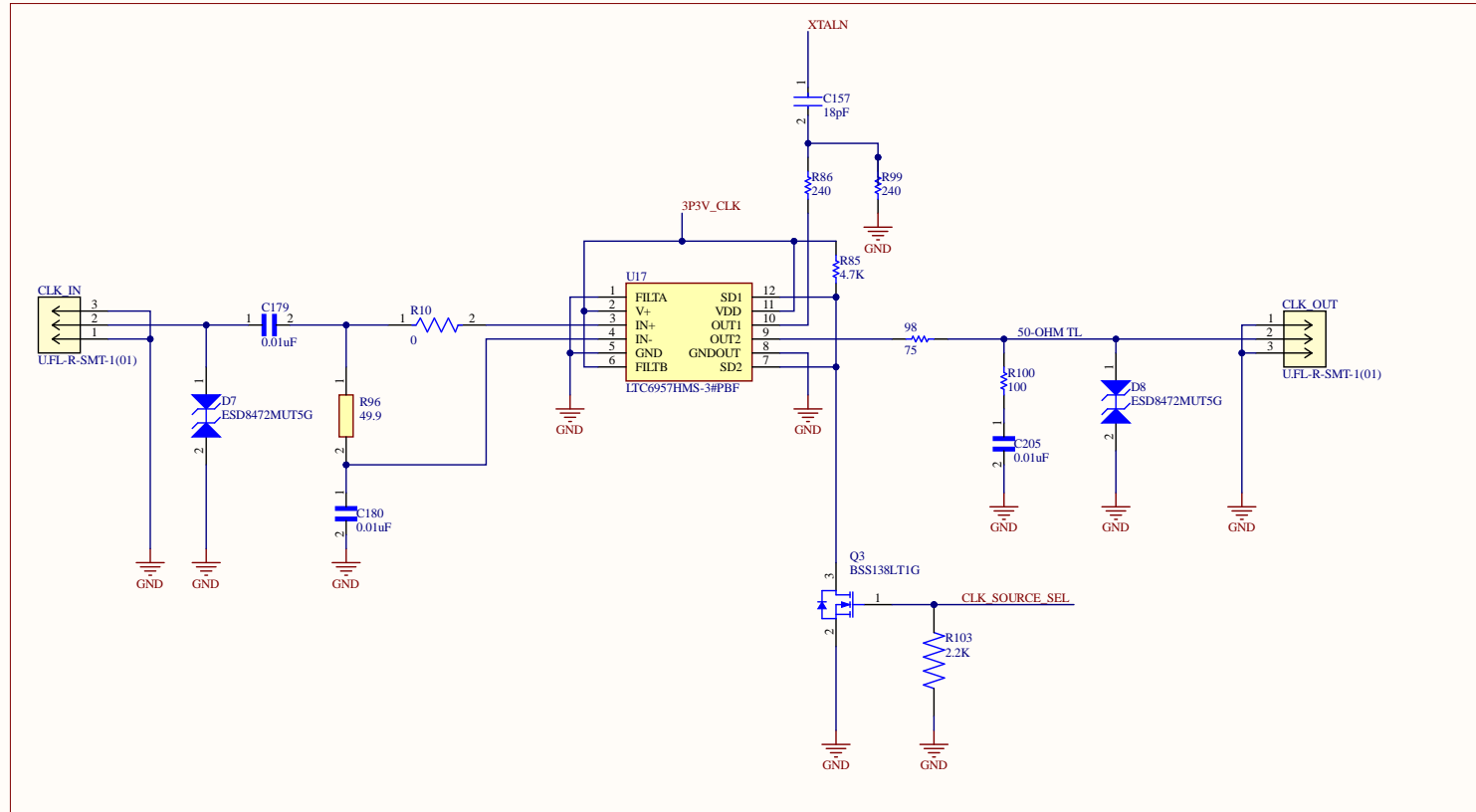


AD9363

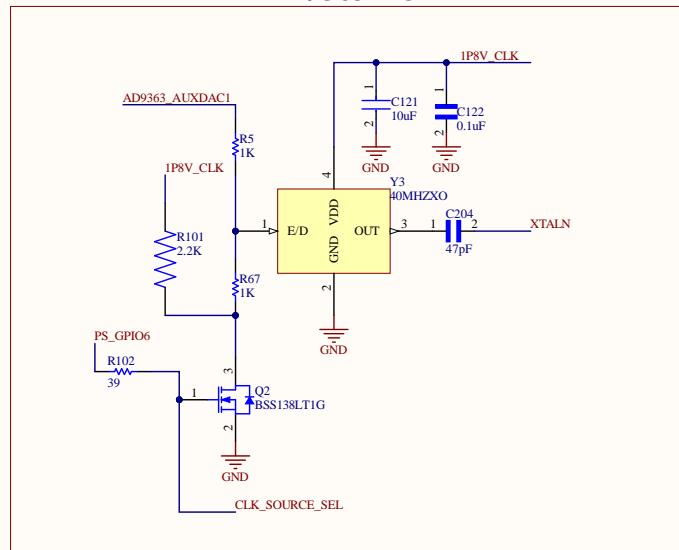


Title			PlutoV2 Schematic	
Size	Number	Revision		
A3		A		
Date:	6/06/2022	Sheet of		
File:	D:\EEET 2022\TESTPOINTS.SchDoc	Drawn By:	R.Pender	

AD9363 EXTERNAL CLK



AD9363 XO



Title		
PlutoV2 Schematic		
Size	Number	Revision
A3		A
Date:	6/06/2022	Sheet of
File:	D:\EET 2022\AD9363 OSCILLATORS	By: R.Pender

Board Stack Report