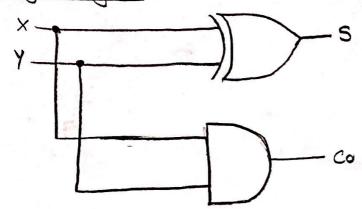
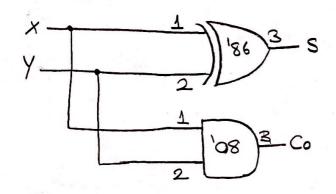
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1) Half Adder. S= A.B+A.B, Co=A.B

* Logic Diagram:



* Circuit Schematiz:



* Piu Numbers for GND and +V

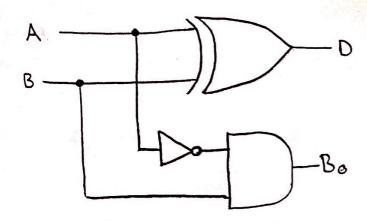
* IC List:

- 1 One 7486 Good 2-jupot XOR Gate
- 2) One 7408 and 2-input AND Gate

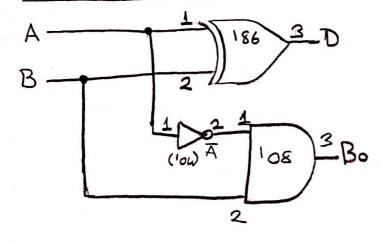
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2) Half Subtractor:

*Logic Diagram:







*Pin Num	abers for	GNO and +V
* 7404	*7486	*7408
F-DND	GND-7	F-OND
154-14	+5V-14	+5V-14

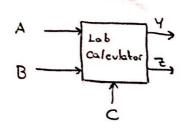
KIC List

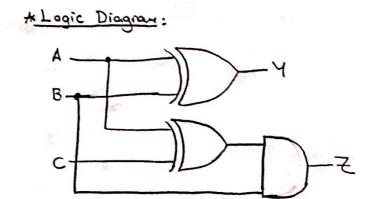
- 1 One 7404 Hex Inventer Cote
- 2) One 7408 Quad 2-input AND Gate
- 3 One 7486 Quad 2-Input XOR Gate

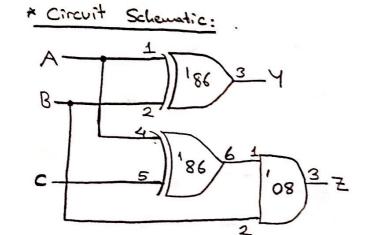
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3 Lab Calculator Y= A·B+A.B , Z= (A·B+A.B).B







* IC List:

- 1 One 7408 Quad 2-input AND Gate
- 2 One 7486 Qued 2-input XOR Gete