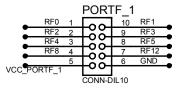


PORTC_1					
RC1 1	\sim	10	RC2	_	
RC3 2	$\simeq \simeq$	9	RC4	-	
RC12 3	\sim	8	RC13		
RC14 4	\sim	7	RC15		
3V3 5	\sim	6	GND		
	5		,	•	
CONN-DIL 10					

PORTD 1					
RD0 1	\Box	10	RD1	_	
RD2 2	$\square \square$	9	RD3	_	
RD4 3	\square	8	RD5	_	
RD6 4	\square	7	RD7	_	
5	$\simeq \simeq$	6	GND	_	
VCC_PORTD_1				•	
CONN-DIL10					

			PORT	ΓE_1		
	RE0	1	\Box	10	RE1	_
	RE2	2	\sim	9	RE3	_
	RE4	3	\sim	8	RE5	_
	RE6	4	$\sim \sim$	7	RE7	_
		5	$\sim \sim$	6	GND	_
VCC	PORTE_1		\Box			_
CONN-DIL10						



	PORTG_1					
_	RG0	1		10	RG1	_
	RG6	2	\square	9	RG7	•
_	RG8	3	\sim	8	RG9	-
_ =	RG12	4	$\sim \sim$	7	RG13	_
		5	\sim	6	GND	-
VCC_P	ORTG_1		\Box		,	•
CONN-DIL10						

	PORT_EXT				
RE8 1	\sim	10	RE9	_	
RF13 2	\sim	9	RG14	-	
RG15 3	$\sim \sim$	8	RA9	-	
RA10 4	$\sim \sim$	7	RA14	-	
3V3 5	$\simeq \simeq$	6	GND	_	
	\sim			•	

	PORTB_2				
RB8 1	\Box	10	RB9	_	
RB10 2	$\simeq \simeq$	9	RB11	Ξ	
RB12 3	\sim	8	RB13	Ξ	
RB14 4	\sim	7	RB15	Ξ	
3V3 5	\sim	6	GND	Ξ	
	00			•	
CONN-DIL10					

