

# The Adventure Begins With PIC<sup>32</sup>



# **BETİ 32-bit PIC Trainer**

## **Student Workbook**

# Yazarlar:

Dr. F. Zeynep KÖKSAL

Ph.D. EEE, ODTÜ/1990

Embedded Systems Expert

Murat AKPINAR

B. Sc. EEE, ODTÜ/2011

Embedded Systems Engineer

#### İsteme Adresi:

Beti Bilişim Teknolojileri İmalat Sanayi İç ve Dış Tic. Ltd Şti.

Şerefli Sokak No:40/5 Mebusevleri / ANKARA

Tel: 0-312 222 18 00

Fax: 0-312 222 18 08

www.elektrovadi.com

#### **Contents**

- 1.1) High-Performance, USB, CAN and Ethernet 32-bit Flash Microcontrollers
- 2.1) The Topology and Technical Features of 32-bit PIC Trainer Set
- 3.1) On-board application modules:
  - 3.1.1) 8xLED's
  - 3.1.2) DAC (Digital-to-Analog-Converter) Module
  - 3.1.3) SHT1x Humidity and Temperature Sensor
  - 3.1.4) 8xTAC Switches
  - 3.1.5) Audio Module
  - 3.1.6) 4-Digit 7-Segment Display
  - 3.1.7) 8-Character LCD Display
  - 3.1.8) DC Motor-Encoder
  - 3.1.9) Digilent Basys2 FPGA Card Interface
- 4.1) Additional Circuitry related to Beti PIC-32 microboard, pull-up's pull-down's and ports used

#### **Beti 32-bit PIC MCU Trainer**

We implemented the PIC32MX795F512L as 32-bit MCU on our microboard. However PIC32MX5XX/6XX/7XX 32-bit MCU's have common technical specifications, that's why we tried to summarize these features. For further reading please use pdf document of Microchip's DS61156G.

# 1.2) High-Performance, USB, CAN and Ethernet 32-bit Flash Microcontrollers

#### **High-Performance 32-bit RISC CPU:**

- MIPS32® M4K® 32-bit core with 5-stage pipeline
- 80 MHz maximum frequency
- 1.56 DMIPS/MHz (Dhrystone 2.1) performance at zero Wait state Flash access
- Single-cycle multiply and high-performance divide unit
- MIPS16e<sup>TM</sup> mode for up to 40% smaller code size
- Two sets of 32 core register files (32-bit) to reduce interrupt latency
- Prefetch Cache module to speed execution from Flash

#### **Microcontroller Features:**

- Operating voltage range of 2.3V to 3.6V
- 64K to 512K Flash memory (plus an additional 12 KB of Boot Flash)
- 16K to 128K SRAM memory
- Pin-compatible with most PIC24/dsPIC® DSC devices
- Multiple power management modes
- Multiple interrupt vectors with individually programmable priority
- Fail-Safe Clock Monitor mode
- Configurable Watchdog Timer with on-chip Low-Power RC oscillator for reliable operation

#### **Peripheral Features:**

- Atomic SET, CLEAR and INVERT operation on select peripheral registers
- Up to 8-channels of hardware DMA with automatic data size detection

- USB 2.0-compliant full-speed device and On-The-Go (OTG) controller:
  - Dedicated DMA channels
- 10/100 Mbps Ethernet MAC with MII and RMII interface:
  - Dedicated DMA channels
- CAN module:
  - 2.0B Active with DeviceNet<sup>™</sup> addressing support
  - Dedicated DMA channels
- 3 MHz to 25 MHz crystal oscillator
- Internal 8 MHz and 32 kHz oscillators
- Six UART modules with:
  - RS-232, RS-485 and LIN 1.2 support
  - IrDA® with on-chip hardware encoder and decoder
- Up to four SPI modules
- Up to five I2C<sup>TM</sup> modules
- Separate PLLs for CPU and USB clocks
- Parallel Master and Slave Port (PMP/PSP) with 8-bit and 16-bit data, and up to 16 address lines
- Hardware Real-Time Clock and Calendar (RTCC)
- Five 16-bit Timers/Counters (two 16-bit pairs combine to create two 32-bit timers)
- Five Capture inputs
- Five Compare/PWM outputs
- Five external interrupt pins
- High-speed I/O pins capable of toggling at up to 80 MHz
- High-current sink/source (18 mA/18 mA) on all I/O pins
- Configurable open-drain output on digital I/O pins

# **Debug Features:**

- Two programming and debugging Interfaces:
  - 2-wire interface with unintrusive access and real-time data exchange with application
  - 4-wire MIPS® standard enhanced Joint Test Action Group (JTAG) interface
- Unintrusive hardware-based instruction trace

• IEEE Standard 1149.2 compatible (JTAG) boundary scan

# **Analog Features:**

- Up to 16-channel, 10-bit Analog-to-Digital Converter:
  - 1 Msps conversion rate
  - Conversion available during Sleep and Idle
- Two Analog Comparators
- 5V tolerant input pins (digital pins only)

					USE	3, Eth	ernet	and CAN									
Device	Pins	Program Memory (KB)	Data Memory (KB)	USB	Ethernet	CAN	Timers/Capture/Compare	DMA Channels (Programmable/ Dedicated)	UART <sup>(2,3)</sup>	Spl <sup>(3)</sup>	I2С™(3)	10-bit 1 Msps ADC (Channels)	Comparators	PMP/PSP	JTAG	Trace	Packages <sup>(4)</sup>
PIC32MX795F512L	100	512 + 12 <sup>(1)</sup>	128	1	1	2	5/5/5	8/8	6	4	5	16	2	Yes	Yes	Yes	PT, PF, BG

**Legend:** PF, PT = TQFP MR = QFN BG = XBGA

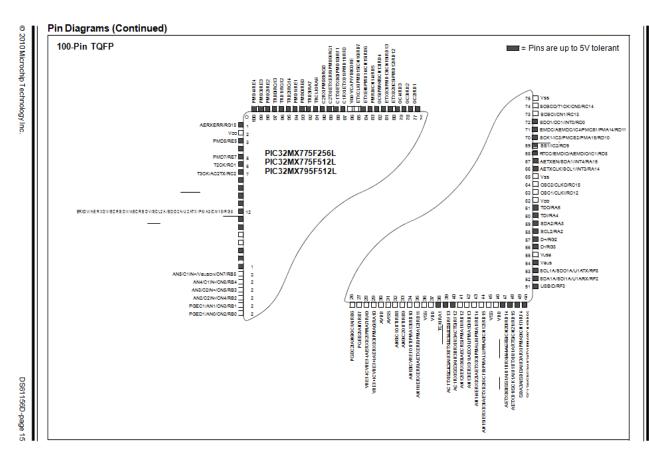


TABLE 1: PIN NAMES: PIC32MX775F256L, PIC32MX775F512L VE PIC32MX795F512L

# TABLE 6: PIN NAME S: PIC32MX775F256L, PIC32MX775F512L AND PIC32MX795F512L DEVICES

٠	IABLE	6: PIN NAMES: PIC32MX775F256L
	Pin Number	Full Ph. Neme
	Ad	PMD4/RE4
	A2	PMD3/RE3
	A3	TRD0/RG13
	A4	PMD0/RE0
	AS	C2RX/PMD8/RG0
	A6	C1TX/ETXD0/PMD10/RF1
	A7	Vbo
	AB	Vas
	AB	ETXD2/ICS/PMD12/RD12
	A10	OC3/RD2
	A11	OC2/RD1
	B1	No Connect (NC)
	82	AERXERR/RG15
	53	PMD2/RE2
	84	PMD1/RE1
	85	TRD3/RA7
	86	C1RX/ETXD1/PMD11/RF0
	87	VoseNbbc ons
	88	PMRD/CN14/RD5
	89	OC4/RD3
	B10	Vaa
	B11	SOSCO/T1CK/CN0/RC14
	C1	PMD6/RE6
	C2	Vbp
	C3	TRD1/RG12
	04	TRD2/RG14
	CS	TRCLK/RAS
	C6	No Connect (NC)
	C7	ETXCLK/PMD15/CN16/RD7
	C8	OCS/PMWR/CN13/RD4
	09	Vbp
	C10	8OSCI/CN1/RC13
	C11	BMDC / A EM D C / IC 4 / P M C 8 1 / PM A1 4 / R D1 1
	D1	T2CK/RC1
	02	PMD7/RE7
	03	PMD5/RE5
	D4	Vaa
	06	Vaa
	06	No Connect (NC)
	07	ETXEN/PMD14/CN15/RD6
	08	ETXD3/PMD13/CN19/RD13
	09	8D01/0C1/INT0/RD0
	D10	No Connect (NC)
	D11	8CK1/IC3/PMC82/PMA15RD10
	E1	TSCK/8DI1/RC4
	E2	T4CK/AC2RX/RC3
	E3	BOOL IBC K 2 A/U 2 B T X/U 2 A R T B/P M A S/C N B/R G 6
	84	T3CK/AC2TX/RC2
	85	Vbo
	86	C2TX/ETXERR/PMD9/RG1
	E7	Vas

C32MX7	32MX775F512L AND PIC32MX795F512L DEVICES					
Pin Number	Full Ph. Name					
88	AETXEN/8DA1/INT4/RA15					
89	RTCC/EMDIO/AEMDIO/IC1/RD8					
E10	881/IC2/RD9					
E11	AETXCLK/8CL1/INT3/RA14					
P1	MOLR					
F2	ERXD V / A E R X D V / E C R 8 D V / A E C R 8 D V / 8 C L 2 A / 8 D O 2 A / U2AT X / PM A 3 / C N 1 D / R G 8					
FB	BRXCLK/AERXCLK/EREFCLK/AEREFCLK/882A/U28RX/ U2ACT8/FM A2/CN11/RG9					
F4	ECR8/8DA2A/8DI2AU2ARX.PMA4/CN9/RG7					
FS	Vas					
F6	No Connect (NC)					
F7	No Connect (NC)					
F8	Vbo					
F9	OBC1/CLKI/RC12					
F10	Vas					
F11	OSC2/CLKO/RC15					
G1	AERXD0/INT1/RE8					
G2	AERXD1/INT2/RE9					
G8	TM8/RA0					
94	No Connect (NC)					
G5	Vbo					
G6	Vas					
G7	Vas					
G8	No Connect (NC)					
G9	TDORAS					
G10	8DA2/RA3					
G11	TDI/RA4					
H1	ANS/C1IN+/Vauson/CN7/RB5					
H2	AN4/C1IN-ICN6/RB4					
H3	Vas					
H4	Vbo					
H5	No Connect (NC)					
H6	Vbo					
H7	No Connect (NC)					
H8	Vaus					
Н9	Vusa					
H10	D+/RG2					
H11	SCL2/RA2					
J1	AN3/C2IN+/CN5/RB3					
42	AN2/C2IN-/CN4/RB2					
13	PGED2/AN7/RB7					
- 14	Albo					
J5	AN11/ERXERR/AETXERR/PMA12/RB11					
J6	TCK/RA1					
47	AN12/ERXD0/AECR8/PMA11/RB12					
J8	No Connect (NC)					
49	No Connect (NC)					
J10	SCL1A/SDO1A/U1ATX/RFS					
J11	D-IRG3					
K1	PGEC1/AN1/CN3/RB1					
K2	PGED1/AN0/CN2/RB0					
1/3	VRSF+/CVRSF+/AERXD3/PMA6/RA10					

# PIC32MX5XX/6XX/7XX

#### TABLE 6: PIN NAMES: PIC32MX775F256L, PIC32MX775F512L AND PIC32MX795F512L DEVICES

Pin Number	Full Fin Name
KA	ANS/C1OUT/RBS
KS	No Connect (NC)
KE	AC1RX/883A/U3BRX/U3ACTS/RF12
K7	AN14/E R X D 2 / A ET X D 3 / PM A L H / P M A 1 / R 8 14
KS	Vbb
K9	AETXD1/8CK1A/U18TX/U1ART8/CN21/RD15
K10	USBID/RF3
K11	SDA1A/SDI1A/U1ARX/RF2
L1	PGEC2/ANS/OCFA/RBS
L2	Vrsr-/CVrsr-/AERXD2/PMA7/RA9

Pin Number	Full Pin Name
L3	Alfas
L4	AN9/C2OUT/RB9
L5	AN10/CVRspout/PMA13/RB10
L6	AC1TX/8CK3A/U3BTX/U3ART8/RF13
L7	AN13/ERXD1/AECOL/PMA10/RB13
L8	AN15/ERXD3/AETX D2/DCF8 /PM ALL/PM AD/CN12/R815
L9	AETXD0/881A/U1BRX/U1ACT8/CN20/RD14
L10	SDA3A/SDI3A/U3ARX/PMA9/CN17/RF4
L11	8CL3A/8DO3A/U3ATX/PMA8/CN18/RF5



Fig.1 Beti 32-bit PIC trainer

# 2.1) The Topology and Technical Features of 32-bit PIC Trainer Set:

PIC32 Development Board is mainly designed to be used with the BETI PIC32 microboard.

Moreover, there are four 6-pin connectors at the bottom left of the PCB. That interface is completely compatible with the **BASYS2 FPGA board of Digilent Inc.** 

In addition to that, any other specific intelligent boards can be easily connected to the development board with the help of that interface.

The development board supports different above mentioned architectures including FPGA and PIC32 MCUs.

#### **Properties of the BETI PIC32 microboard:**

- ✓ Microchip PIC32MX795F512L 32-bit MCU
- ✓ Comes with the **bootloader**
- ✓ In circuit serial programming connector
- ✓ On-Board reset circuit
- ✓ On-Board crystal oscillator
- ✓ Can be used as a **standalone** device
- ✓ Comes with **USB Cable**

The **8 on-board modules** give you chance to build your prototype **by saving** your money and time.

Overview of the on-board application modules:

- $\gt$  8x LEDs,
- > 8x TAC Switches,
- > 2x8 Character LCD,
- > 4-Digit 7-Segment LED Display,
- > DC Motor Encoder,
- > Sht1x Temperature and Humidity Sensor,
- > 12-bit serial DAC module,
- > Audio module.

These on-board modules can be easily examined with the **example codes which** are written in mikroC for PIC32 MCUs and VHDL for FPGAs.

- 3.1) On-board application modules:
- 3.1.1) 8xLEDs

#### **Purpose**

- 1) To demonstrate 32-bit MCU IC; interfacing to an external LED display,
  - 2) To provide an opportunity to write a number of output programs,
- 3) To show the students LED shift-right or shift-left with the help of MCU controlled; as well as various LED on times while shifting.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (1) on-board 8xLED Display module.

The first experiment is 8xLEDs, which are designed by SMD LEDs. By this experiment student are assigned to realize shifting of LED lights.

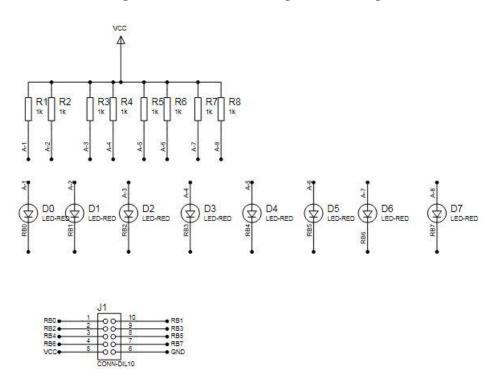


Fig.2 on-board 8xLED Display

#### 3.1.2) DAC (Digital-to-Analog-Converter) Module

#### **Purpose**

- 1) By the help of Digital/Analog converter circuit (DAC) and 32-bit MCU IC; data communication is realized,
- 2) To show the students how to realize coding for Microchip's MCP4921; 12-bit Serial DAC IC and with the help of MCU/DAC interface to realize Digital/Analog conversions,
- 3) With the help of MCU controlled DAC; to develop Waveform Generation; ramp, triangular, square waveform generation.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (1) MCP4921; SPI DAC module,
- (1) Oscilloscope.

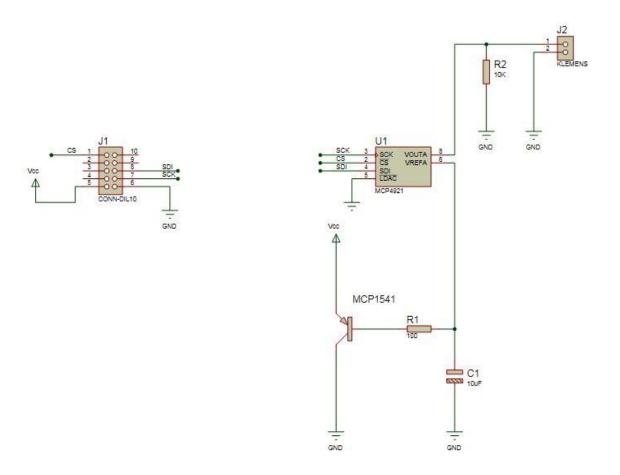


Fig.3 on-board 12-bit serial DAC

#### 3.1.3) SHT1x Humidity and Temperature Sensor

#### **Purpose**

- 1) To know more closely Sensiron company product IC type Humidity and Temperature Sensor ,
- 2) With the help of on-board SHT1x Humidity and Temperature Sensor to demonstrate with 32-bit PIC Trainer how to measure ambient temperature and humidity,
- 3) To show the students how to display on 8-character LCD continuously measured ambient Humidity and Temperature.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (2) SHT1x Humidity and Temperature Sensor.

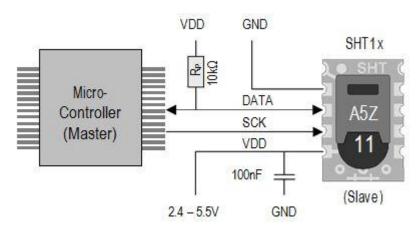


Fig.4 on-board SHT1x Block Diagram

Interfacing of SHT1x to 32-bit PIC Trainer is realized with a similar Serial Interface to I2C which is by interfacing and timing signals very good explained in SHT1x Datasheet pdf, Version 4.0-July 2008.

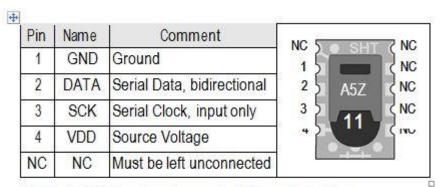


Table 1: SHT1x pin assignment, NC remain floating.

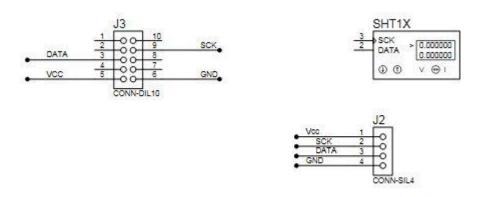


Fig.5 on-board SHT1x Interfacing to 32-bit PIC Trainer

#### 3.1.4) 8xTAC Switches

#### **Purpose**

- 1) To demonstrate 32-bit MCU IC; interfacing to an external input 8xTAC switches,
  - 2) To provide an opportunity to write a number of input programs,
- 3) To show the students writing codes for various input functions; like switch controlled LEDs or switch controlled songs.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (1) on-board 8xTAC Switch module.

The fourth experiment is 8xTAC switches, which are also designed by SMD. By this experiment student are assigned to realize several input functions.

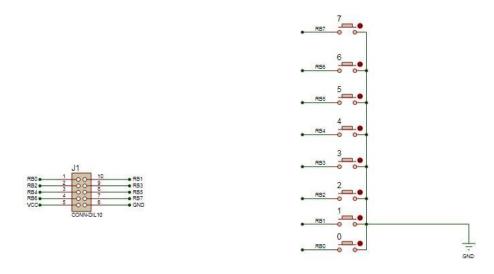


Fig.6 on-board 8x TAC switch Interfacing to 32-bit PIC Trainer

#### 3.1.5) Audio Module

#### **Purpose**

- 1) To know more closely LM386 Audio Amplifier IC,
- 2) With the help of on-board Audio Module to demonstrate with 32-bit PIC Trainer how to create audio music,
- 3) To show the students how to display different audio songs with the help of 8xTAC switches.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (2) Audio Module.

The LM386 is an audio amplifier designed for use in low voltage consumer applications. The gain is internally set to 20 to keep external part count low, but the addition of an external resistor and capacitor between pins 1 and 8 will increase the gain to any value from 20 to 200. We used gain 20, and a very special speaker 8 Ohm, 300Hz~20 kHz, with the dimesions 15.00mm L x 11.00mm W; produced by Knowles Company; with Digikey Part No: 423-1171-ND.

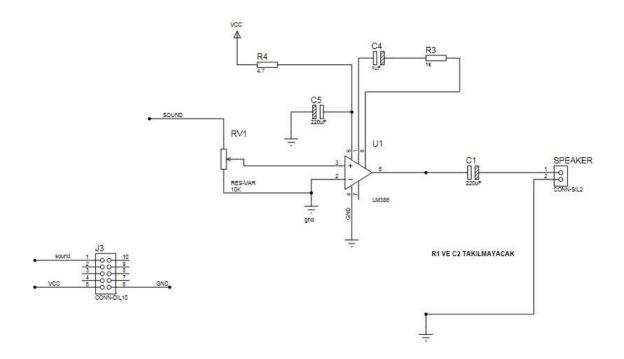


Fig.7 on-board Audio Module interfaced to the BETİ 32-bit PIC Trainer

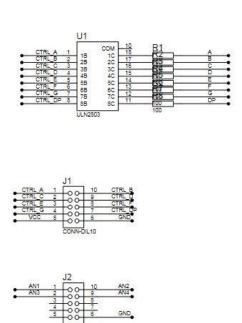
# 3.1.6) 4-Digit 7-Segment Display

#### **Purpose**

- 1) To show the students how to multiplex 4-digit 7-segment LED Display to the 32-bit PIC Trainer ,
- 2) To show how different refresh rates effect the display, demonstrate the effect of using different display on times.

# **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (2) 4-digit 7-Segment Display.



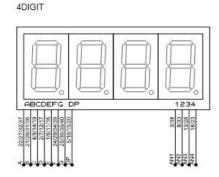


Fig.8 on-board 4-Digit 7-Segment LED Display module interfaced to the BETİ 32-bit PIC Trainer

# 3.1.7) 8-character LCD Display

#### **Purpose**

- 1) To show the students how to multiplex 4-digit 7-segment LED Display to the 32-bit PIC Trainer ,
- 2) To show how different refresh rates effect the display, demonstrate the effect of using different display on times.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (2) 8-Character LCD Display.

Fig.9 on-board 8-Character LCD Display module interfaced to the BETİ 32-bit PIC Trainer

#### 3.1.8) DC Motor-Encoder

#### **Purpose**

- 1) To exhibit the construction of a DC Motor speed control circuit and measure motor speed using a 32-bit PIC MCU controlled optical-interrupter circuit ,
- 2) To show the calculation of motor rpm from motor speed data provided by the Beti 32-bit PIC MCU Trainer.

## **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (2) DC Motor-Encoder.

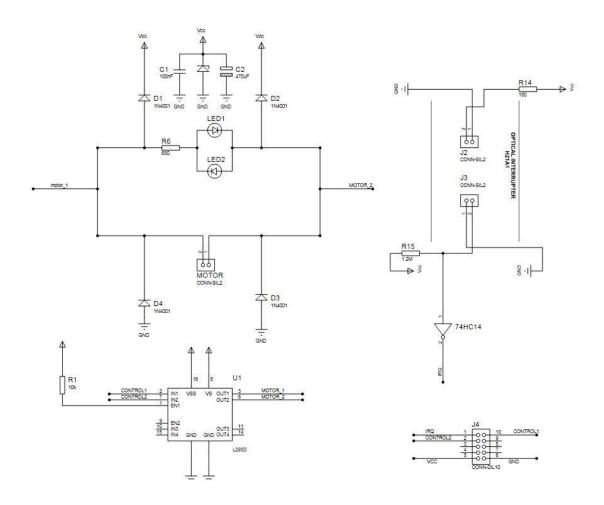


Fig.10 on-board DC Motor-Encoder Module interfaced to the BETİ 32-bit PIC Trainer

# 3.1.9) Digilent Basys2 FPGA Card Interface

# **Purpose**

- 1) To realize the communication with the Basys2 FPGA card and 32-bit PIC MCU ,
- 2) To show the students how FPGA card I/O can be interfaced to the Beti 32-bit PIC MCU and vice versa.

#### **Materials Required**

- (1) Beti 32-bit PIC Trainer,
- (2) Digilent Basys2 FPGA Card Interface.

There are four 6-pin connectors at the bottom left of the PCB. That interface is completely compatible with the **BASYS2 FPGA board of Digilent Inc.** 

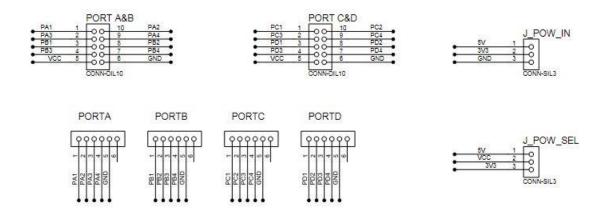


Fig.11 on-board Digilent Basys2 FPGA Card Module interfaced to the BETİ 32-bit PIC Trainer

# 4.1) Additional Circuitry related to Beti PIC-32 microboard, pull-up's and ports used:

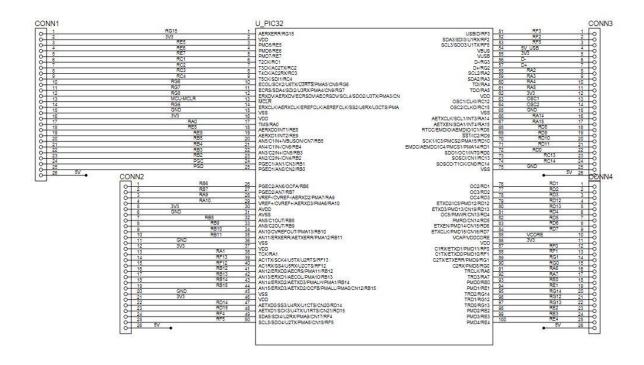


Fig.12 microboard of the BETİ 32-bit PIC Trainer

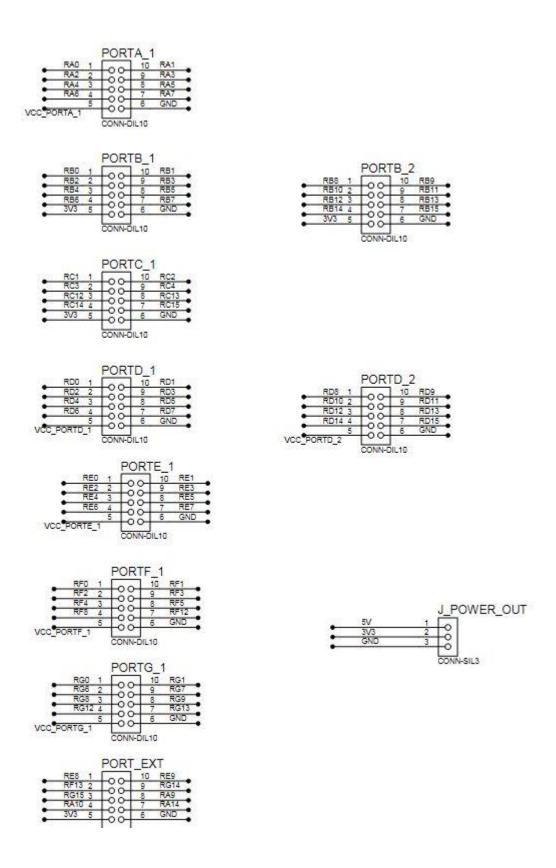


Fig.13 Port's used in the BETİ 32-bit PIC Trainer

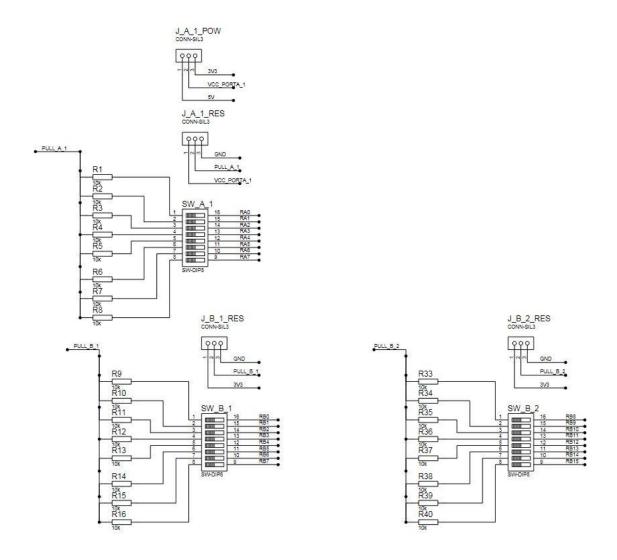


Fig.14 Port pull-up's&pull-down's used in the motherboard of the BETİ 32-bit PIC Trainer





www.elektrovadi.com