

```

entry:
  %a = alloca i32, align 4
  %_shadow_a = alloca i32, align 4
  %b = alloca i32, align 4
  %_shadow_b = alloca i32, align 4
  call void @llvm.dbg.declare(metadata i32* %a, metadata !11, metadata !12),
  ... !dbg !13
  store i32 10, i32* %_shadow_a, align 4
  store i32 10, i32* %a, align 4, !dbg !14
  call void @llvm.dbg.declare(metadata i32* %b, metadata !15, metadata !12),
  ... !dbg !16
  store i32 20, i32* %_shadow_b, align 4
  store i32 20, i32* %b, align 4, !dbg !16
  %0 = load i32, i32* %b, align 4, !dbg !17
  %cmp = icmp eq i32 %0, 20, !dbg !19
  br i1 %cmp, label %if.then, label %if.else, !dbg !20

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if.then:

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%1 = load i32, i32* %a, align 4, !dbg !21
%add = add nsw i32 %1, 1, !dbg !23
store i32 %add, i32* %a, align 4, !dbg !24
br label %if.end, !dbg !25

```

if.else:

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%2 = load i32, i32* %a, align 4, !dbg !26
%add1 = add nsw i32 %2, 2, !dbg !28
store i32 %add1, i32* %a, align 4, !dbg !29
br label %if.end

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if.end:

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%3 = load i32, i32* %a, align 4, !dbg !30
%call = call i32 @_Z9passParamiPi(i32 %3, i32* %a), !dbg !31
%4 = load i32, i32* %a, align 4, !dbg !32
ret i32 %4, !dbg !33

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CFG for '_Z10dependencyv' function