```
entry:
 %aParam.addr = alloca i32, align 4
 %aPointer.addr = alloca i32*, align 8
 store i32 %aParam, i32* %aParam.addr, align 4
 call void @llvm.dbg.declare(metadata i32* %aParam.addr, metadata !12,
... metadata !13), !dbg !14
 store i32* %aPointer, i32** %aPointer.addr, align 8
 call void @llvm.dbg.declare(metadata i32** %aPointer.addr, metadata !15,
... metadata !13), !dbg !16
 %0 = load i32, i32* %aParam.addr, align 4, !dbg !17
 %add = add nsw i32 %0, 1, !dbg !18
 store i32 %add, i32* %aParam.addr, align 4, !dbg !19
 %1 = load i32*, i32** %aPointer.addr, align 8, !dbg !20
 %2 = load i32, i32* %1, align 4, !dbg !21
 %add1 = add nsw i32 %2, 1, !dbg !22
 %3 = load i32*, i32** %aPointer.addr, align 8, !dbg !23
 store i32 %add1, i32* %3, align 4, !dbg !24
 %4 = load i32, i32* %aParam.addr, align 4, !dbg !25
 ret i32 %4, !dbg !26
```

CFG for '_Z9passParamiPi' function