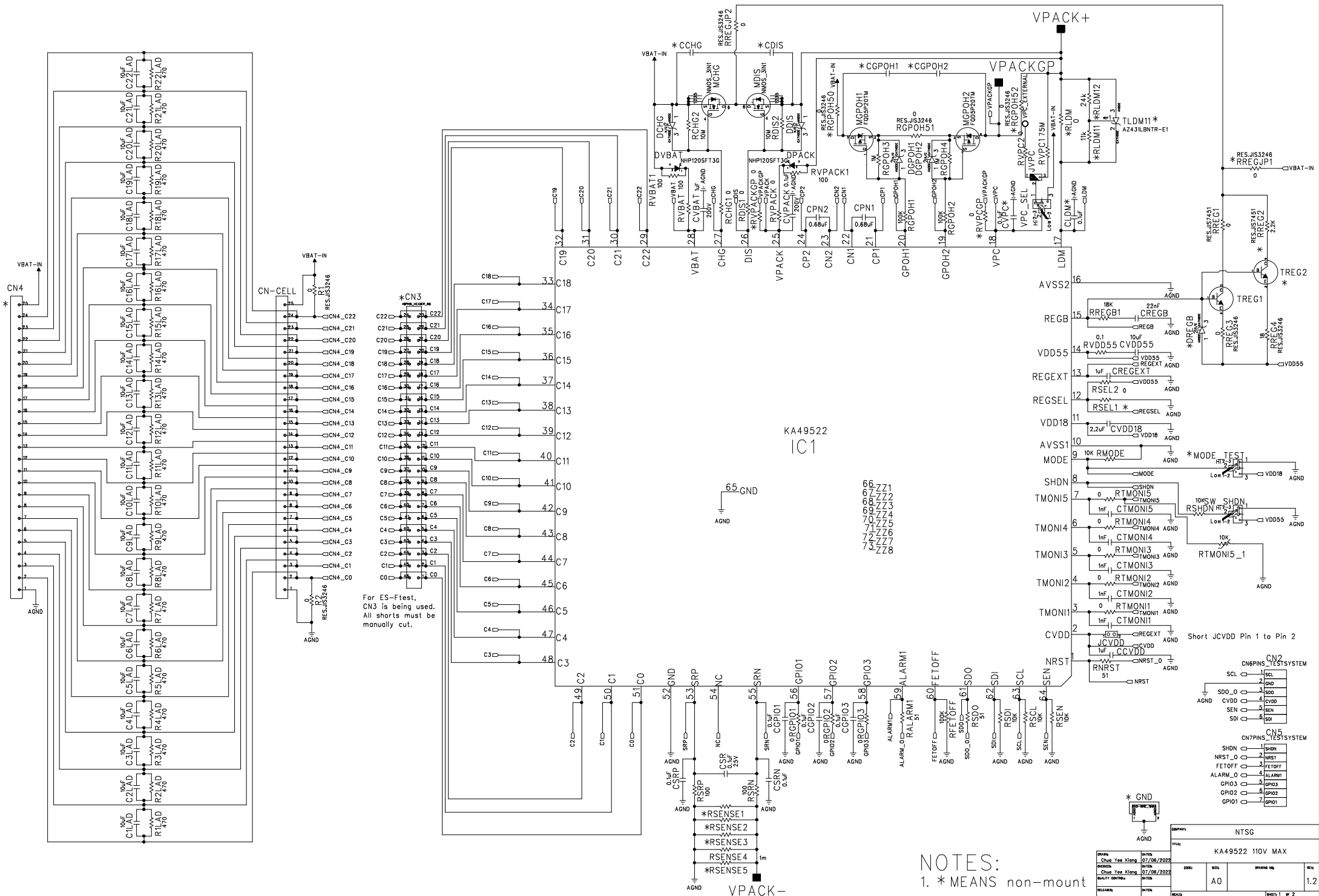
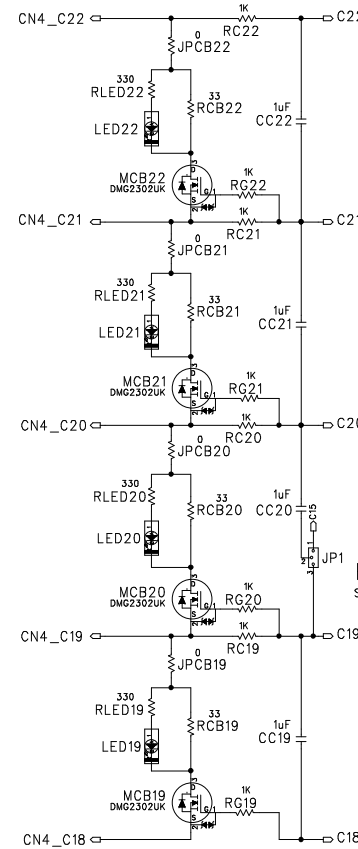
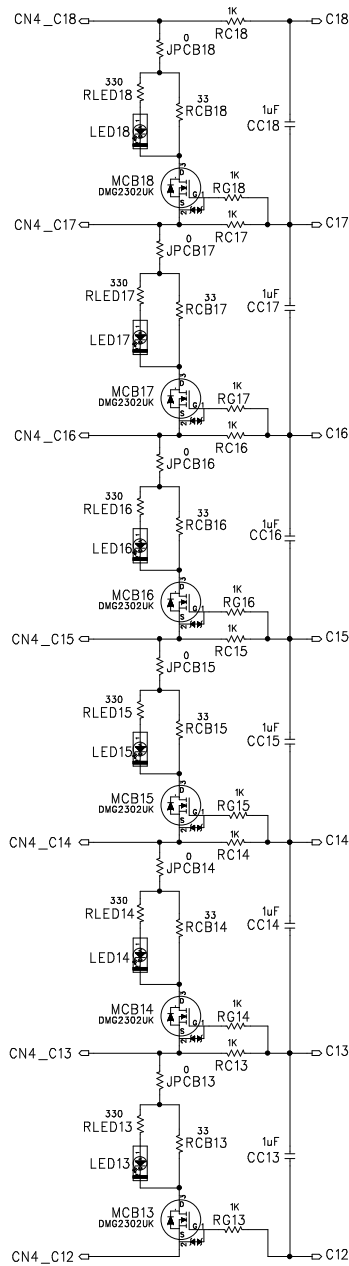
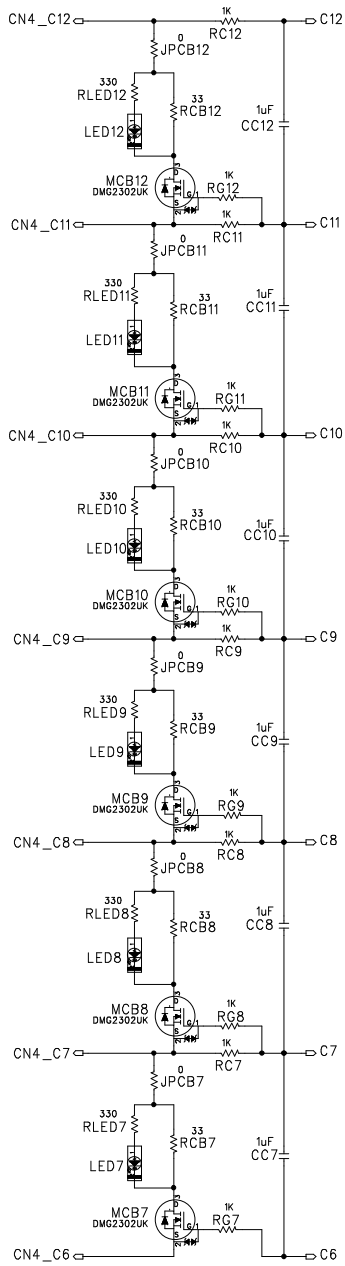
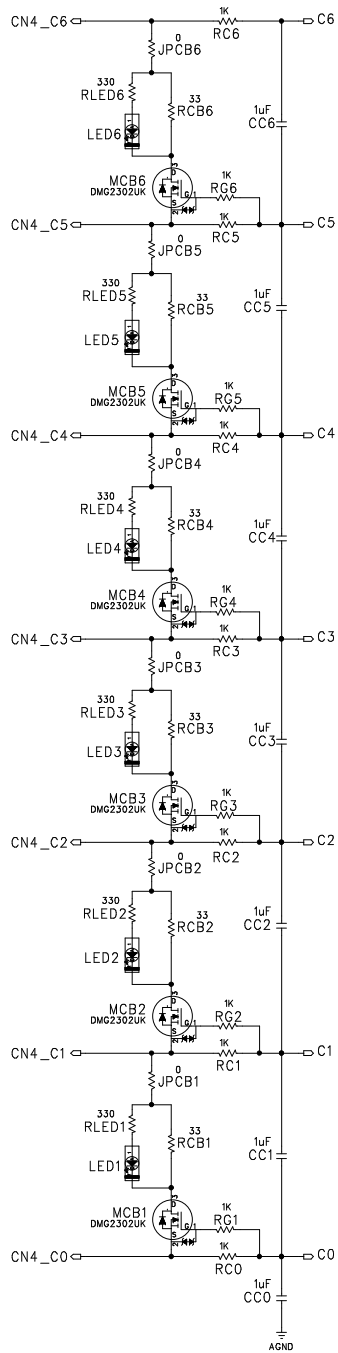


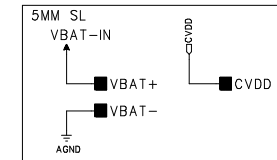
KA49522 {110V MAX} EVALUATION BOARD [Ver 1.21]



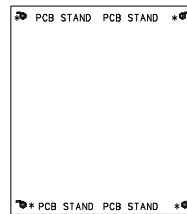
KA49522 {110V MAX} EVALUATION BOARD [Ver 1.21]



CERAMIC ADAPTOR INTERFACE PINS			
CN1-D CN-64PINS			
C2	4.9	C2	4.9
C1	5.0	C1	5.0
C0	5.1	C0	5.1
AGND	5.2	AGND	5.2
SRP	5.3	SRP	5.3
NC	5.4	NC	5.4
SRN	5.5	SRN	5.5
GPIO1	5.6	GPIO1	5.6
GPIO2	5.7	GPIO2	5.7
GPIO3	5.8	GPIO3	5.8
ALARM1	5.9	ALARM1	5.9
FETOFF	6.0	FETOFF	6.0
SDO	6.1	SDO	6.1
SDI	6.2	SDI	6.2
SCL	6.3	SCL	6.3
SEN	6.4	SEN	6.4
CN1-C CN-64PINS			
C18	3.3	C18	3.3
C17	3.4	C17	3.4
C16	3.5	C16	3.5
C15	3.6	C15	3.6
C14	3.7	C14	3.7
C13	3.8	C13	3.8
C12	3.9	C12	3.9
C11	4.0	C11	4.0
C10	4.1	C10	4.1
C9	4.2	C9	4.2
C8	4.3	C8	4.3
C7	4.4	C7	4.4
C6	4.5	C6	4.5
C5	4.6	C5	4.6
C4	4.7	C4	4.7
C3	4.8	C3	4.8
CN1-B CN-64PINS			
LDM	17	LDM	17
VPC	18	VPC	18
GPOH2	19	GPOH2	19
GPOH1	20	GPOH1	20
CP1	21	CP1	21
CP2	22	CP2	22
CP3	23	CP3	23
CP4	24	CP4	24
VPACK	25	VPACK	25
DIS	26	DIS	26
CHG	27	CHG	27
VBAT	28	VBAT	28
C22	29	C22	29
C21	30	C21	30
C20	31	C20	31
C19	32	C19	32
CN1-A CN-64PINS			
NRST	1	NRST	1
CVDD	2	CVDD	2
TMON1	3	TMON1	3
TMON2	4	TMON2	4
TMON3	5	TMON3	5
TMON4	6	TMON4	6
TMON5	7	TMON5	7
SHDN	8	SHDN	8
MODE	9	MODE	9
VSYS1	10	VSYS1	10
VDD18	11	VDD18	11
REGSEL	12	REGSEL	12
REGEXT	13	REGEXT	13
VDD55	14	VDD55	14
REGB	15	REGB	15
VSYS2	16	VSYS2	16



For 522
Short Pin2 to Pin3 for JP1



COMPANY			
NTSG			
TITLE			
KA49522 110V MAX			
DESIGNER	DATE	COMP	REV
Chao Yee Kiong	07/06/2022		
CHIEF	DATE	DESIGNED BY	REV
Chao Yee Kiong	07/06/2022		
QUALITY CONTROL	DATE		
RELEASED	DATE	SOURCE	SHEET 2 OF 2