





A B C D E

В		C		D		E	_
		Description (TOP)	Description (BOT)	Pin			
	1	GND	3V3	2			
	3	RESETn	3V3	4			
	5	AUX0 (BOOT0)	VBAT	6			1
	7	JTAG_TDI	AUX1 (PA1)	8			ľ
	9	JTAG_TDI  JTAG TDO TRACESWO	GND	10			
	11	JTAG_TMS_SWDIO	USB DN	12			
	13	JTAG TCK SWCLK	USB DP	14			
	15	GND	GND	16			
	10	CARD NOTCH	CARD NOTCH	10			
	17	GND	GND	18			
	19	SPI1_SCK (PA5)	I2C1_SCL (PB6)	20			
	21	SPI1_MISO (PA6)	I2C1_SDA (PB7)	22			2
	23	SPI1_MOSI (PA7)	I2C2_SDA (PB11)	24			
	25	GND	I2C2 SCL (PB10)	26			
	27	SPI2_SCK (PB13)	GND	28			
	29	SPI2_MISO (PB14)	UART1_MCUTX (PA9)	30			
	31	SPI2_MOSI (PB15)	UART1_MCURX (PA10)	32			
	33	GND	UART2_MCUTX (PA2)	34			
	35	GPIO0 (PA0)	UART2_MCURX (PA3)	36			
	37	GPIO1 (PC0)	GND	38			3
	39	GPIO2 (PC1)	GPIO13 (PC12)	40			
	41	GPIO3 (PC2)	GPIO12 (PC11)	42			
	43	GPIO4 (PC3)	GPIO11 (PC10)	44			
	45	GPIO5 (PC4)	PIO10 (PC9)	46			-
	47	GPIO6 (PC5)	GPIO9 (PC8)	48			
	49	GPIO7 (PD2)	GPIO8 (PC7)	50			
	51	GND	GND	52			
							4
			IntelliModule Alpha (Brd	. Rev. B)	Date: 03.11.2019		$\dashv$
			Technical Documentation Drawings	and Assembly	Time: 14:33	Johannes Berndorfer	
			Document Scale / Size: 1: Units: Millimeters	1 / A4	Revision: Sheet 4 of 4	Konrad Grefe Gasse 22 1230 Wien AUSTRIA	
			PCB-File: IntelliModule-Al				
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