

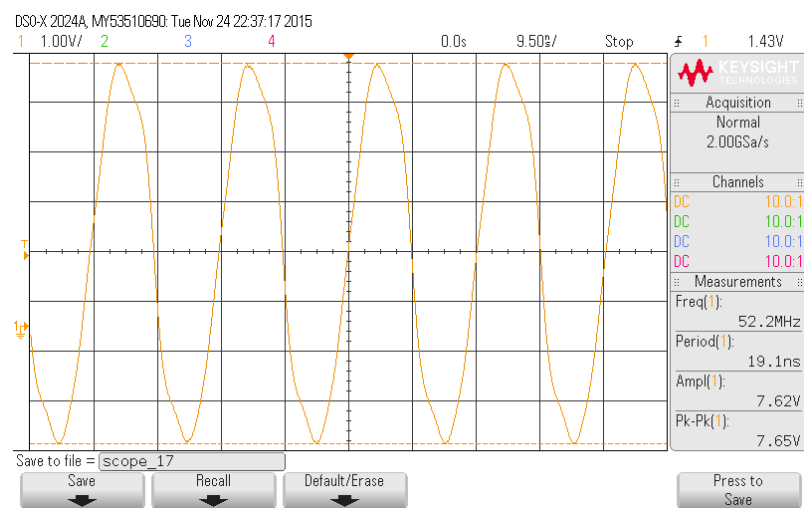
Pars board

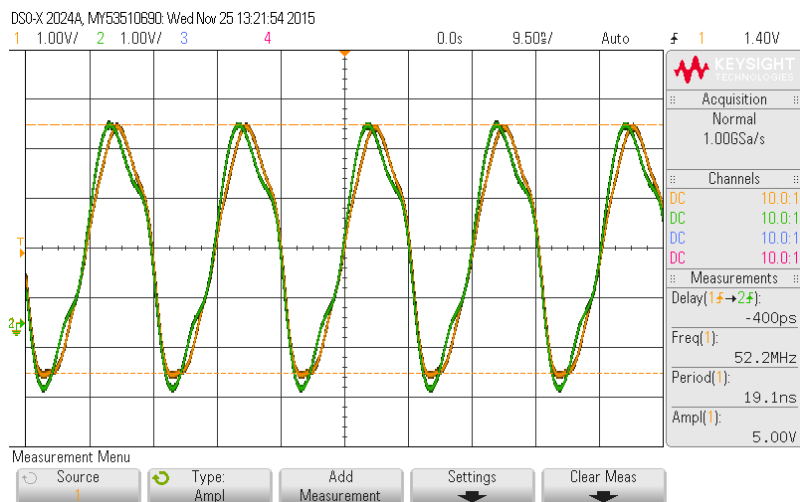
- eMMC P1
 - Reset CLK
 - Reset MMC
 - CLK
 - CMD
 - DATA 0
 - DATA 1
 - DATA 2
 - DATA_3
 - DATA 4
 - DATA 5
 - DATA 6
 - DATA 7
 - POWER

eMMC P1

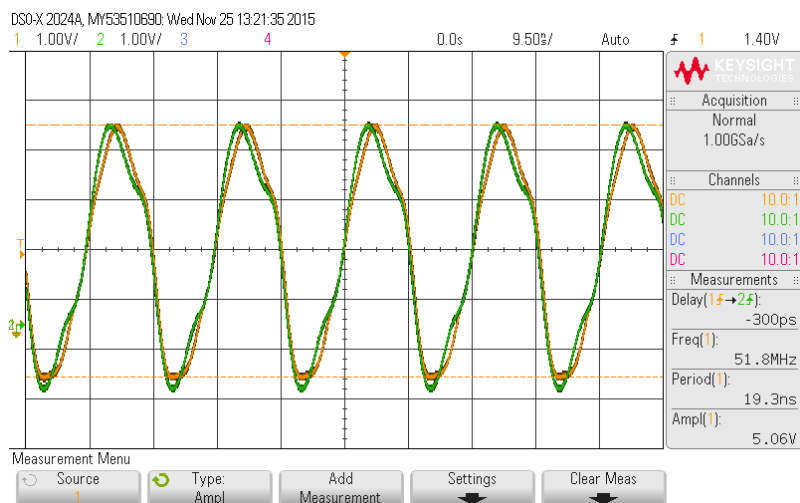
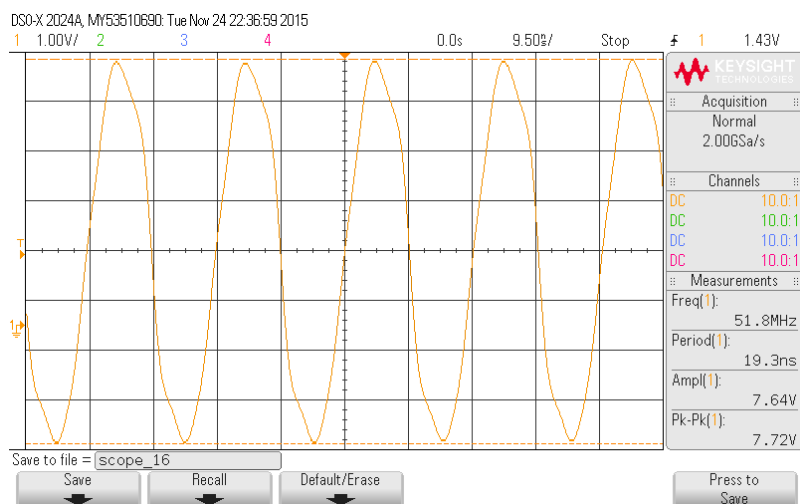
Green signal is taken from PEC connector's pin

Reset CLK

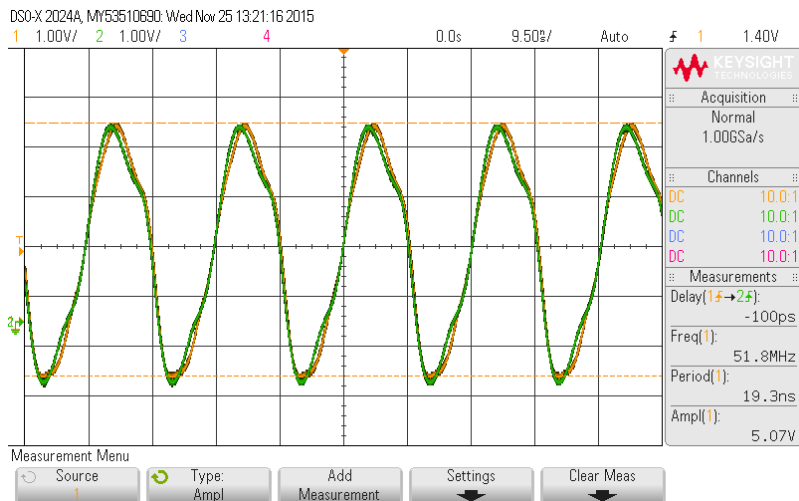
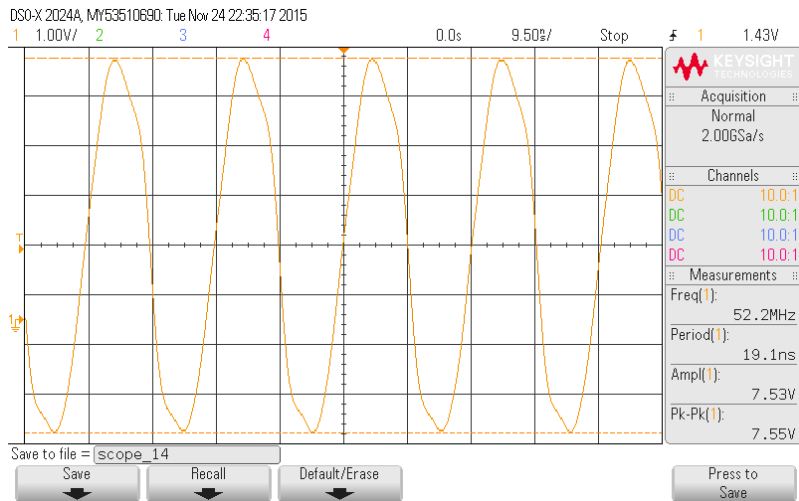




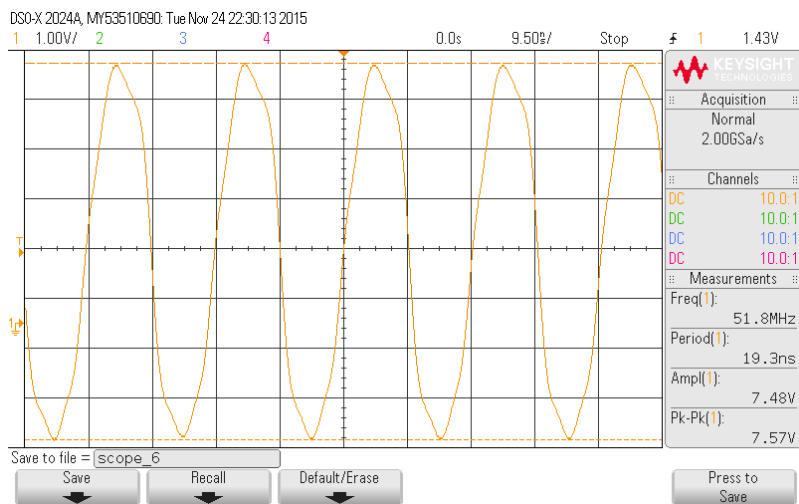
Reset MMC

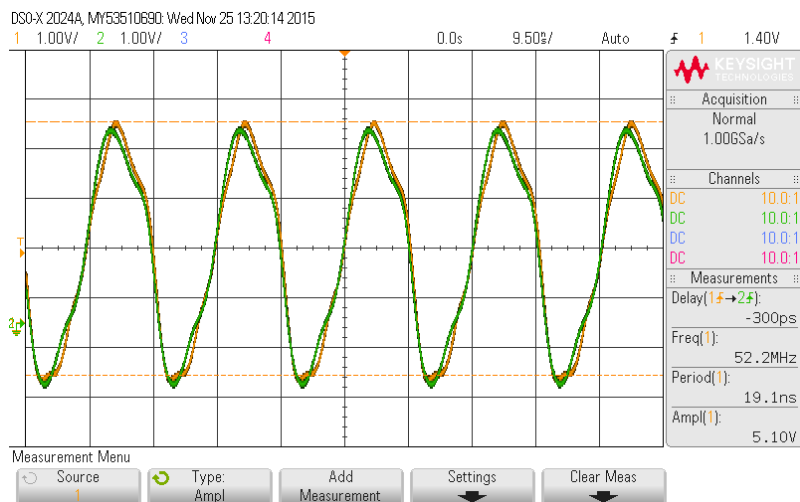


CLK

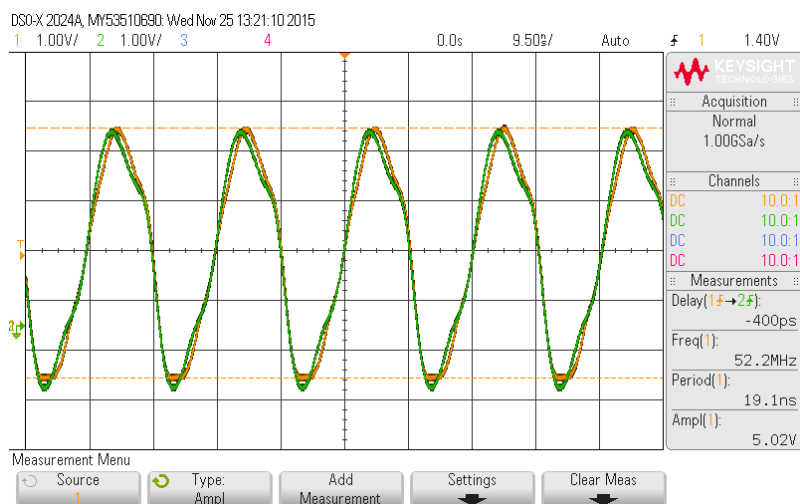
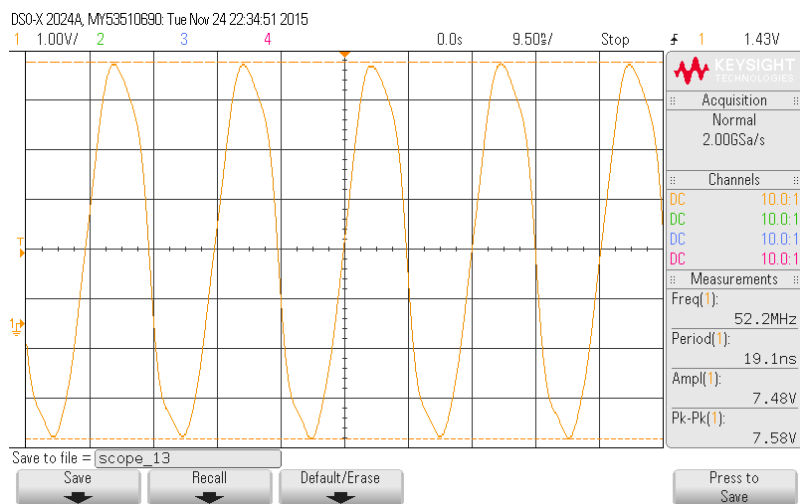


CMD

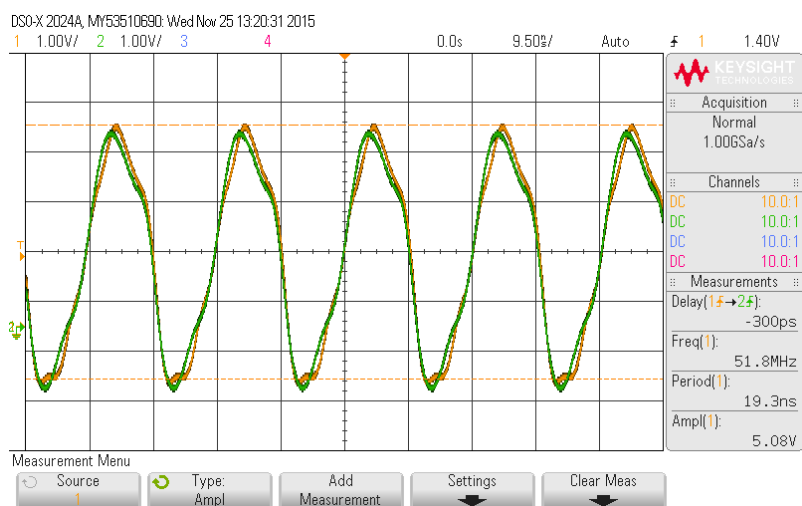
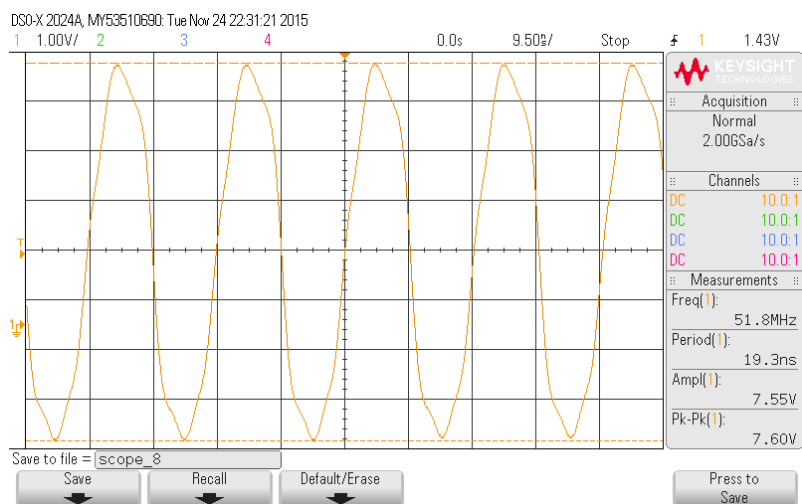




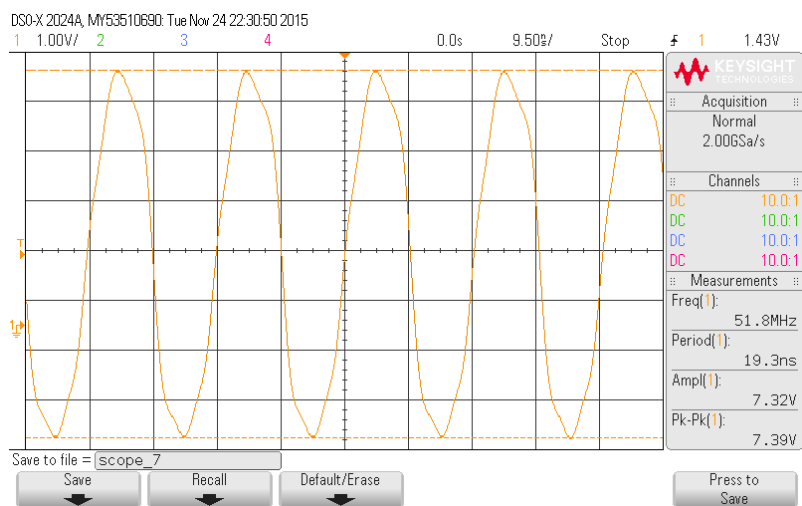
DATA 0

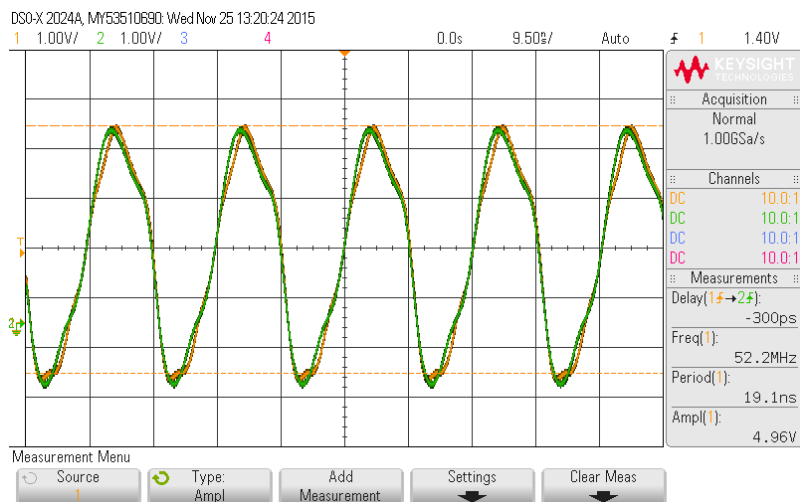


DATA 1

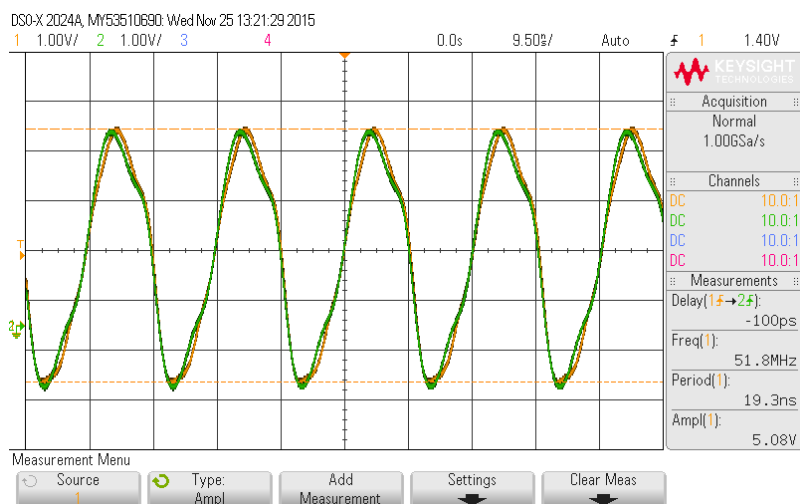
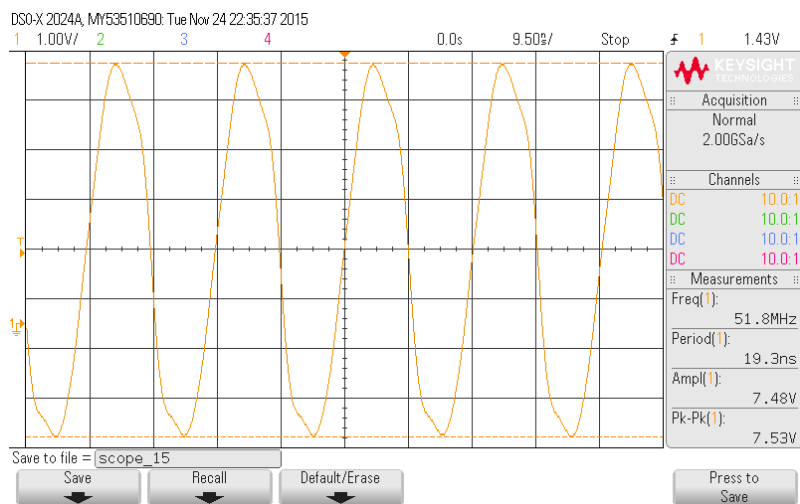


DATA 2

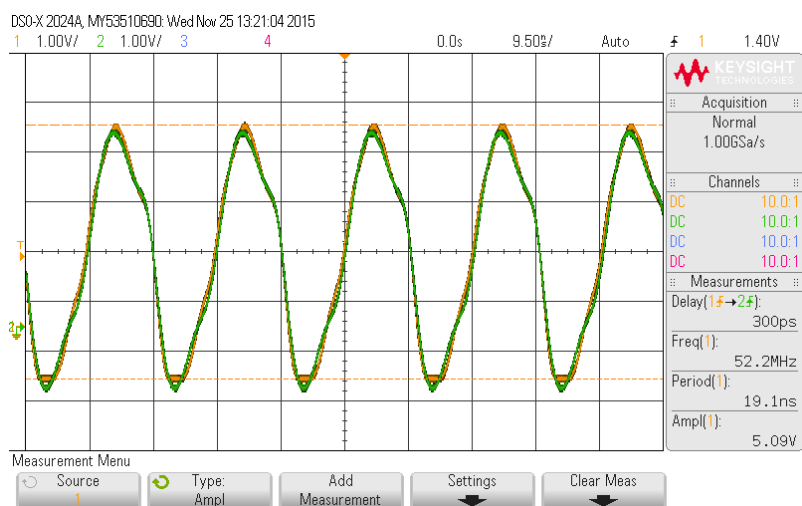
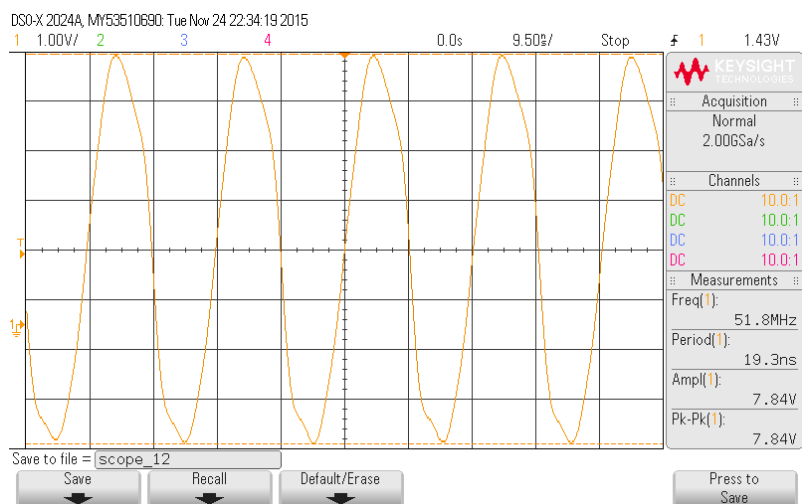




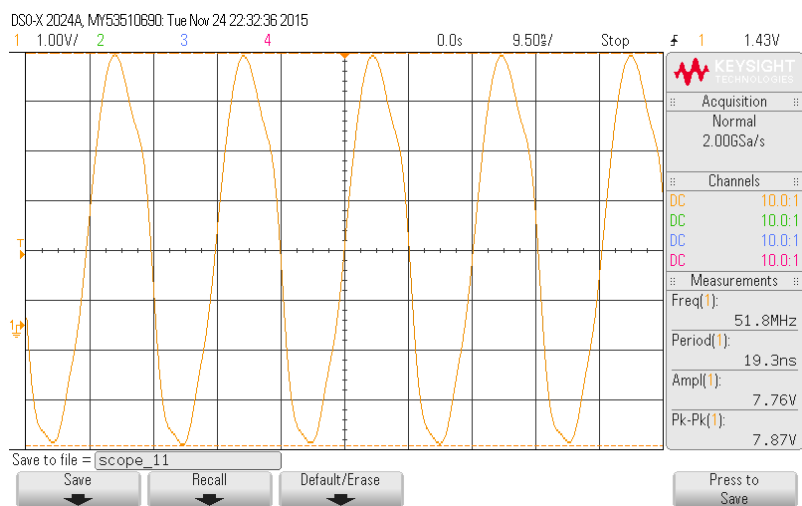
DATA_3

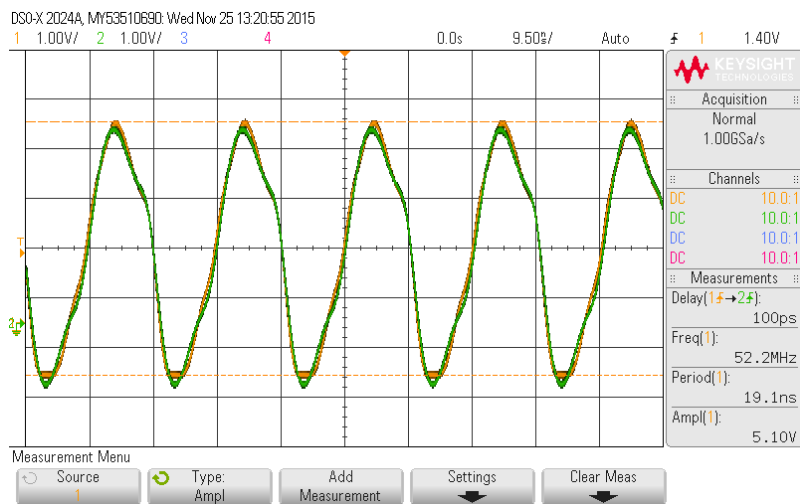


DATA 4

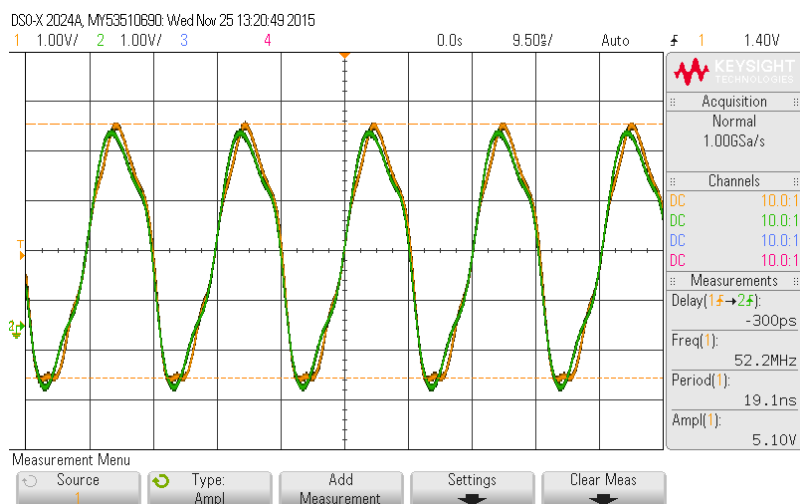
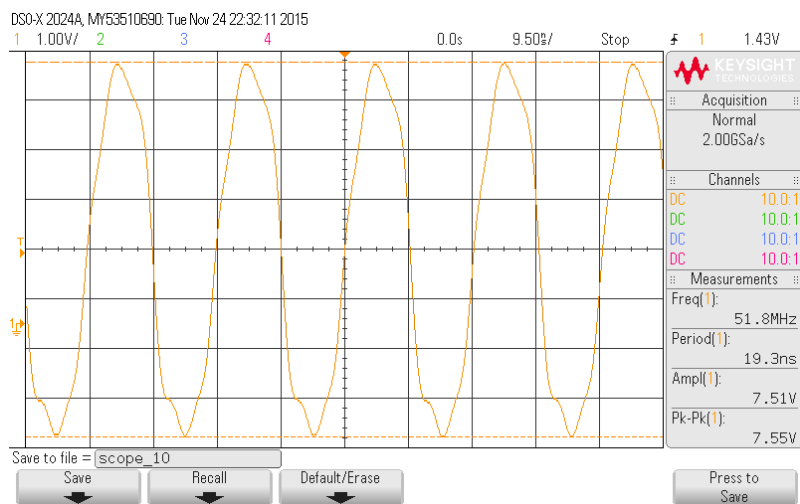


DATA 5

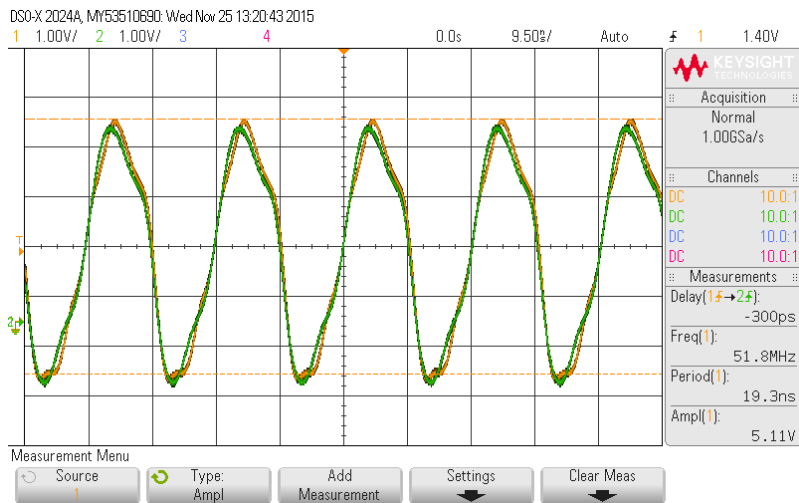
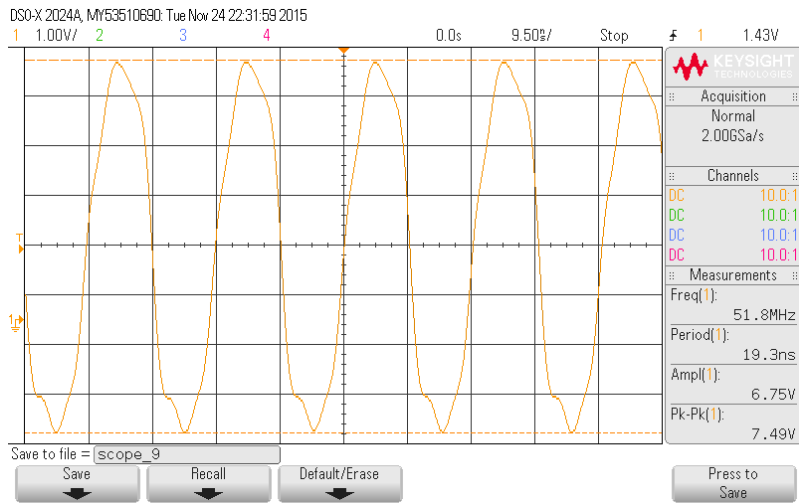




DATA 6



DATA 7



POWER

Green signal is taken from VDDGPIO and yellow from eMMC connector power pin

