

1095**Code : 15CS32T***Register
Number*

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III Semester Diploma Examination, April/May-2019**COMPUTER ORGANIZATION****Time : 3 Hours]****[Max. Marks : 100**

- Instructions :** (i) Answer any **six** questions from PART – A. Each carries **5** marks.
 $5 \times 6 = 30$
- (ii) Answer any **seven** questions from PART – B. Each carries **10** marks.
 $10 \times 7 = 70$

PART – A

1. Mention the Basic functional units of a computer with its purpose. 5
2. Explain the basic memory operations. 5
3. List types of instructions. Explain any one. 5
4. Explain the concept of microprogrammed control unit. 5
5. Explain the different phases for instruction execution. 5
6. Explain how a complete instruction is executed. 5
7. Write a note on Interrupts. 5
8. Explain the significance of cache memory. 5
9. Explain super scalar processor. 5

PART – B

10. List the types of addressing modes. Explain Immediate and Indirect addressing mode. 10
 11. Explain any five assembler directives with its purpose. 10
 12. Explain single bus organization with diagram. 10
 13. Explain how control signals are issued using Hard Wired control unit. 10
 14. Explain DMA controller with a suitable diagram. 10
 15. (a) Differentiate between synchronous and asynchronous bus. 5
(b) Mention the functions performed by I/o Interface. 5
 16. Explain with example parallel port connectivity. 10
 17. Explain static and dynamic memory system. 10
 18. Explain types of ROM. 10
 19. Explain VLIW architecture. 10
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