

Time: 3 Hours]

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| Max. Marks : 100

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III Semester Diploma Examination, Nov./Dec. 2017

COMPUTER ORGANISATION

Note	e: (i) (ii)	Answer any six questions from Section – A. Each question carries 5 in Answer any seven full questions from Section – B. Each question marks.	
		SECTION - A	
Ans	wer any s	ix questions. Each question carries 5 marks.	$5 \times 6 = 30$
1.	Describe	e the role of MAR, MDR, PC and IR.	5 Diploma - [All Branches]
2.	Describe	e the Big-endian and Little-endian addressability.	5
3.	Explain	the basic memory operation.	Diploma Question Papers [2015- 19] Beta Console Education
4.	Explain	how a complete instruction is executed.	5
5.	Write a	note on Hard Wired Control Unit.	5
6.	Explain	the memory mapped I/O concept.	5
7.	Explain	how to enable and disable an interrupt.	5
8.	Illustrate	e how to implement a static RAM memory cell.	5
9.	Write a	note on Flash memories. 1 of 2	5 Turn over

$\boldsymbol{SECTION-B}$

10. Illustrate with example direct addressing mode, indirect addressing mode. 11. (a) Explain different assembler directives. (b) Describe fetching a word from memory. 12. With block diagram explain complete processor. 13. With block diagram explain serial port interface. 14. Describe the working of DMA. 15. Explain the internal organisation of memory chips. 16. Explain the use of memory controller with diagram. 17. Write a note on: (a) Rambus Memory (b) Memory hierarchy 18. With neat diagram explain multicore Architecture. 19. (a) Compare super scalar versus VLIW. (b) Explain Non-linear pipeline processor. 5 10. 10. 11. In Display of the scalar versus VLIW. 5 (c) Explain Non-linear pipeline processor. 5 18. September 10. 19. (a) Compare super scalar versus VLIW. 5 (b) Explain Non-linear pipeline processor. 10.		Answer any seven full questions. Each question carries 10 marks.	$10\times7=70$
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