Code : 15CS32T

Register Number | | | | | | | | | | |

III Semester Diploma Examination, Oct./Nov.-2019

COMPUTER ORGANIZATION

Time : 3 Hours] **[Max. Marks : 100**

Instructions : (i) Answer any **six** questions from Part – **A**, each question carries **5** marks.
(ii) Answer any **seven** full questions from Part – **B**, each question carries **10** marks.

PART – A

(Answer any **six** questions, each caries **5** marks) **6 × 5 = 30**

1. Explain the basic functional units of a computer. **5**

2. Describe register and absolute addressing mode. **5**

3. Describe the Big-endion and Little-endion addressability. **5**

4. Explain different faces of instruction execution. **5**

5. Explain the concept of micro programmed control unit. **5**

6. Explain bus arbitration logic. **5**

7. Explain the configuration of ROM cell. **5**

8. Summarise the concept of flash memory. **5**

9. Compare CISC verses RISC. **5**

[Turn over

PART – B

(Answer any **seven** questions, each carries **10** marks) $7 \times 10 = 7($

10. Explain different assembler directives. **10**

11. Illustrate with examples indirect addressing. **10**

12. With block diagram, explain complete processor. **10**

13. Describe the working of DMA. **10**

14. Illustrate with example Interrupt Service Routine (ISR). **10**

15. Describe types of ROM. **10**

16. Explain the operation of asynchronous DRAM. **10**

17. Explain the internal organization of memory chips. **10**

18. Explain VLSI architecture. **10**

19. With neat diagram explain multicore architecture. **10**