

Time: 3 Hours 1

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## III Semester Diploma Examination, April/May-2017

## **COMPUTER ORGANISATION**

Tin	me: 3 Hours ] auditasqui Ou ballonness ml	Max. Marks: 100
Not	te: (i) Answer any $\mathbf{six}$ full questions from Part – $\mathbf{A}$ . Each question	ons carries 5 marks.
	(ii) Answer any seven full questions from Part – B. Each marks.	questions carries 10
	gram explain emples procesors	ob Analdribyy E!
	PART – A	BETA CONSOLE
1.	Explain the basic functional Unit of a Computer.	Diploma - [All Branches  Beta Console Education  B
2.	Describe the Big-endian and Little-endian addressability.	Sielens Oustin Dress 19915
3.	Describe register and absolute addressing mode.	Diploma Question Papers [2015- 19]  Beta Console Educates  **But Console Educates**  **But Conso
4.	Write a note on hardwired control unit.	Lorizona Tis
5.	Write a note on vectored interrupts.	14 (4. (auly 23 - 21 <b>5</b>
6.	Illustrate how to implement a static RAM memory cell.	21) Explana (15

Explain super scalar processor. 9.

7.

8.

Explain the configuration of ROM cell.

Explain the significance of cache memory.

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## PART - B

10.	Describe following addressing modes with examples:	10
	(a) Immediate	
	(b) Autoincrement	
	(c) Indirect	
11.	Describe program controlled I/O operation.	10
12.	Explain single bus organization.	10
13.	With block diagram explain complete processor.	10
14.	(a) Write a note on DMA.	TA COSSOLE
	(b) Explain how to enable and disable an interrupt.	Diploma - [All Branches]
15.	Explain Serial Port Interface.	10
16.	Explain static and dynamic memory system.	Diploma Question Papers [2015-19] 10  Beta Console Education
17.	Describe the features of PROM, EPROM and EEPROM.	10
18.	Explain VLIW Architecture.	10
19.	Explain CISC Scalar and RISC Scalar Processor.	10