1224

Code : 15CS-32T

Register Number | | | | | | | |

III Semester Diploma Examination, April/May-2017

COMPUTER ORGANISATION

Time : 3 Hours] **[Max. Marks : 100**

Note : (i) Answer any **six** full questions from Part – **A**. Each questions carries **5** marks.

(ii) Answer any **seven** full questions from Part – **B**. Each questions carries **10** marks.

PART – A

1. Explain the basic functional Unit of a Computer. **5**

2. Describe the Big-endian and Little-endian addressability. **5**

3. Describe register and absolute addressing mode. **5**

4. Write a note on hardwired control unit. **5**

5. Write a note on vectored interrupts. **5**

6. Illustrate how to implement a static RAM memory cell. **5**

7. Explain the configuration of ROM cell. **5**

8. Explain the significance of cache memory. **5**

9. Explain super scalar processor. **5**

[Turn over

PART – B

10. Describe following addressing modes with examples : 10

    (a) Immediate

    (b) Autoincrement

    (c) Indirect

11. Describe program controlled I/O operation. 10

12. Explain single bus organization. 10

13. With block diagram explain complete processor. 10

14. (a) Write a note on DMA. 5

    (b) Explain how to enable and disable an interrupt. 5

15. Explain Serial Port Interface. 10

16. Explain static and dynamic memory system. 10

17. Describe the features of PROM, EPROM and EEPROM. 10

18. Explain VLIW Architecture. 10

19. Explain CISC Scalar and RISC Scalar Processor. 10
