

1154**Code : 15CS32T**

Register Number						
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III Semester Diploma Examination, Nov./Dec. 2017**COMPUTER ORGANISATION****Time : 3 Hours]****[Max. Marks : 100**

- Note :** (i) Answer any **six** questions from Section – A. Each question carries **5** marks.
(ii) Answer any **seven** full questions from Section – B. Each question carries **10** marks.

SECTION – AAnswer any **six** questions. Each question carries **5** marks.

$$5 \times 6 = 30$$

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1. Describe the role of MAR, MDR, PC and IR.
2. Describe the Big-endian and Little-endian addressability.
3. Explain the basic memory operation.
4. Explain how a complete instruction is executed.
5. Write a note on Hard Wired Control Unit.
6. Explain the memory mapped I/O concept.
7. Explain how to enable and disable an interrupt.
8. Illustrate how to implement a static RAM memory cell.
9. Write a note on Flash memories.



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SECTION – BAnswer any **seven** full questions. Each question carries **10** marks.**10 × 7 = 70**

10. Illustrate with example direct addressing mode, indirect addressing mode. **10**
11. (a) Explain different assembler directives. **5**
(b) Describe fetching a word from memory. **5**
12. With block diagram explain complete processor. **10**
13. With block diagram explain serial port interface. **10**
14. Describe the working of DMA. **10**
15. Explain the internal organisation of memory chips. **10**
16. Explain the use of memory controller with diagram. **10**
17. Write a note on :
(a) Rambus Memory **5**
(b) Memory hierarchy **5**
18. With neat diagram explain multicore Architecture. **10**
19. (a) Compare super scalar versus VLIW. **5**
(b) Explain Non-linear pipeline processor. **5**

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