1217

Code : 15CS32T

Register Number | | | | | | | | | | |

III Semester Diploma Examination, Nov./Dec.-2018

COMPUTER ORGANIZATION

Time : 3 Hours] [Max. Marks : 100

Instructions : (1) Answer any **six** questions from Part – **A**. Each carries **5** marks.

(2) Answer any **seven** full questions from Part – **B**. Each carries **10** marks.

PART – A

1. Explain he basic functional unit of a computer.

2. Describe the Big-endian and Little-endian addressability.

3. Explain the basic memory operation.

4. Explain the different phases for instruction execution.

5. Write a note on interrupts.

6. Illustrate how to implement a static RAM memory cell.

7. Write a note on flash memory.

8. Explain the configuration of ROM cell.

9. Compare CISC with RISC.

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PART – B

10. Explain with examples one-address, two-address and three-address instruction types.

11. (a) Describe register and absolute addressing mode. 5

    (b) Write a note on conditional codes. 5

12. Explain single bus organization.

13. With block diagram, explain complete processor.

14. Describe the working of DMA.

15. Explain with example parallel port connectivity.

16. Describe various types of ROM.

17. Illustrate with diagram memory hierarchy with respect to speed, size and cost.

18. Explain VLIW Architecture.

19. (a) Explain super scalar processor. 5

    (b) List the advantages of multi core architecture. 5