1456

Code : 15CS32T

Register Number | | | | | | | |

III Semester Diploma Examination, Nov./Dec. 2016

COMPUTER ORGANISATION

Time : 3 Hours] **[Max. Marks : 100**

Note : (i) Answer any **six** questions from Part – **A**. Each question carries **5** marks.

(ii) Answer any **seven** questions from Part – **B**. Each question carries **10** marks.

PART – A

1. Describe the basic functional unit of computer. **5**

2. Explain the Big-Endian and Little-Endian addressability. **5**

3. Explain the basic memory operation. **5**

4. Write a note on Register transfer. **5**

5. Write a note on interrupts. **5**

6. Illustrate how to implement a static RAM memory cell. **5**

7. Explain the configuration of ROM cell. **5**

8. Write a note on RAM bus memory. **5**

9. Compare CISC Verses RISC. **5**

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PART – B

10. Explain with example one-address, two-address and three – address instruction types. **10**

11. Illustrate with example the following addressing modes : **10**
    (i) Direct
    (ii) Indirect
    (iii) Immediate

12. Explain single bus organization. **10**

13. With block diagram explain complete processor. **10**

14. (a) Explain bus arbitration logic. **5**
    (b) List the activities of I/O interface. **5**

15. Explain hardware components for connecting a keyboard to a processor with block diagram. **10**

16. Describe types of ROM. **10**

17. Illustrate with diagram memory hierarchy with respect to speed, size and cost. **10**

18. Explain VLIW Architecture. **10**

19. Describe arithmetic, instruction and processor pipelining. **10**