

Code	:	15E	C427	
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Register									
Number					7, 11			, ,	

IV Semester Diploma Examination, Oct./Nov.-2019

MICROCONTROLLERS AND APPLICATIONS

Tim	ne: 3 Hours] [Max. Marks: 1	100
Inst	ructions: (i) Answer any six questions from Part – A. $(5 \times 6 = 30 \text{ marks})$	
	(ii) Answer any seven full questions from Part – B. $(7 \times 10 = 70 \text{ marks})$	
	PART – A	
1.	List the features of 8051 microcontroller.	5
2.	Classify the instruction according to its functions and give an example for each.	5
 4. 	Explain the following instructions: (i) NOP (ii) DAA (iii) ORL (iv) ADDA, #55H (v) RETI Write the C statements to do the following tasks: (i) To mask the d³ and d⁵ bits of P ₀ . (ii) To toggle the d ⁷ and d³ bits of P ₂	5
5.	Explain the bit structure of IP register.	5
6.	Explain the TCON register.	5
7.	Explain the significance of SBUF register.	5
8.	Sketch the schematic for interfacing DC motor to 8051.	5
9.	With the help of schematic diagram, explain triangular wave generation using DAC. 1 of 2 [Turn or	5 ver

PART – B

10.	Exp	ain the architecture of 8051 microcontroller.	16
11.	(a)	Explain PSW register of 8051 μC.	5
	(b)	List the applications of microcontrollers.	5
12.	Writ	te an ALP to find smallest to 'n' 8 bit numbers.	10
13.	(a)	Write an ALP to send values 0 to 4 to port P ₂ .	5
	(b)	Write an ALP to toggle the bit 1 of port P ₀ continuously.	5
l4.	(a)	Write a 8051 C program to convert unpacked BCD to ASCII and to display it on P_0 .	5
	(b)	Write a 8051 C program to convert ASCII digits '4' and '7' into packed BCD and to display on port P_1 .	5
15.	(a)	List the advantages and disadvantages of using 8051 C.	5
	(b)	Explain the different data types available in 8051 C.	5
16.	(a)	Explain polling and interrupt method of executing an interrupt.	5
	(b)	Mention different types of interrupts available in 8051 along with its vector table.	5
17.	Wri	te the schematic algorithm and a program to interface push button switch to 8051.	10
18.		te an ALP to receive data serially at a baud rate of 4800 and send the received to R_1 .	10
10	Wei	te a schematic algorithm and program to interface stepper motor with 9051	10