

JTAG Debug

P1D

PRECURSOR

Debug HAT
v4a 09/21 bunnye
<https://precursor.dev>

FPGA Debug and Programming

Top FPC Breakout

GPIO5
VSYS
TS

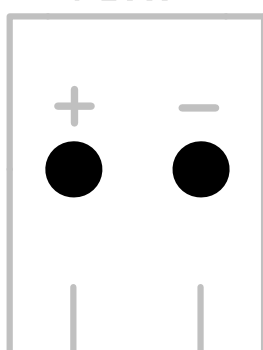
- GPIO0
- GPIO1
- VIBE
- GND
- GPIO2
- GND
- GND
- SCL (1.8V)
- SDA (1.8V)
- 1.8V
- GPIO3
- GPIO4
- GPIO7
- GPIO6 + BAT -
- P21D
- P22D

Note:
GPIO are
3.3V!

P24D



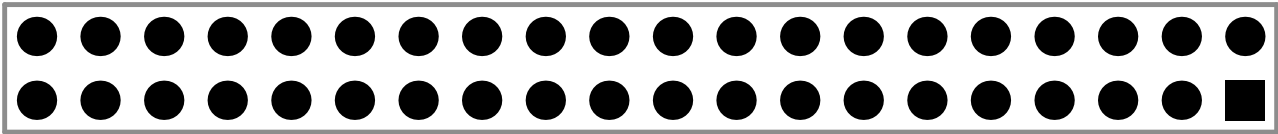
CERN OHL L



Top FPC
Breakout

P20D

J200



P2D



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