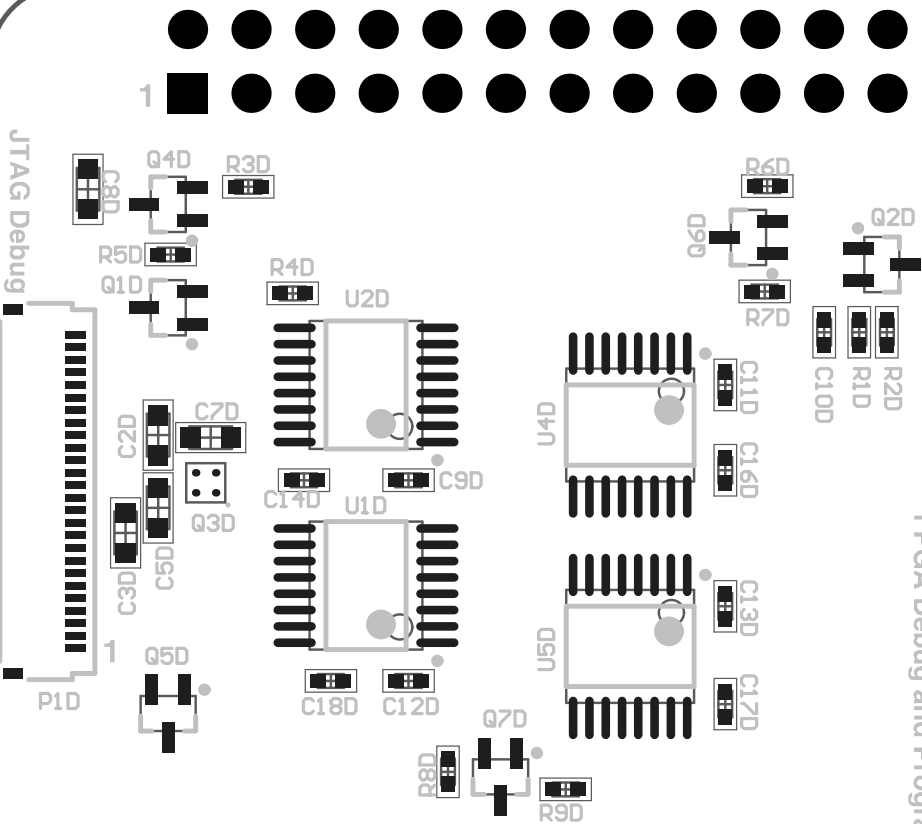


JTAG Debug



PRECURSOR

Debug HAT

v3 12/20 bunnyie
<https://precursor.dev>

FPGA Debug and Programming

Top FPC Breakout

GPIO5
VSYS
TS

GPIO0

GPIO1

VIBE

GND

GPIO2

GND

GND

SCL (1.8V)

SDA (1.8V)

1.8V

GPIO3

GPIO4

GPIO7

GPIO6 + BAT -

P21D

P22D

Note:
GPIO are
3.3V!

P24D

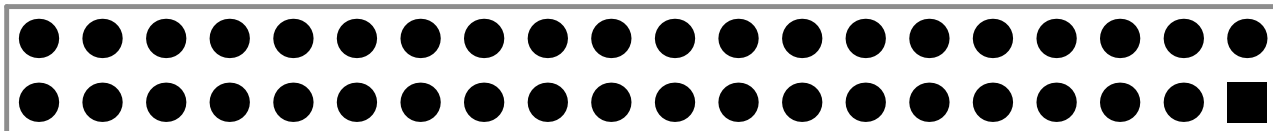
+ SPK -

CERN OHL L

Top FPC Breakout

J20D

+ -



P2D



-



+

