



杨安虎 期中

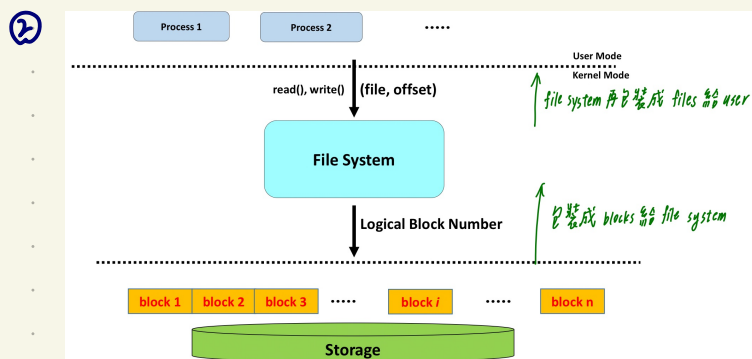


Chapter 11

一、Mass Storage Structure

1. Disk interface: 對 OS 而言, 硬件看起來的樣子

① logical blocks \Rightarrow Logical Block Number (LBN)、LBA



2. Hard disk Drives

① Delay 時間: speed: rotation per min (RPM)

(a) rotational delay (b) transfer time (c) seek time

\Rightarrow access time = seek time + rotational delay + transfer time

\Rightarrow 計算: 設要送 4KB 的 file

Cheetah 15K.5	
Capacity	300 GB
RPM	15,000
Average Seek	4 ms
Max Transfer	125 MB/s
Platters	4
Cache	16 MB
Connects via	SCSI

$T_{seek} = 4ms$

$T_{rotation} = \frac{1}{2} \times \frac{1}{15000} \times 60 \times 1000 = 2ms$

$T_{trans} = \frac{4 \times 2^{10}}{125 \times 2^{20}} = 0.032 \times 2^{-10} s \rightarrow$ 小到可忽略

$\therefore T_{I/O} = 4ms + 2ms = 6ms$

Throughput = $4KB / 6ms = 0.66MB/s$

② 總結: 傳輸愈好 (最好還是 sequential)

ex. ROM, Flash

3. Nonvolatile Memory Devices (NVM): 斷電後資料仍存在

① sol: NAND flash memory, forms: SSD

② pros: 因為 沒有機械裝置, 所以.....

cons: 貴、less capacity、block 被 erase 的次數有限

③ 一些特性:



補充: 外加 Erase 非常耗時!

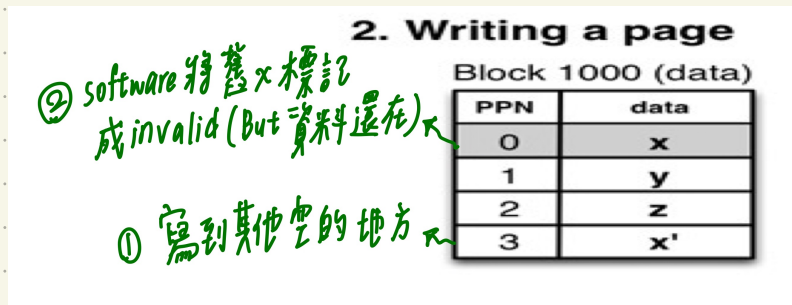
\rightarrow 只能 read/write/erase, 不能 overwrite (傳統 HDD 可 overwrite)

\rightarrow 甚至 erase 只能一次 erase entire block

④ How to handel overwrite?

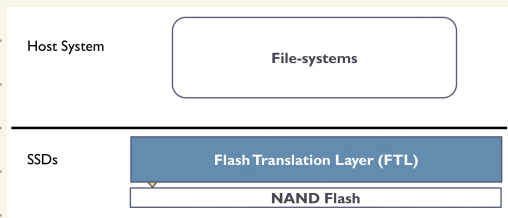
(a) Inplace update X 要copy 2=x

(b) Out-of-place update V ∴ logical block的位置會變



File system for HDD 不能直接用在 SSD.

⑤ FTL (Flash Translation Layer): 把 SSD 的 out-of-place 包裝成 in place 給 O.S. (file sys)



(a) 做 address mapping: 把 logical block \Rightarrow physical page location

(b) 做法:

LPN	PPN \rightarrow physical page no.
:	:
5	10 \rightarrow 2
:	:

logical page no.

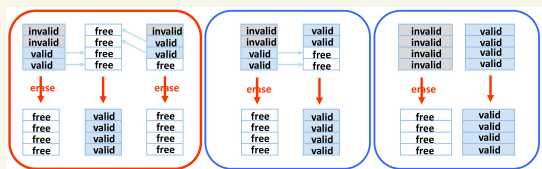
① OS 發出 write (5, A)

② OS 又發 write (5, B)

(會有 software 將 page 10 記為 invalid)

(c) Garbage collection (when No free space available)

會造成 write Amplification



user 發一個 write request,

但實際上可能伴隨其他 read/write/erase

∴ 要準備 over provisioning space for GC

4. Volatile Memory \rightarrow ex. RAM (SRAM, DRAM)

① DRAM used as 高速 temporary storage device

5. Secondary Storage connection Method

① 連接方式: Bus, 又分成

(a) system bus (memory bus): handled by north bridge \Rightarrow memory controller hub

↳ 連接 CPU、memory、高速的 device

(b) I/O bus: handled by southbridge \Rightarrow I/O controller hub

↳ 連接周邊的 slow devices (ex. SATA, USB)