



Readout System Enhancements for ATLAS ITk Project

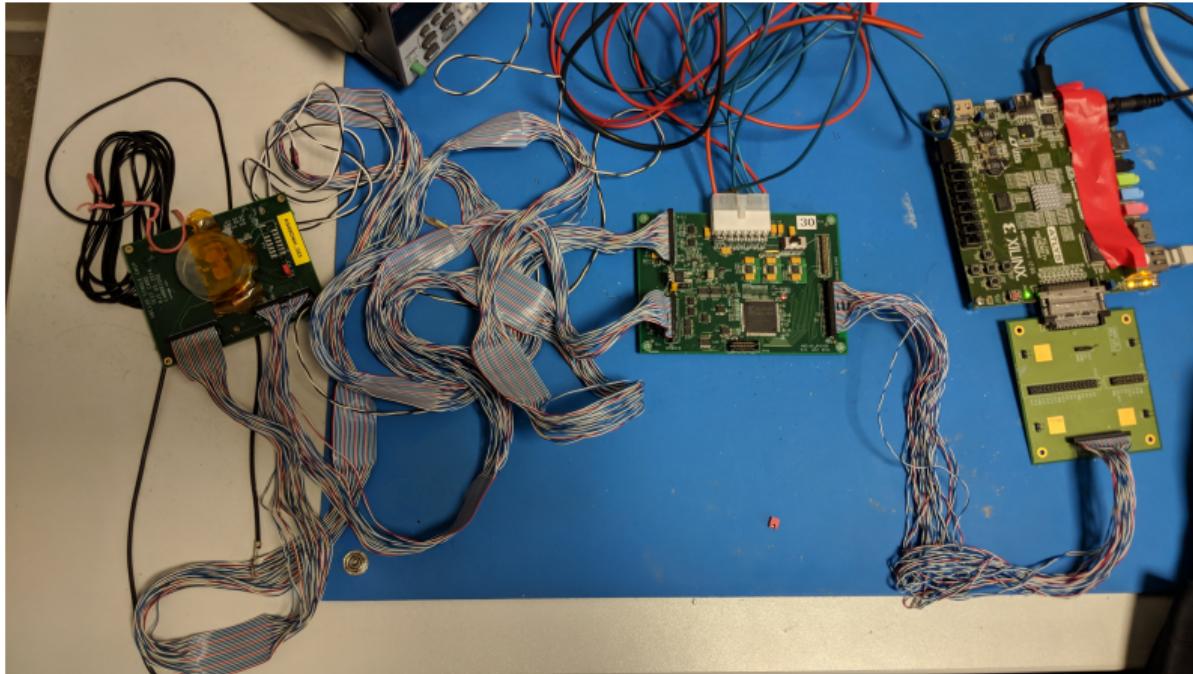
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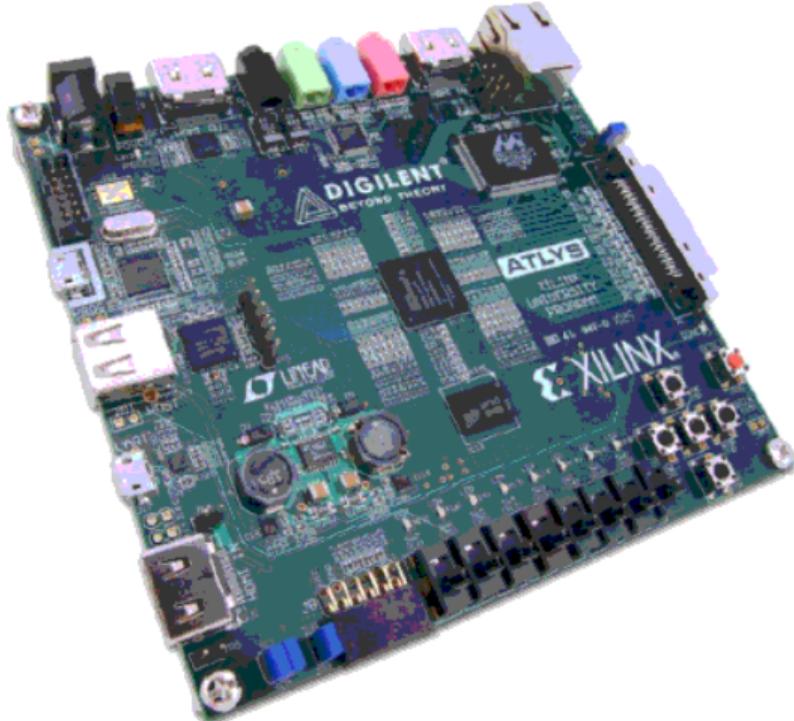


Current DAQ Readout Chain



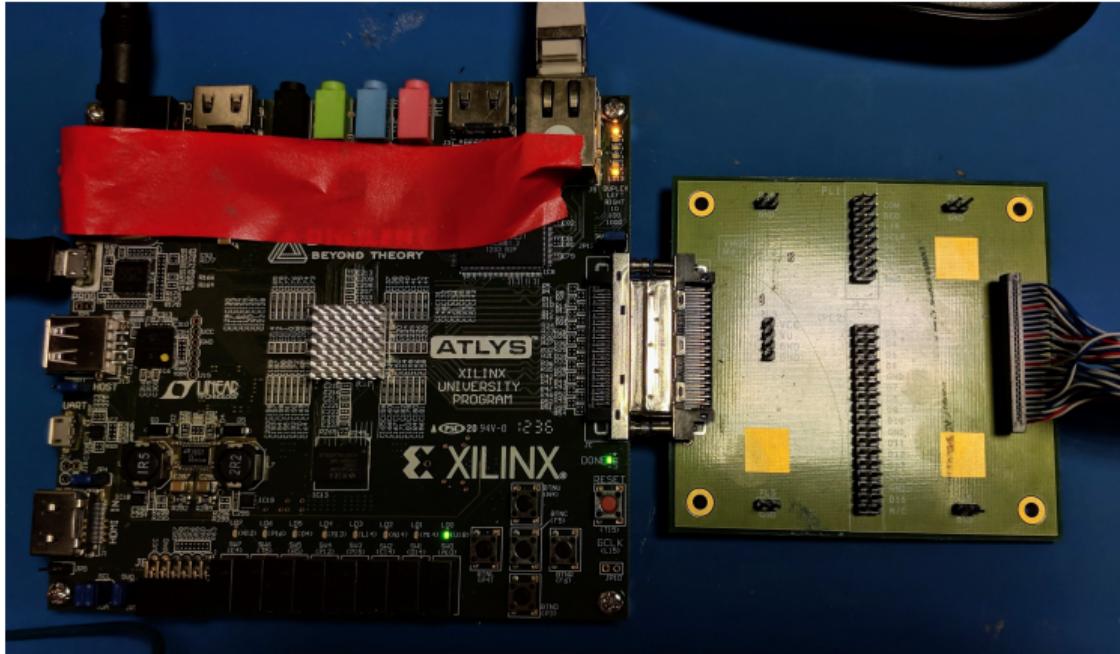
A look at the fully assembled readout chain.

ATLYS Board



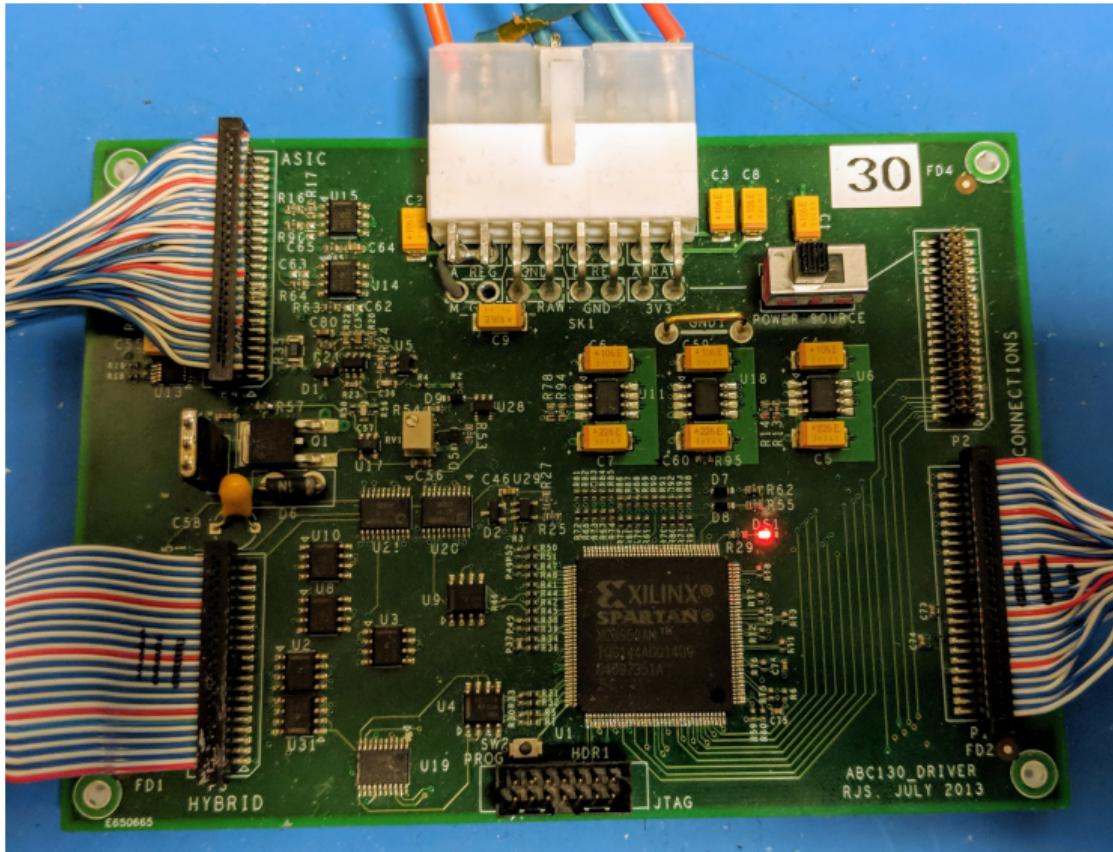
ATLYS is a low cost, widely available board that supports single chip, hybrid, and module tests.

Interface Connection



The ATLYS board is connected to its interface board, VMOD-IB.

Hybrid Board



Orientation of the power,
ABC130, and ATLYS
connections.

ABC130 Single Chip Test Card



Test card, with connection to the Hybrid chip.

Obstacles

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Acknowledgements

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