



Readout System Enhancements for ATLAS ITk Project

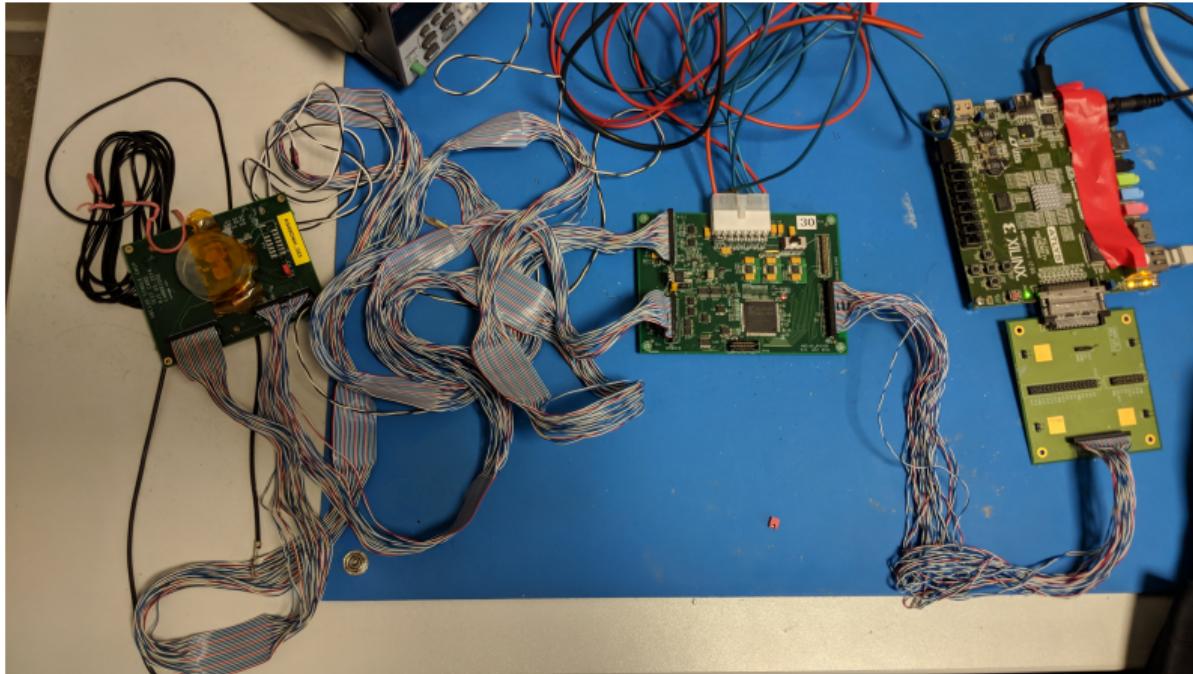
Kyle Beyer, kyle.beyer@cern.ch

Dylan Hatch, dylan.brown.hatch@cern.ch

11 Oct 2018

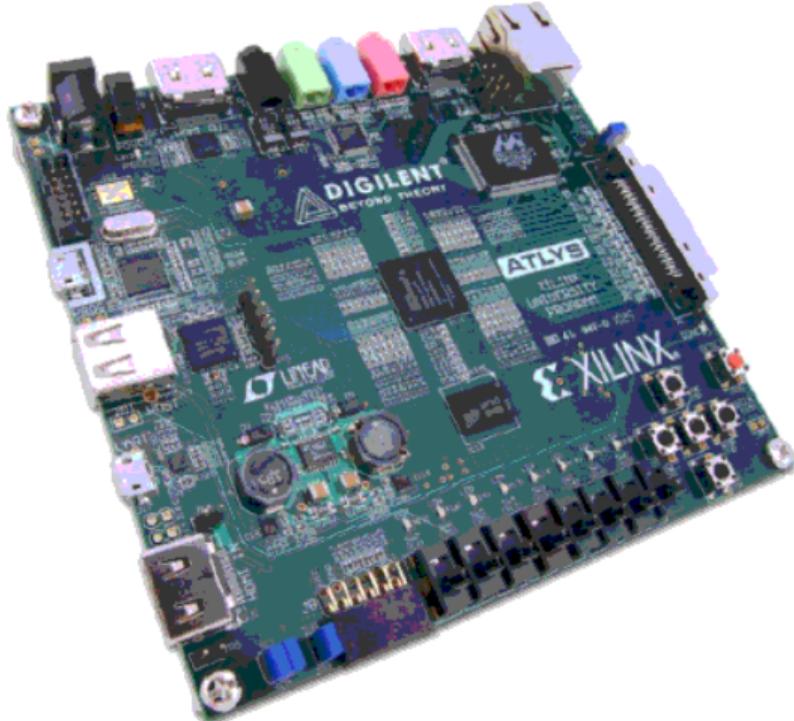


Current DAQ Readout Chain



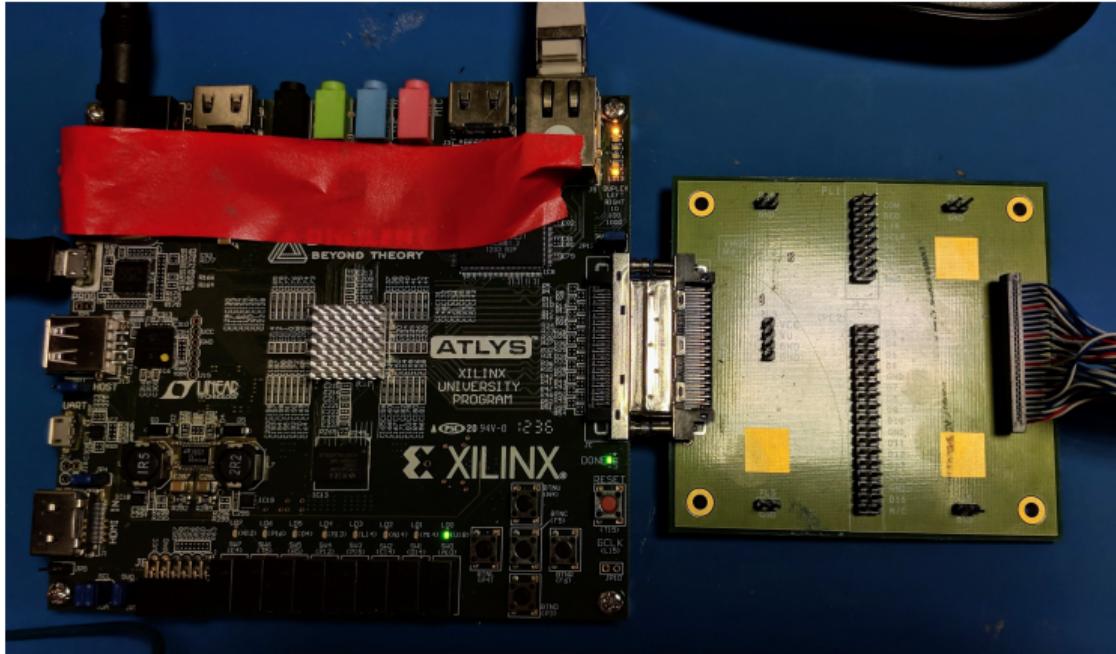
A look at the fully assembled readout chain.

ATLYS Board



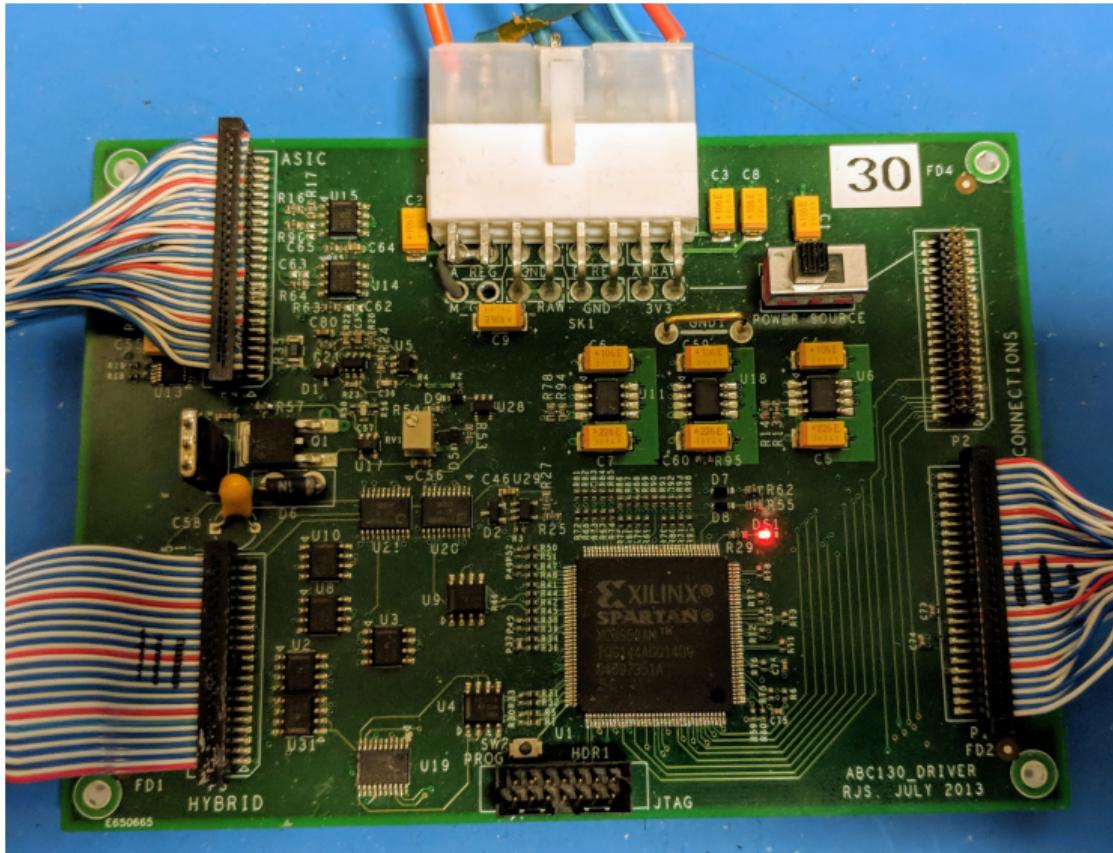
ATLYS is a low cost, widely available board that supports single chip, hybrid, and module tests.

Interface Connection



The ATLYS board is connected to its interface board, VMOD-IB.

Hybrid Board



Orientation of the power,
ABC130, and ATLYS
connections.

ABC130 Single Chip Test Card



Test card, with connection to the Hybrid chip.

Obstacles

- ▶ We accidentally lost a configuration and register assignment file on a formatted Windows SSD.
- ▶ We have a segmentation violation in the GUI of the ITSDAQ software.
- ▶ We have a compile error due to the absence of a header file supposedly in the NI-VISA library.
- ▶ Our new installation of the NI-VISA library has caused problems with gnome on the workstation.

Acknowledgements

We would like to acknowledge the University of Michigan Department of Physics, specifically Jean Krisch and Tom Schwarz, and Steven Goldfarb.
We would also like to acknowledge the support of the Lounsbury foundation.





www.cern.ch