**PRELAB ASSIGNMENT 3**

I have 3 different files in this assignment. The first is the design file. Here I created two numbers ABCD and EFGH, with each letter representing a bit. I compared each bit with the corresponding number of equivalent bits. It has X, Y, Z values ​​as output. Output Y if the first of the numbers is greater, output X if the second is greater, output Z if they are equal. As explained in the lecture, I matched the constraints as they should be. And in the simulation part, I tested whether my code works correctly with some corner values.

Graphical user interface

Description automatically generated

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