


ARM MPU AM572X Industrial EVM ABC Package

- See the Hardware User Guide for board details
- See the Errata Document for design Issues
- See the PCB Build Specification for PCB Details

PCB1
AM572x Industrial EVM Bare PCB

Catalog
Processors

Texas Instruments, Inc.
Catalog Processors Business Unit
12500 TI Blvd
Dallas, TX 75243

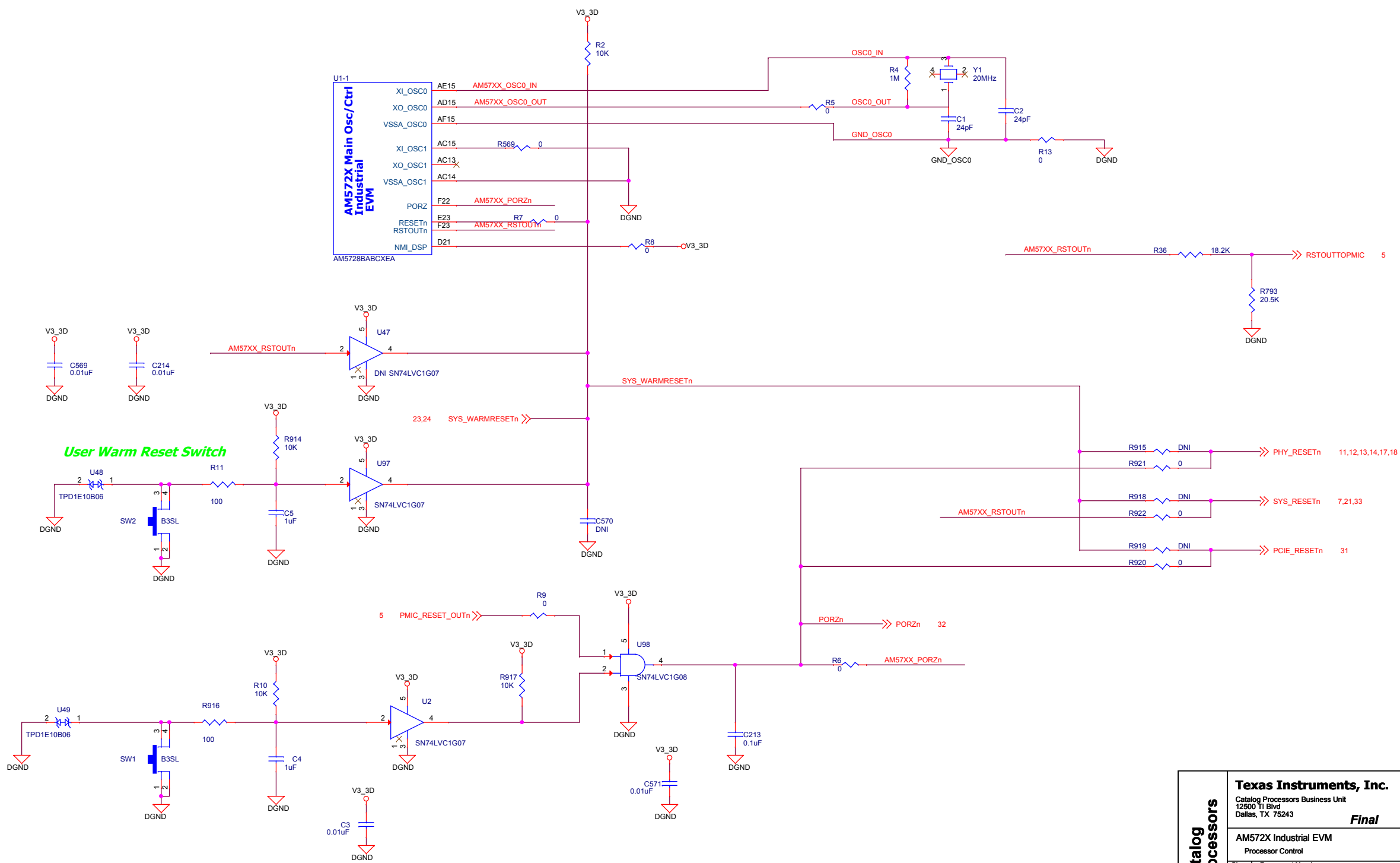


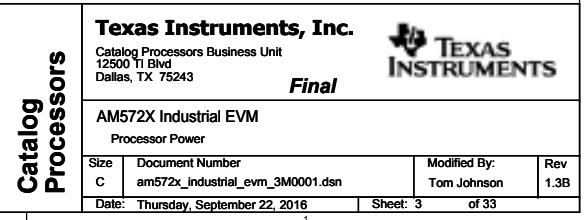
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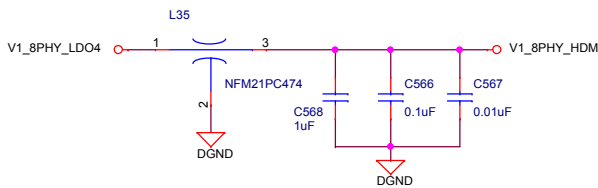
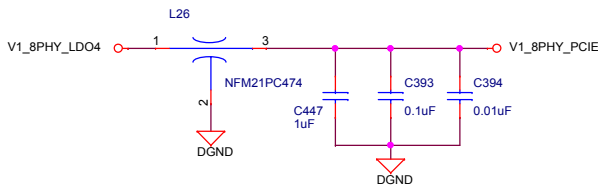
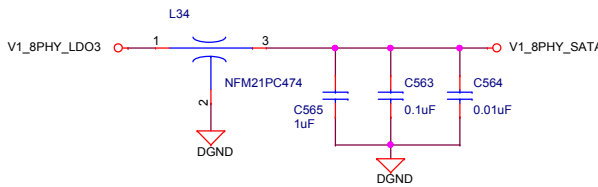
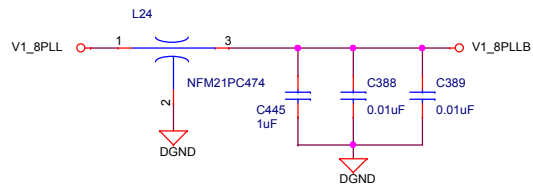
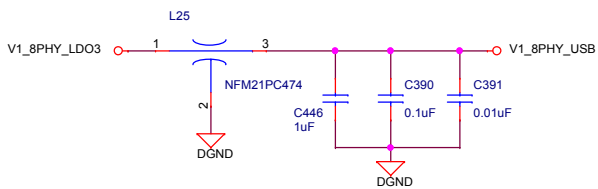
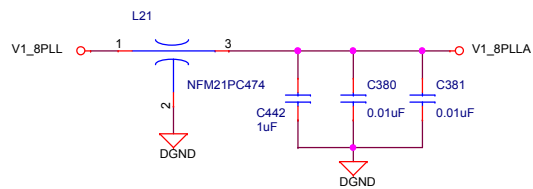
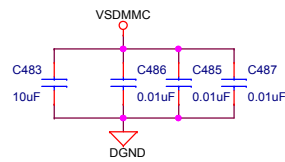
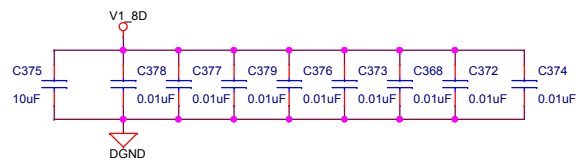
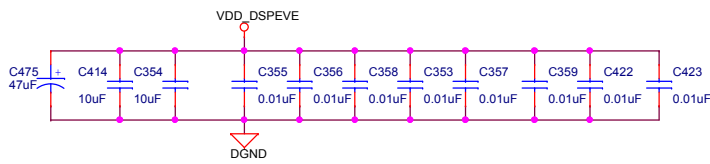
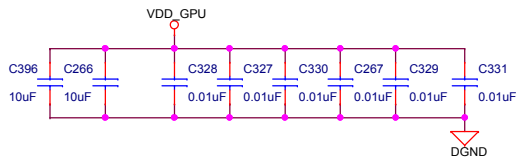
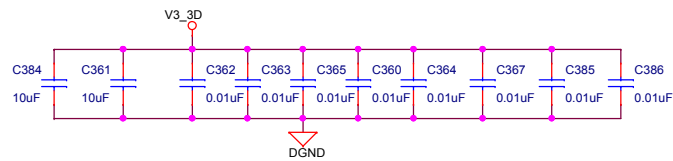
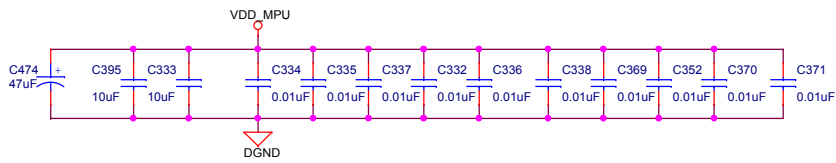
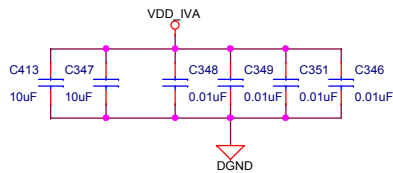
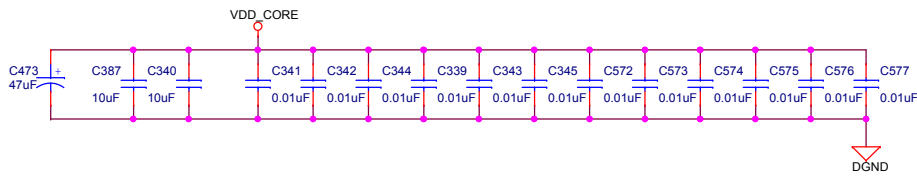
AM572X Industrial EVM

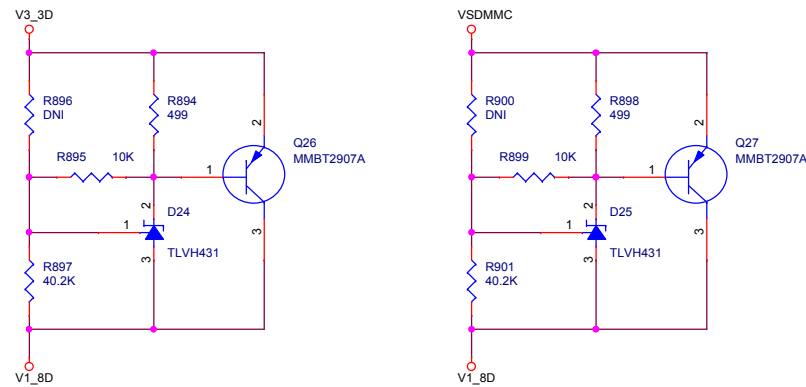
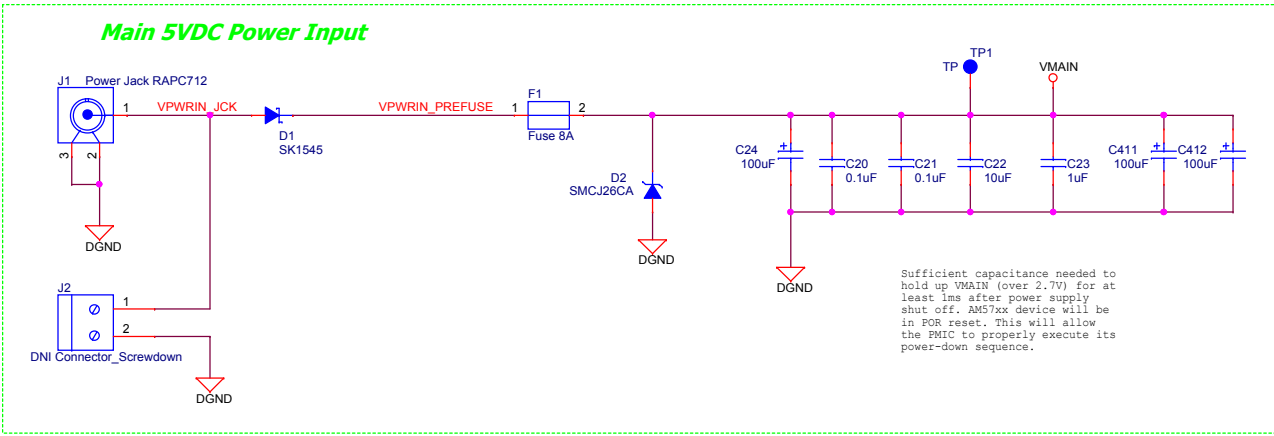
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Date: Thursday, September 22, 2016		Sheet: 1 of 33	







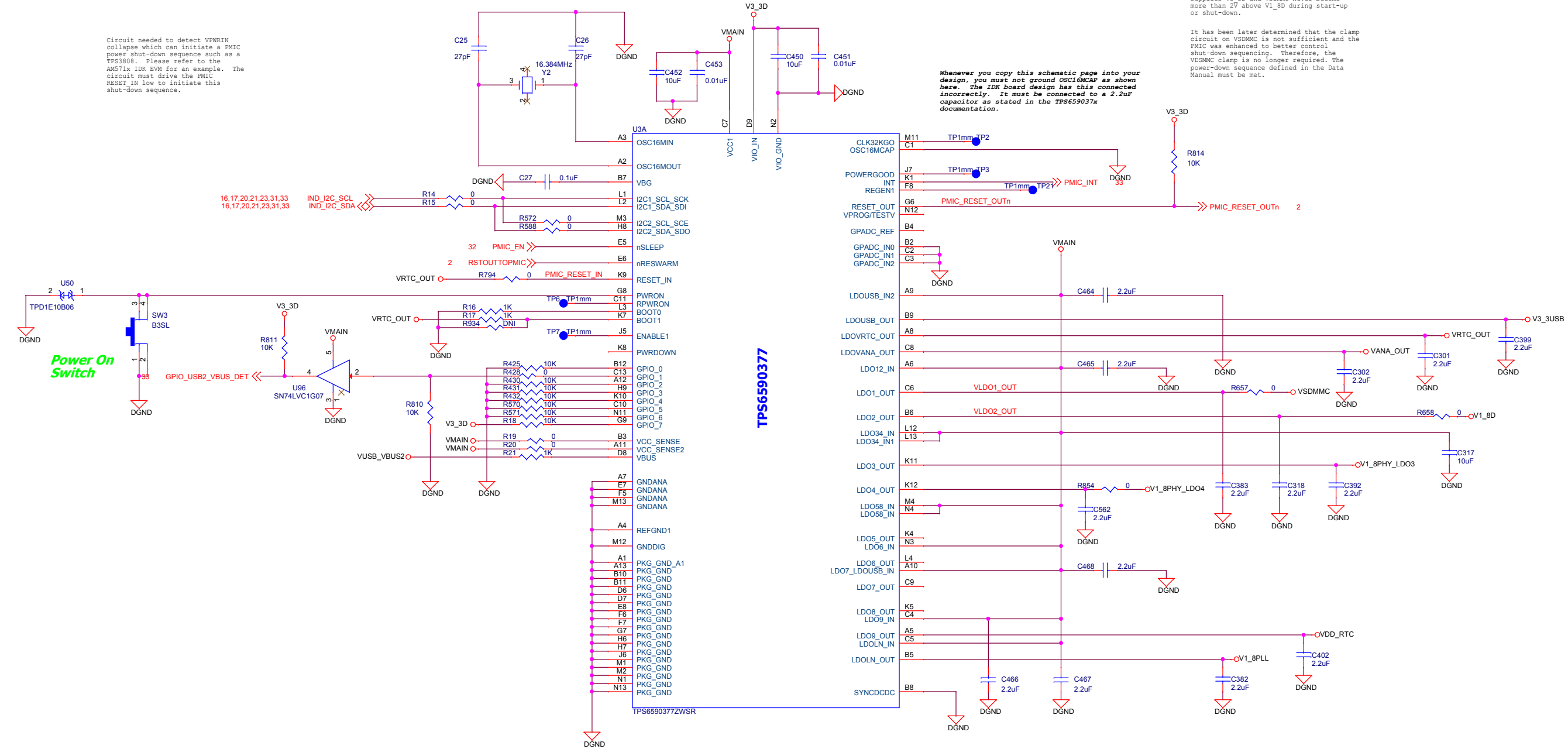


Clamp circuits added to guarantee the 3.3V supplies V3_3D and VSDMMC never become more than 2V above V1_8D during start-up or shut-down.

It has been later determined that the clamp circuit on VSDMMC is not sufficient and the PMIC was enhanced to better control shut-down sequencing. Therefore, the VSDMMC clamp is no longer required. The power-down sequence defined in the Data Manual must be met.

Circuit needed to detect VPWRIN collapse which can initiate a PMIC power shut-down sequence such as a TPS3808. Please refer to the AM571xx IDK EVM for an example. The circuit must drive the PMIC RESET_IN low to initiate this shut-down sequence.

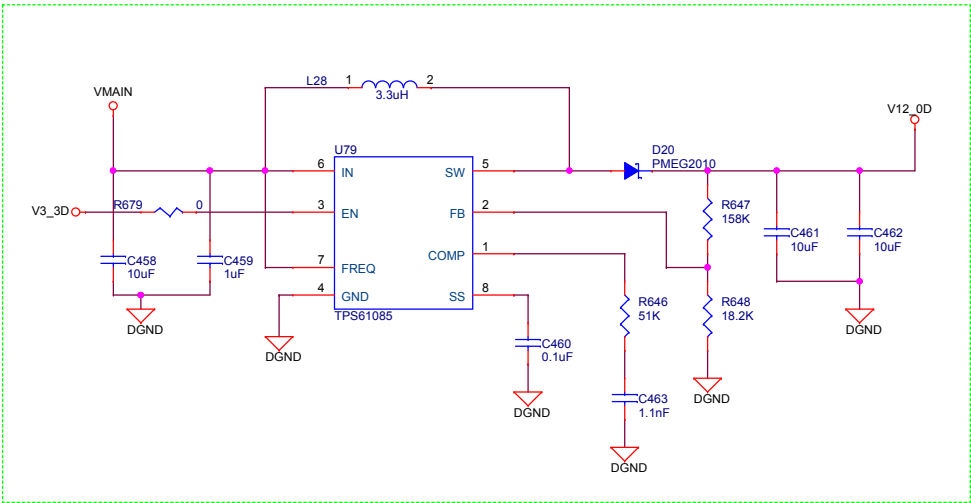
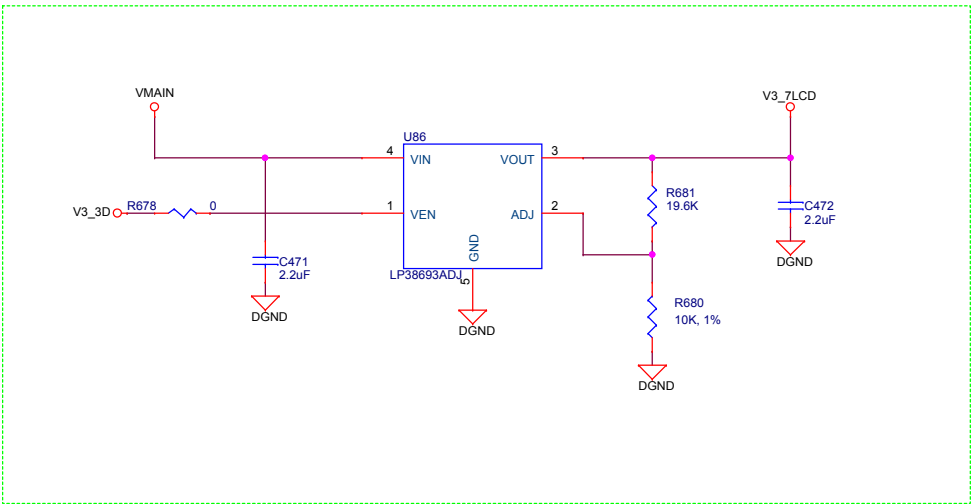
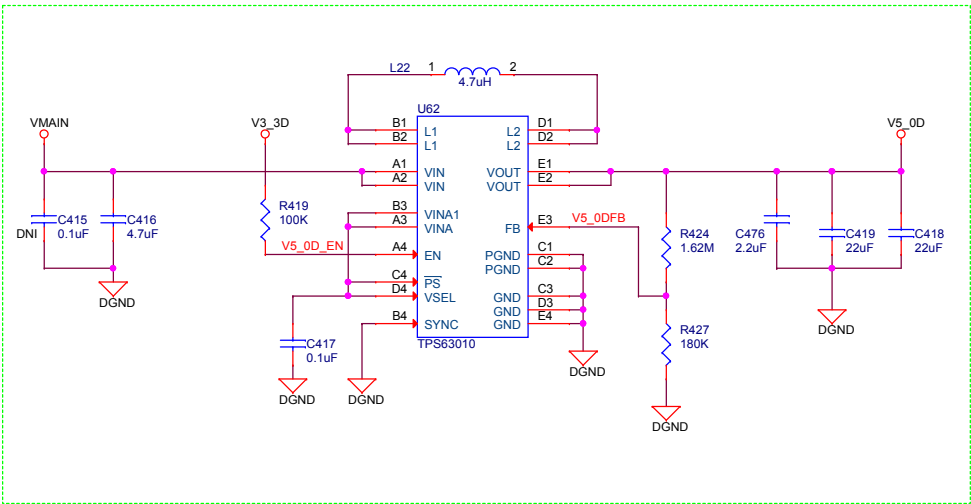
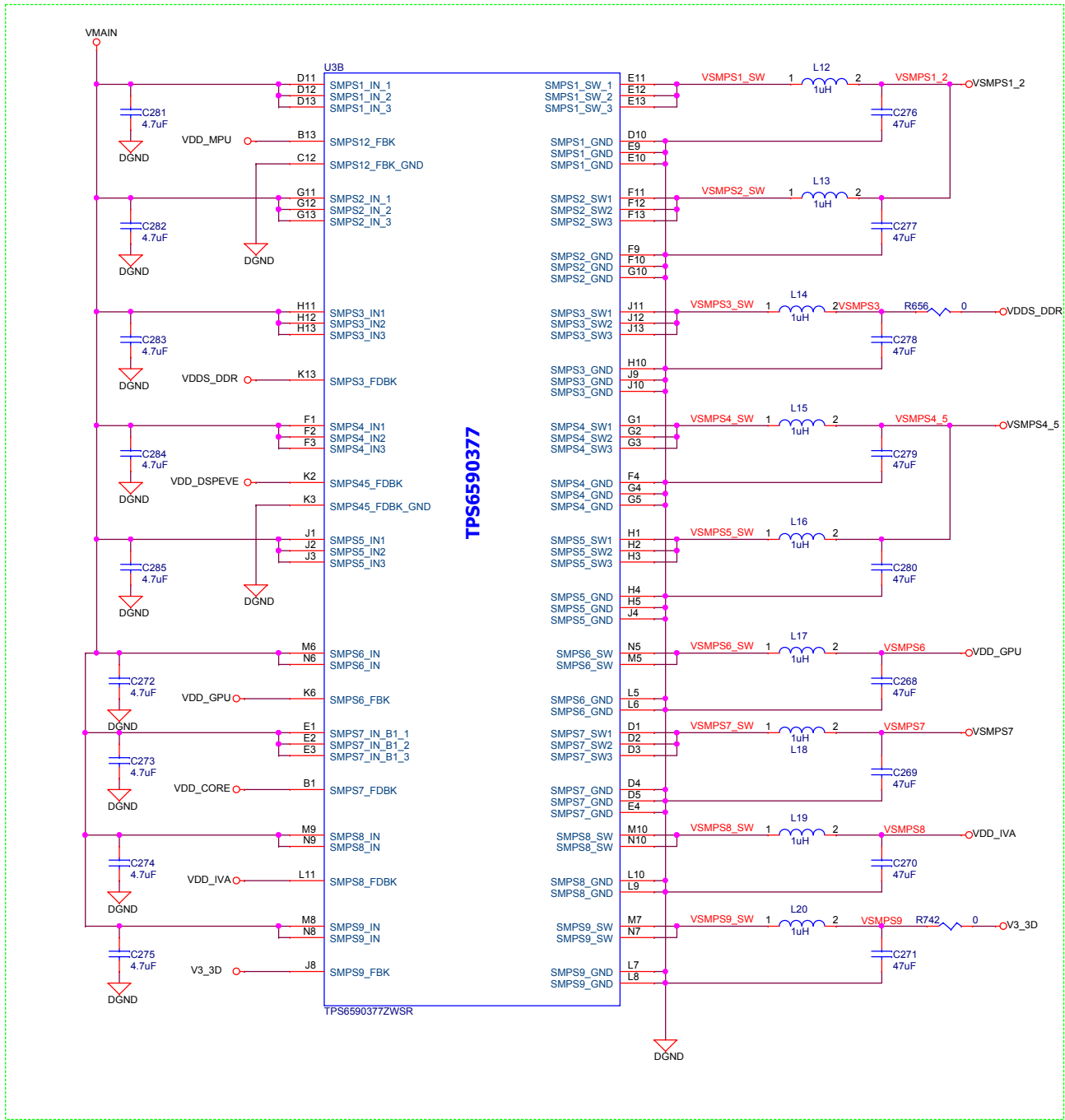
Whenever you copy this schematic page into your design, you must not ground OSC16MCP as shown here. The IDK board design has this connected incorrectly. It must be connected to a 2.2uF capacitor as stated in the TPS659037x documentation.

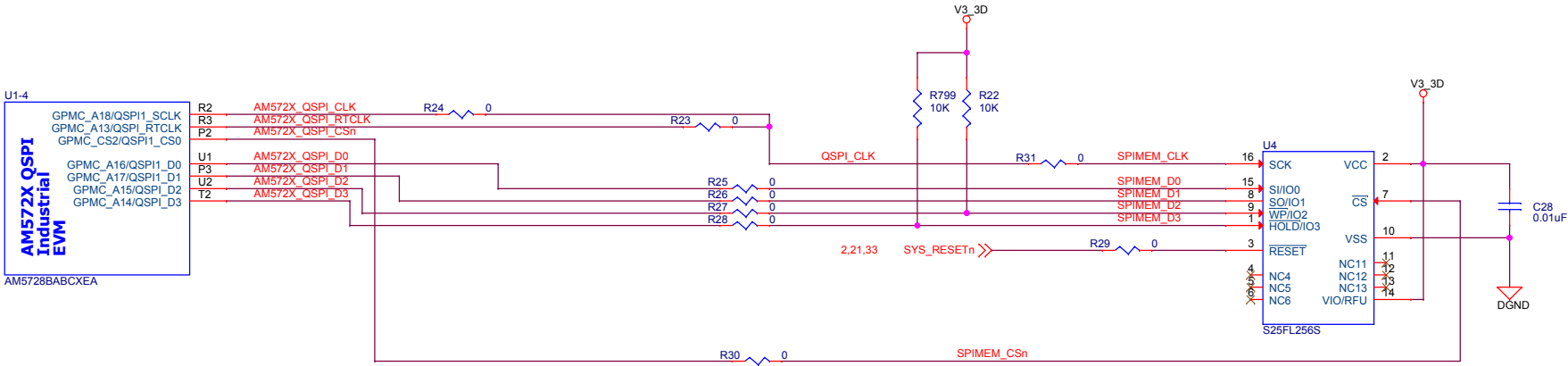


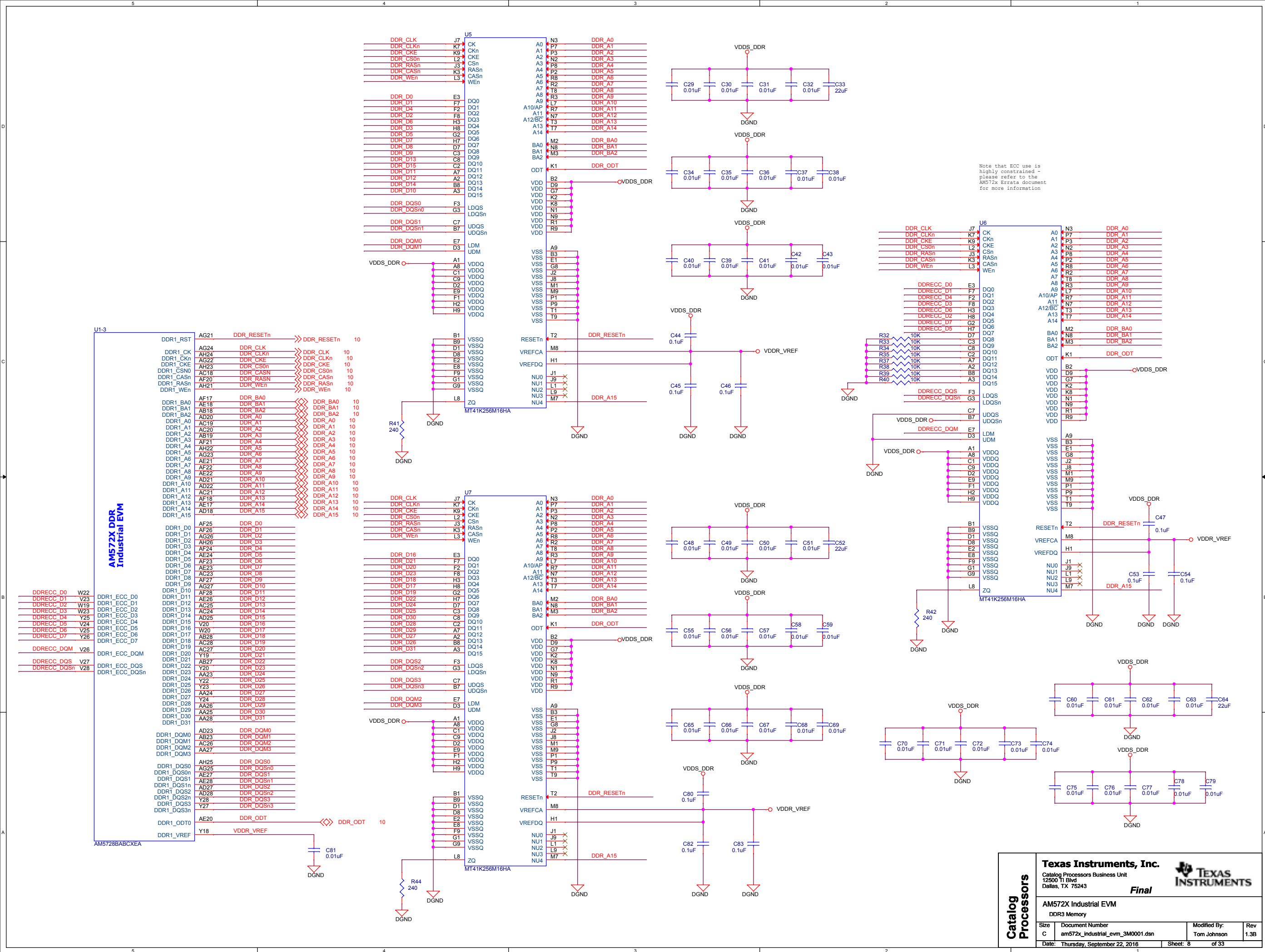
TPS6590377

Power On Switch

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	Catalog Processors Business Unit 12500 TI Blvd Dallas, TX 75243			
	Final			
	AM572X Industrial EVM			
	Power			
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Date:	Saturday, May 05, 2018	Sheet: 5	of 33	

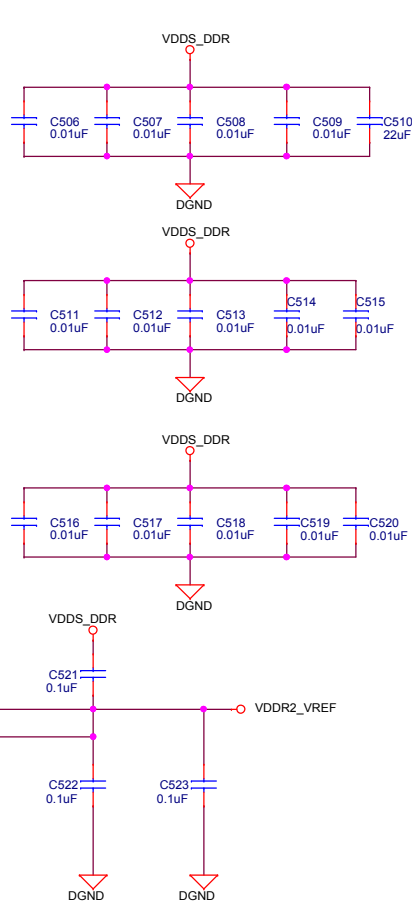
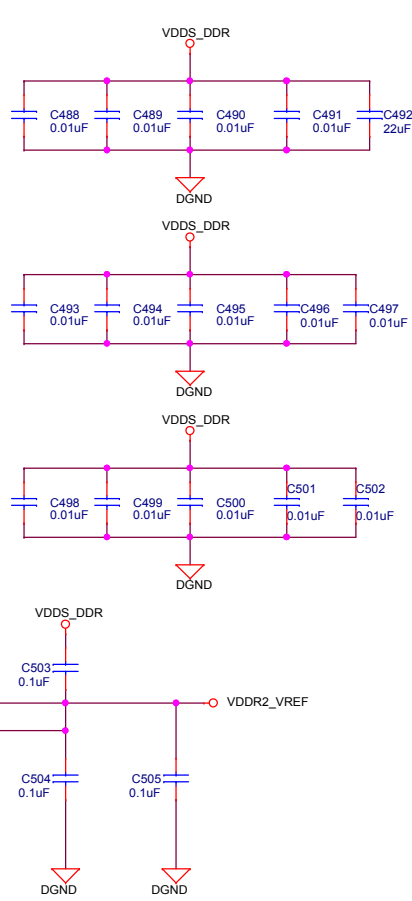
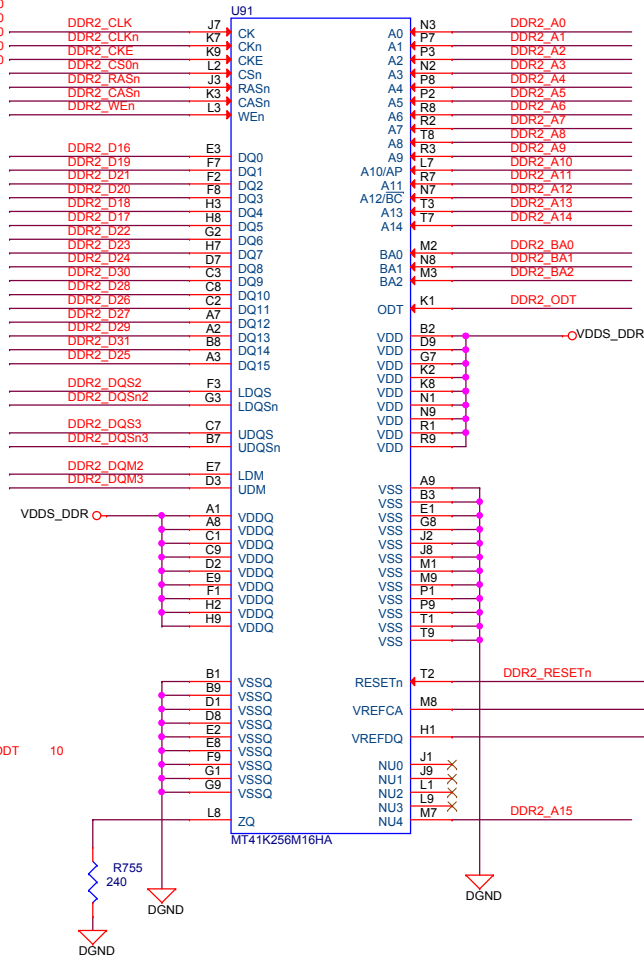
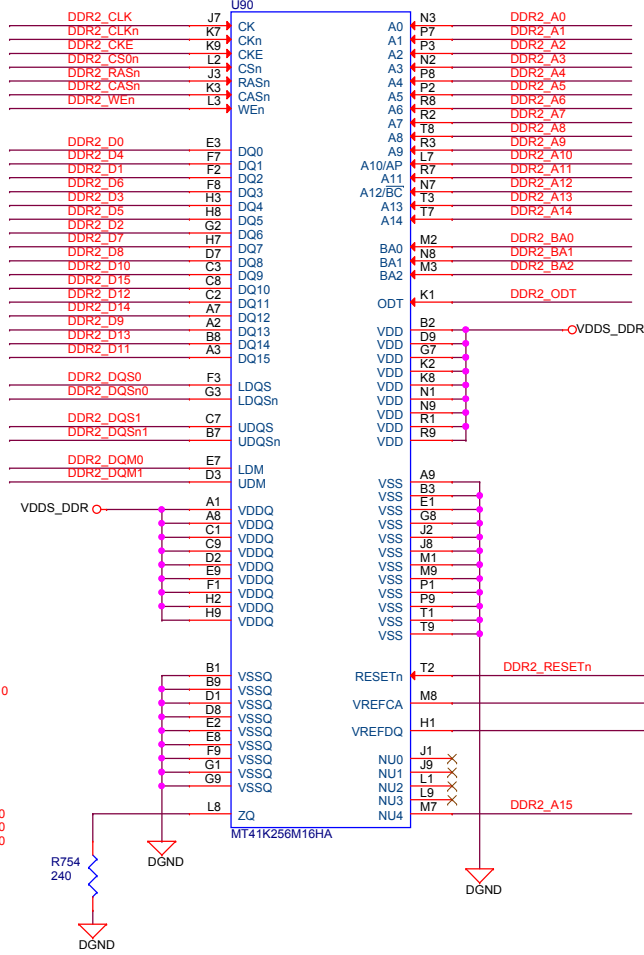
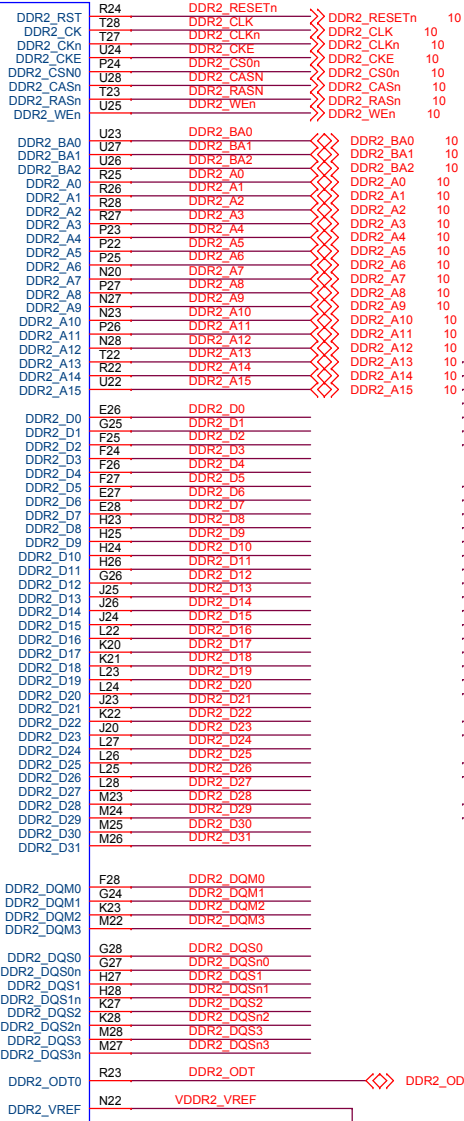


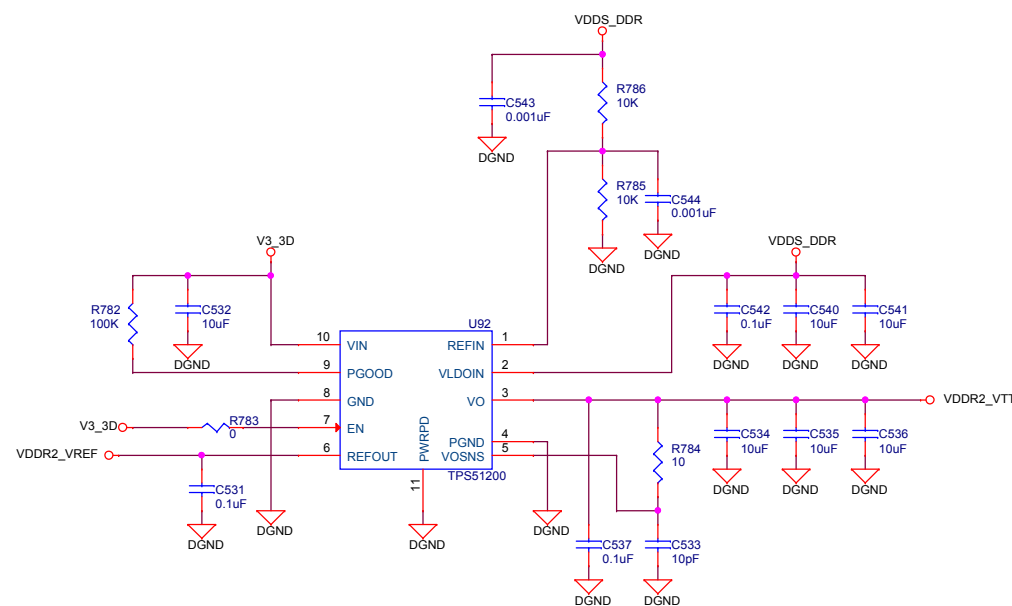
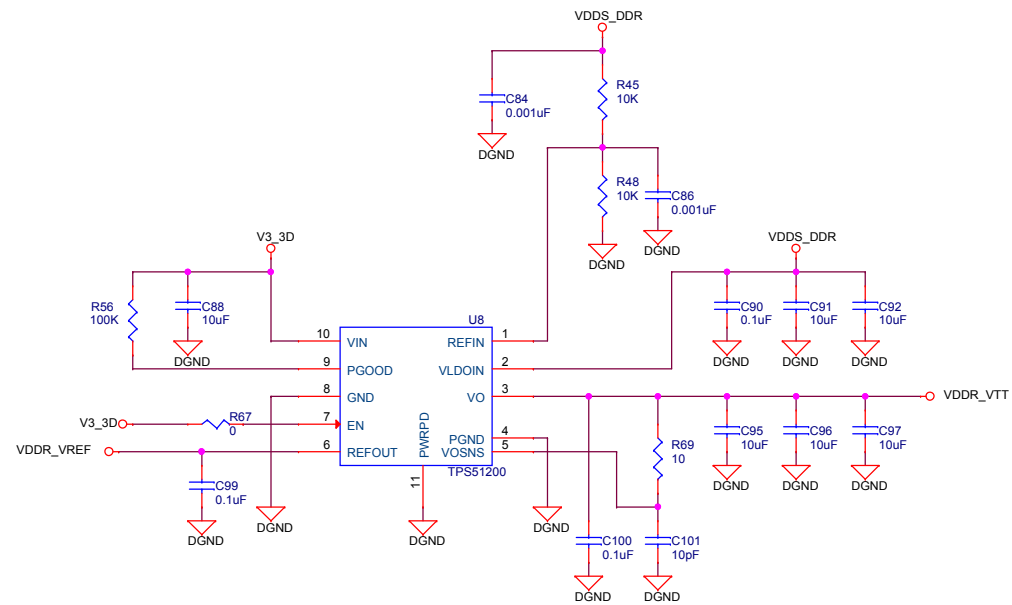
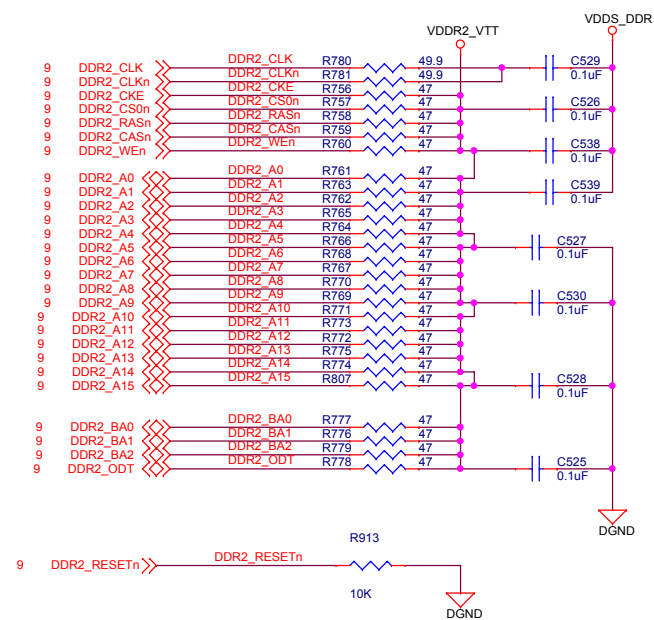
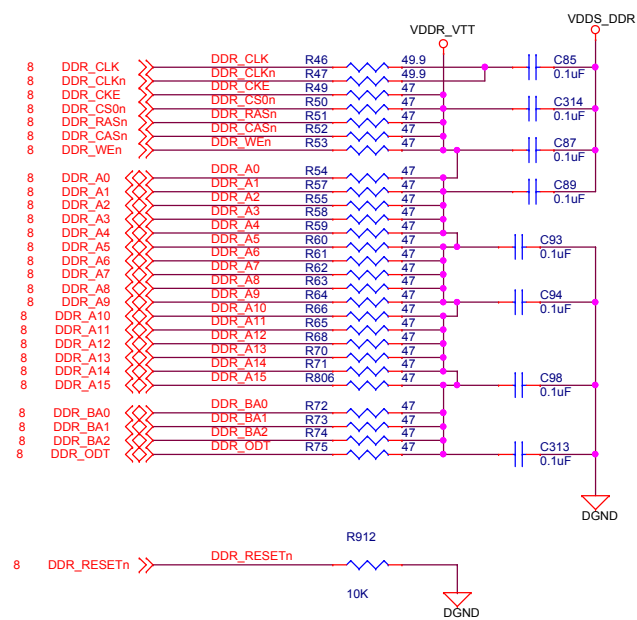


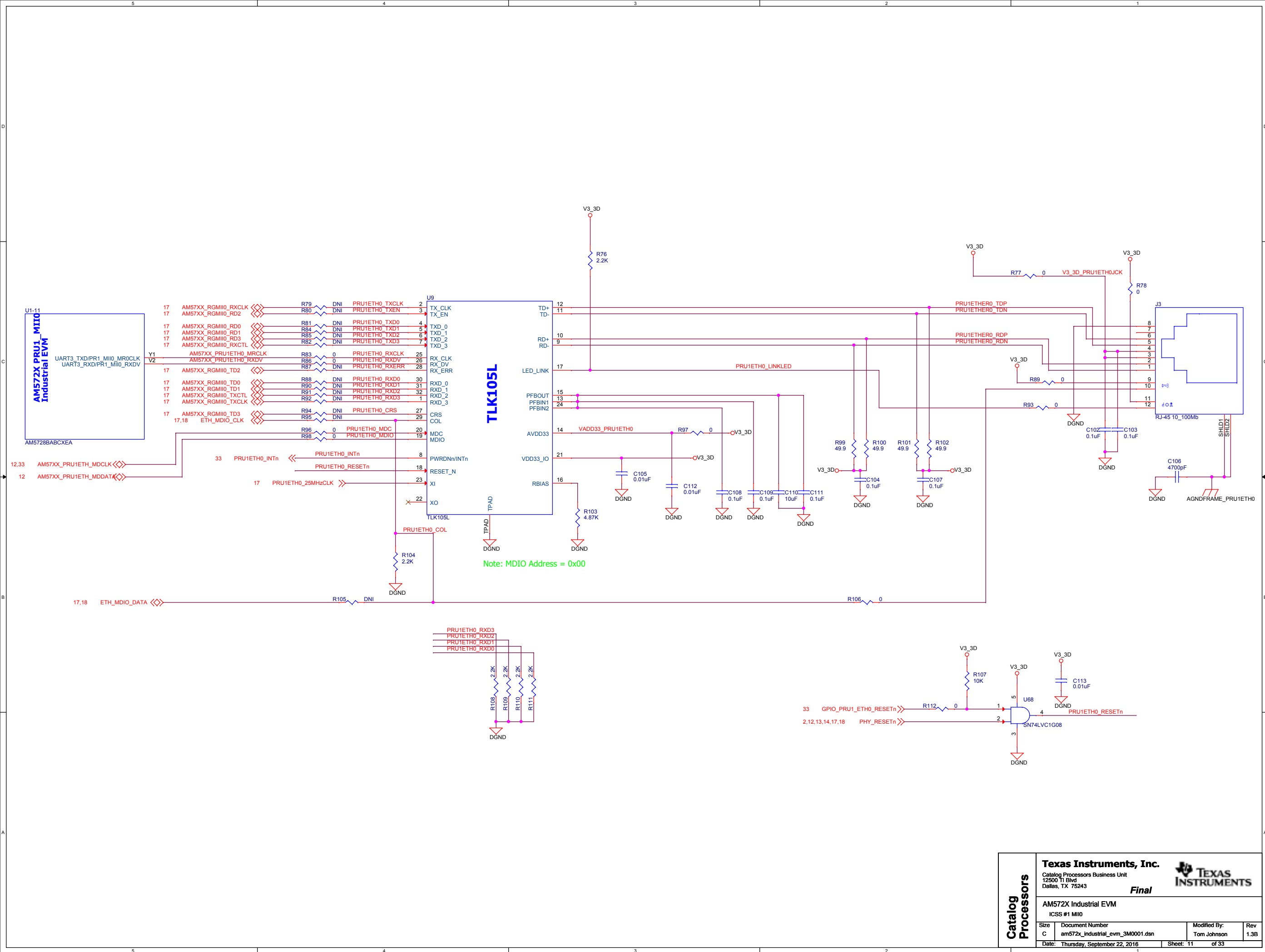


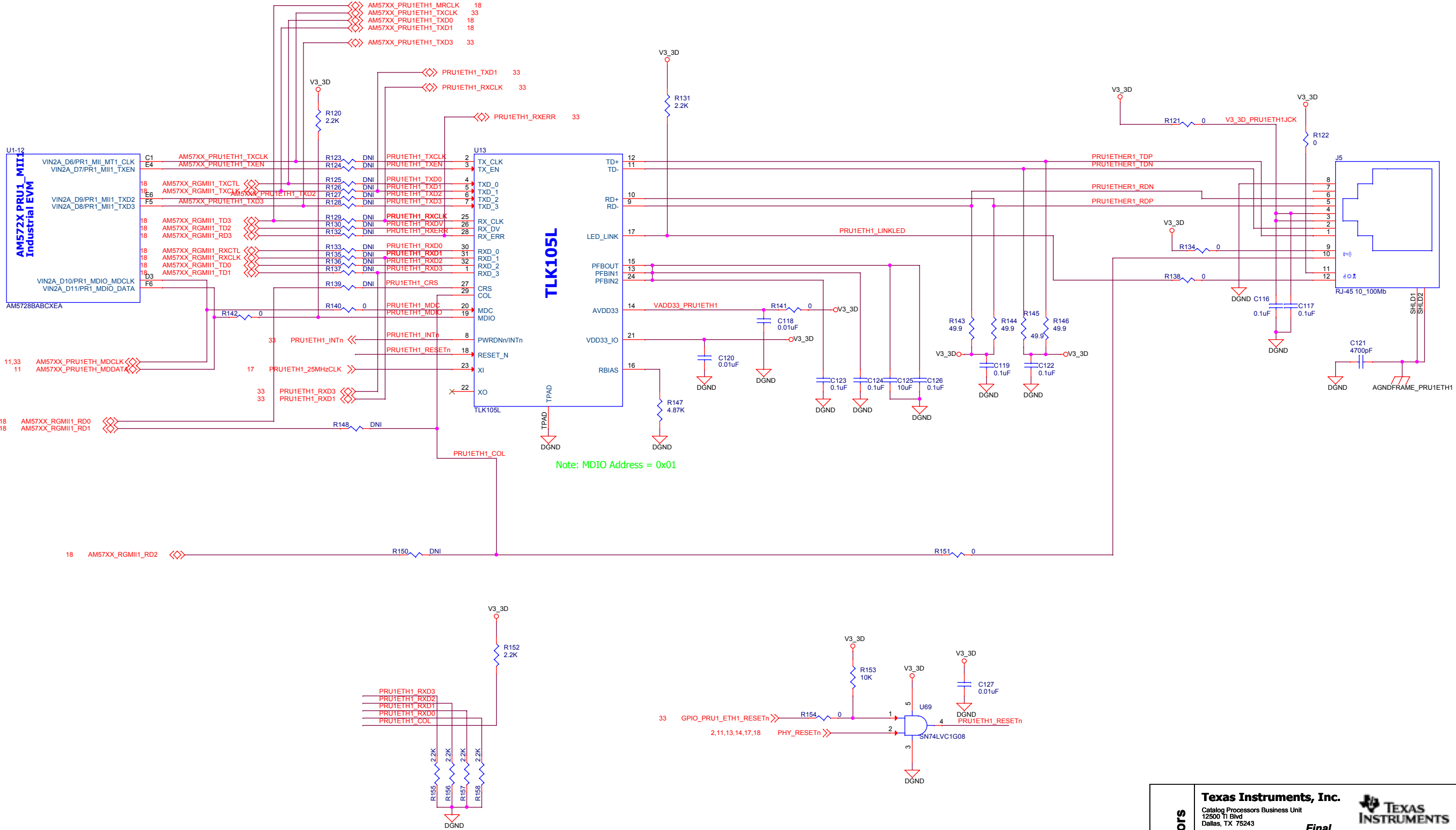
U1-26

AM572X DDR #2
Industrial EVM

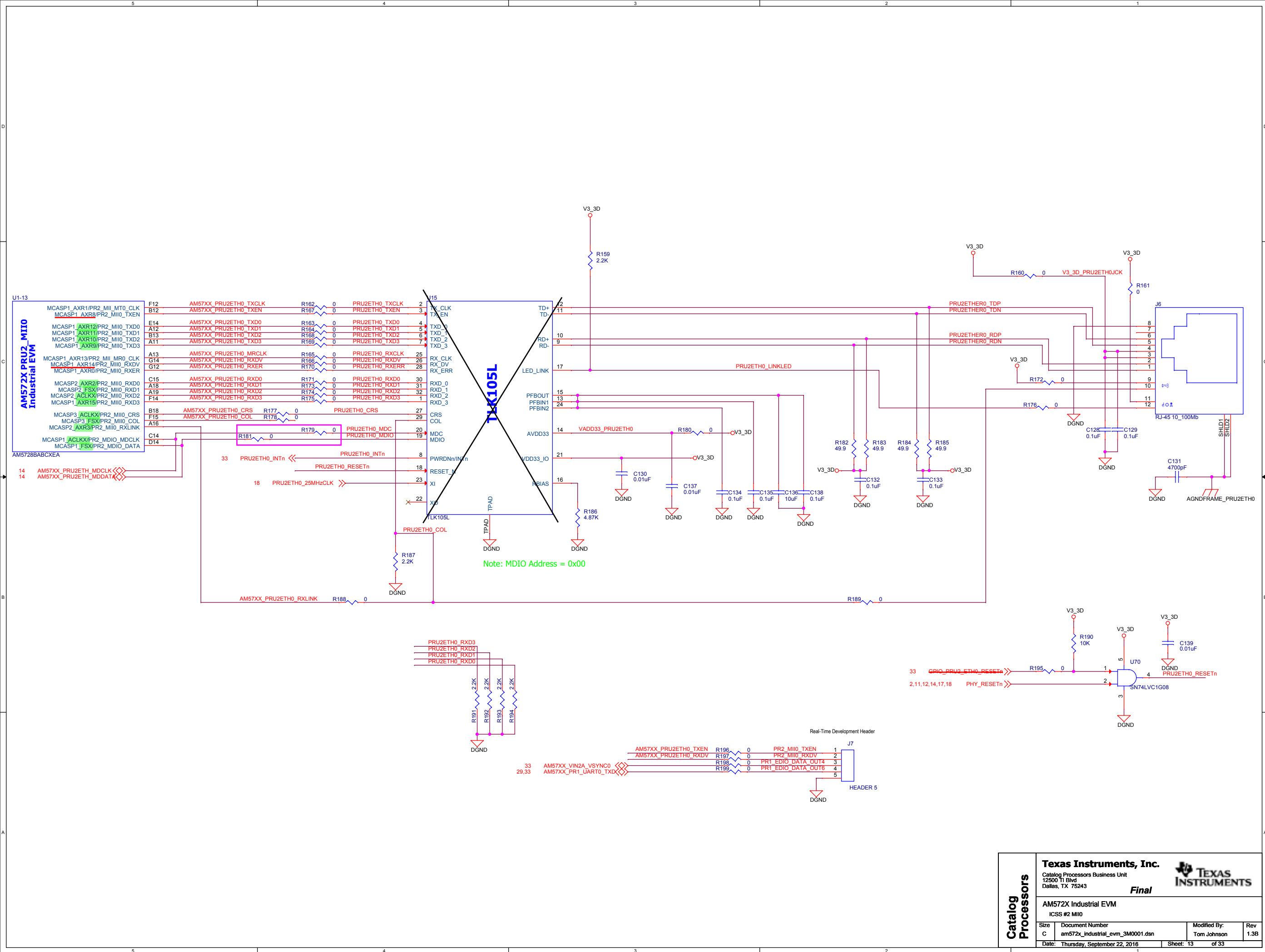


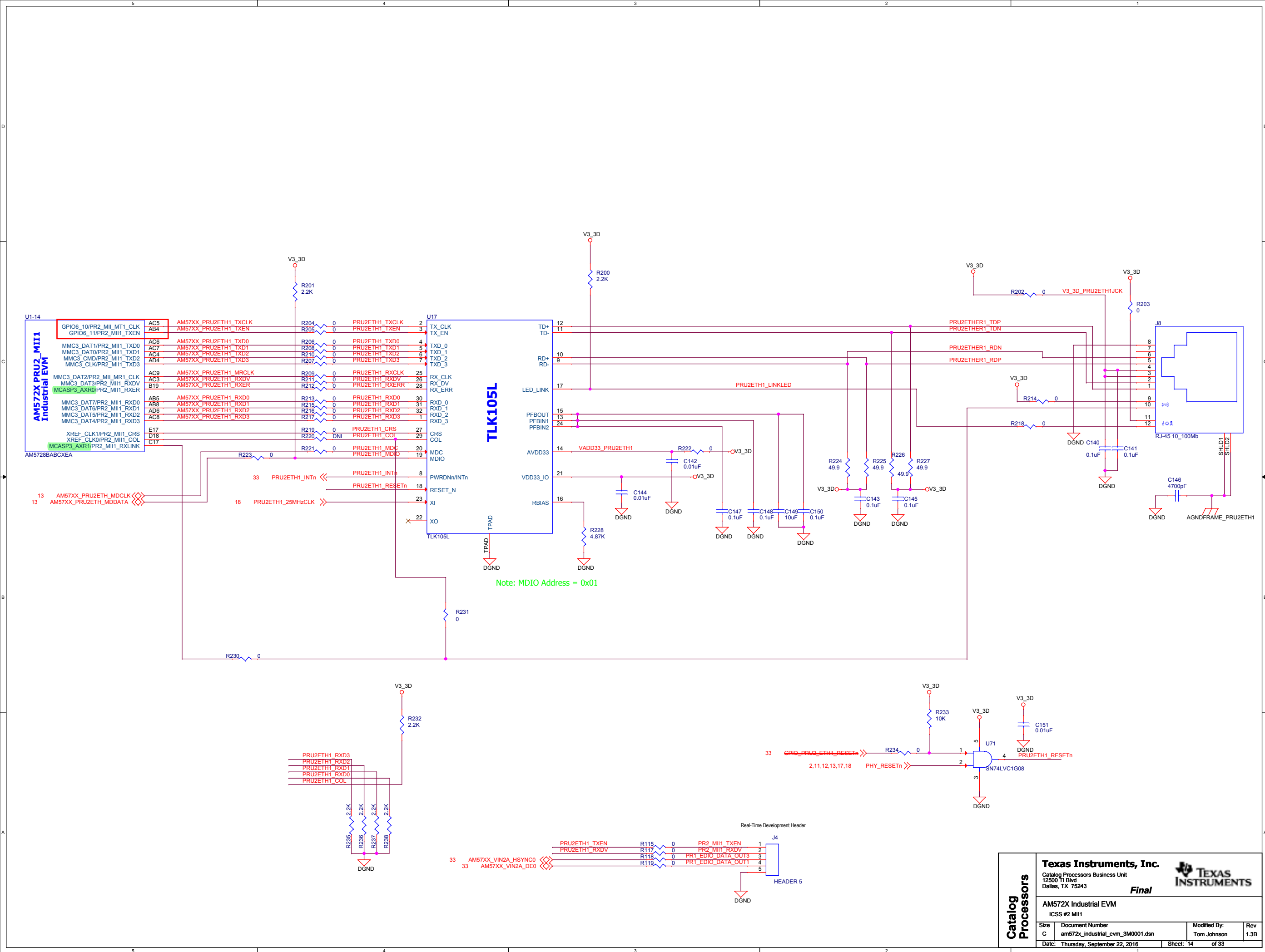




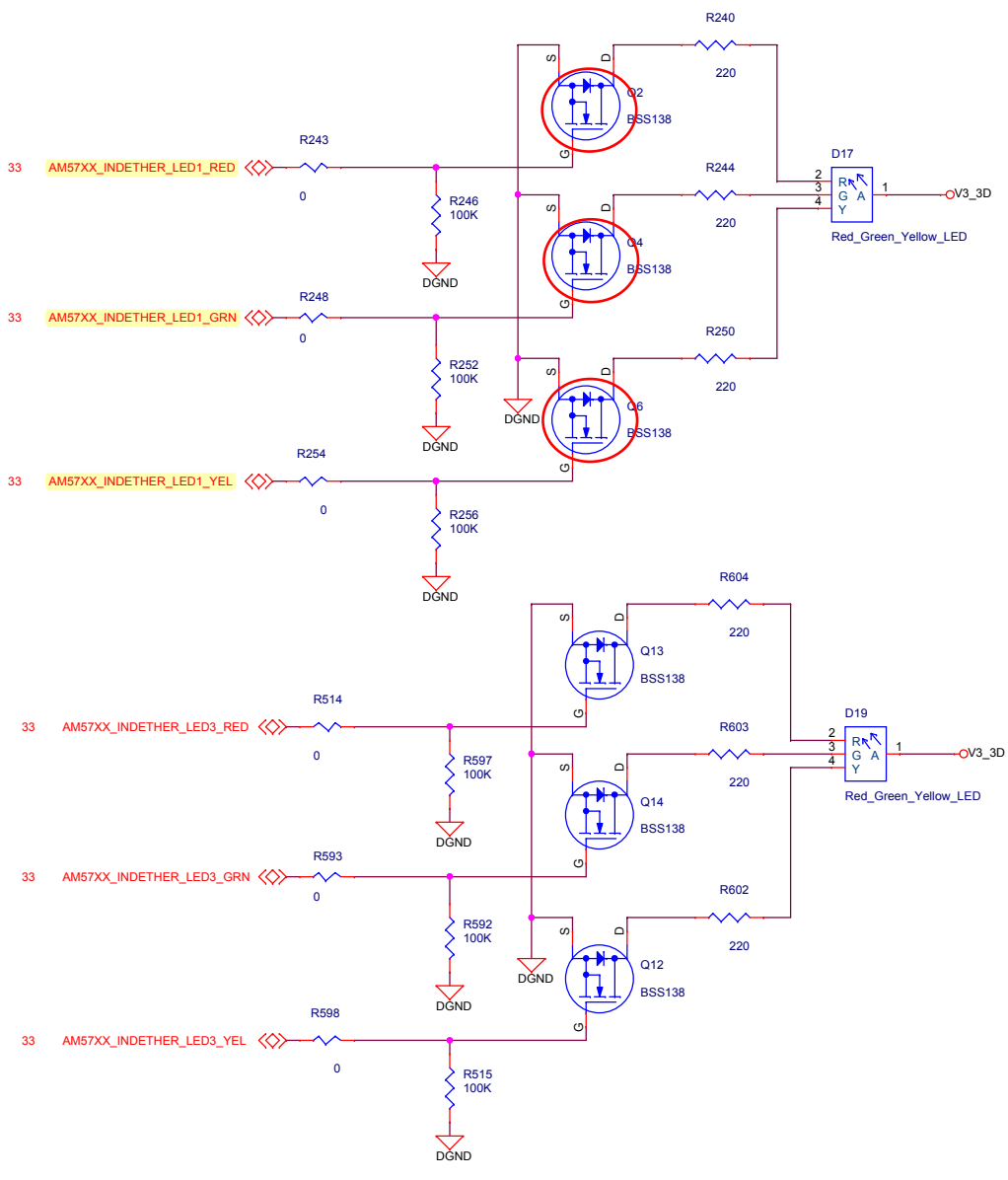
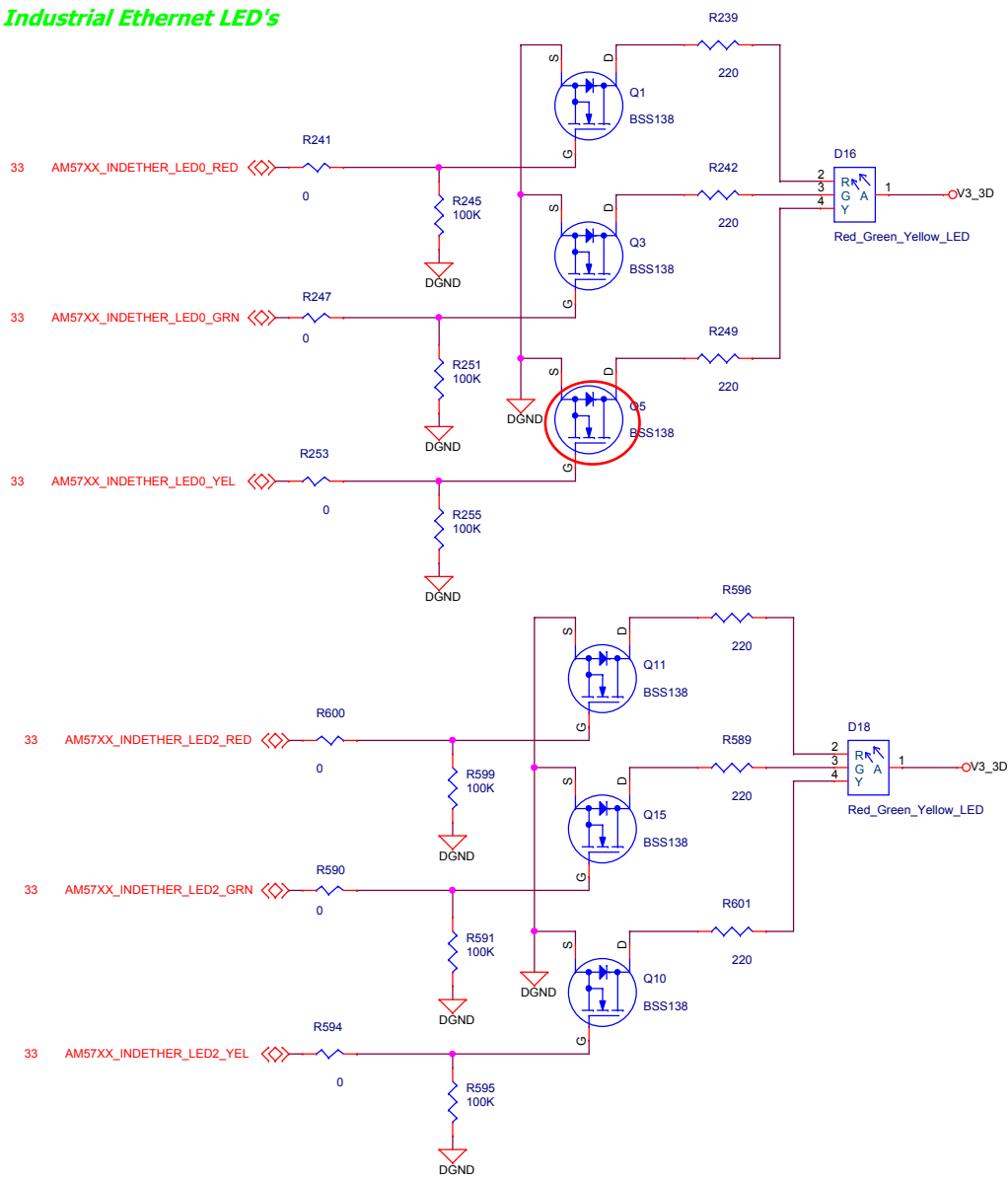


Note: MDIO Address = 0x01

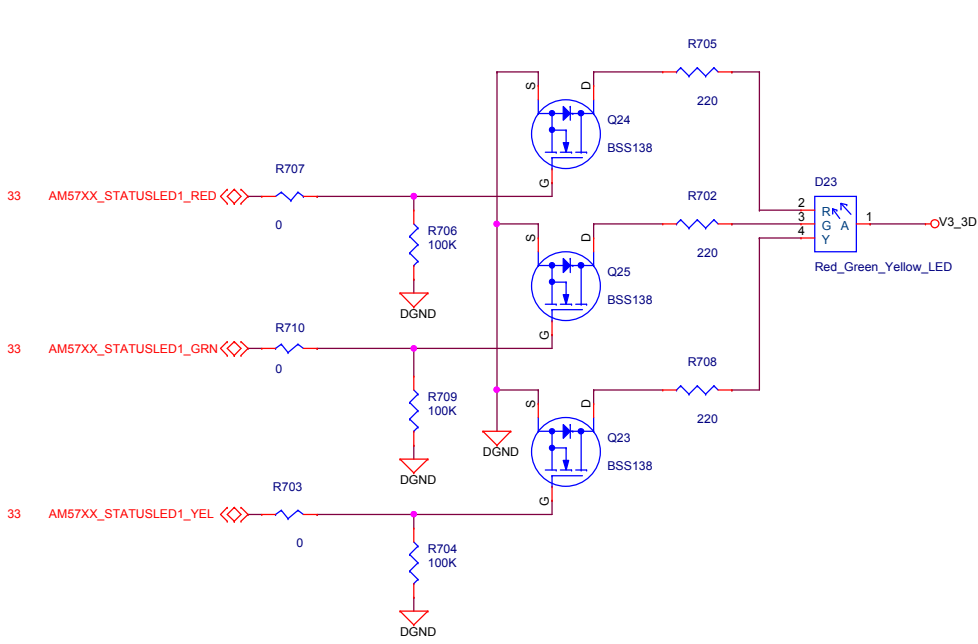
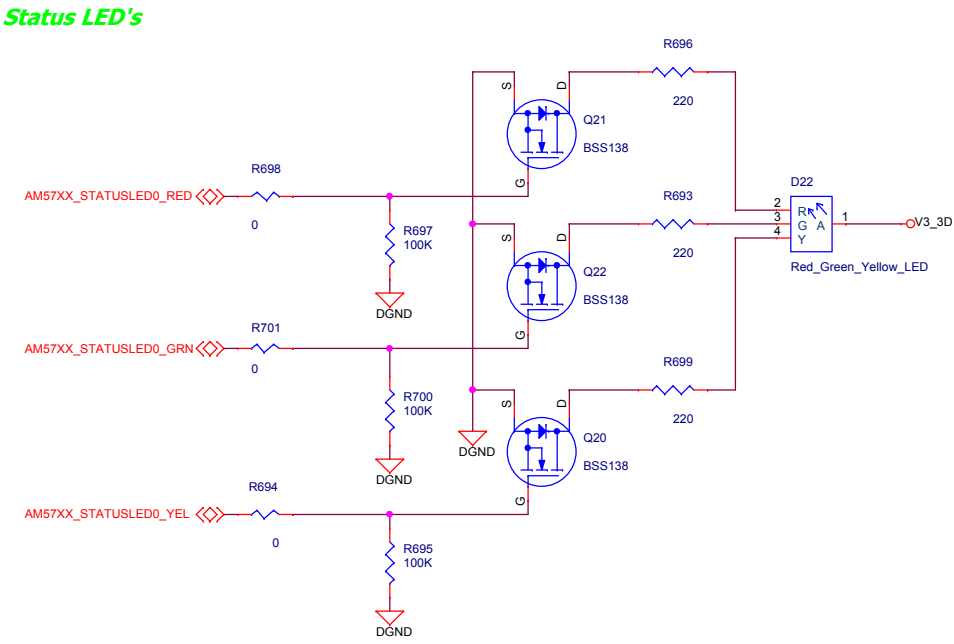




Industrial Ethernet LED's



Status LED's

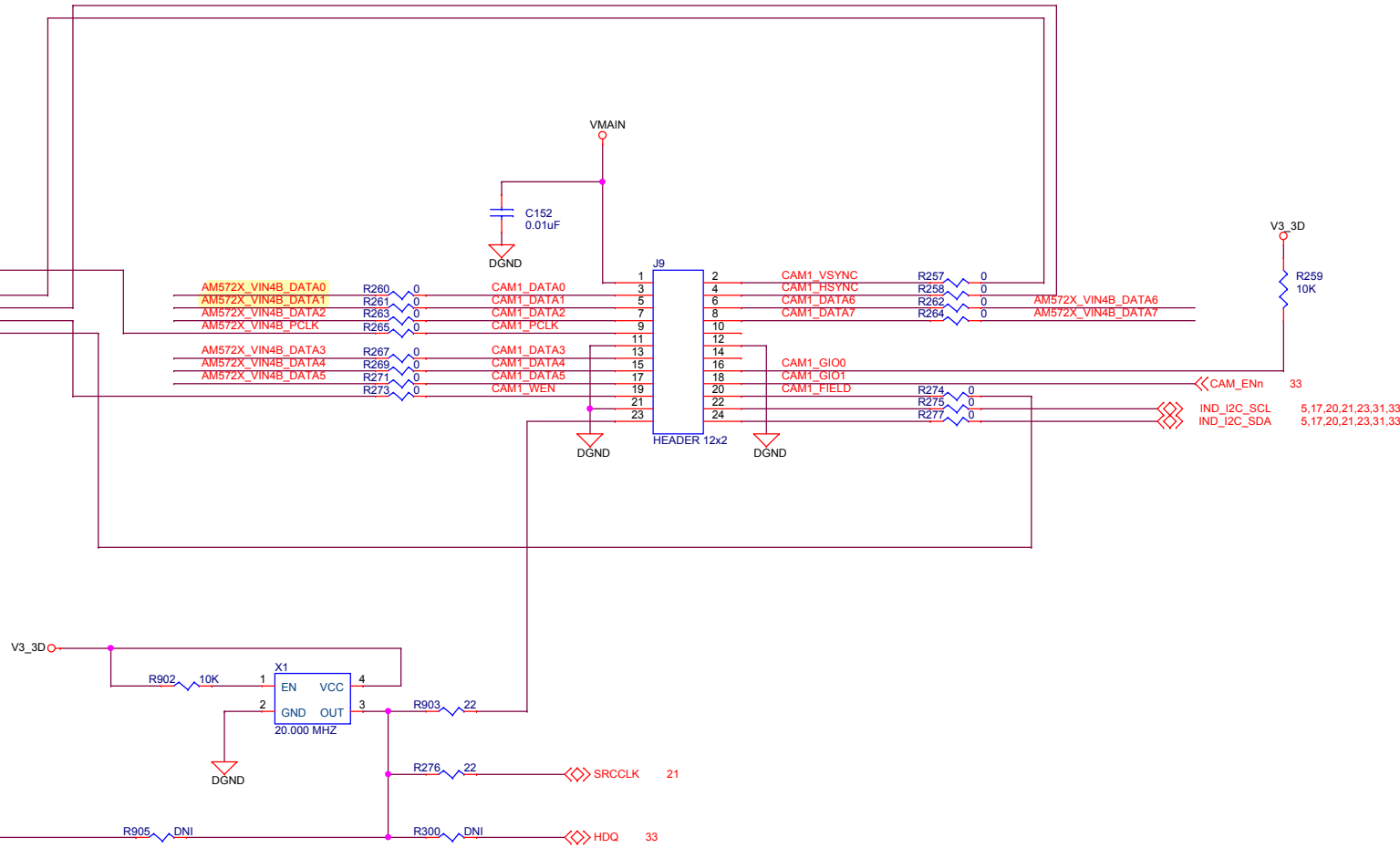


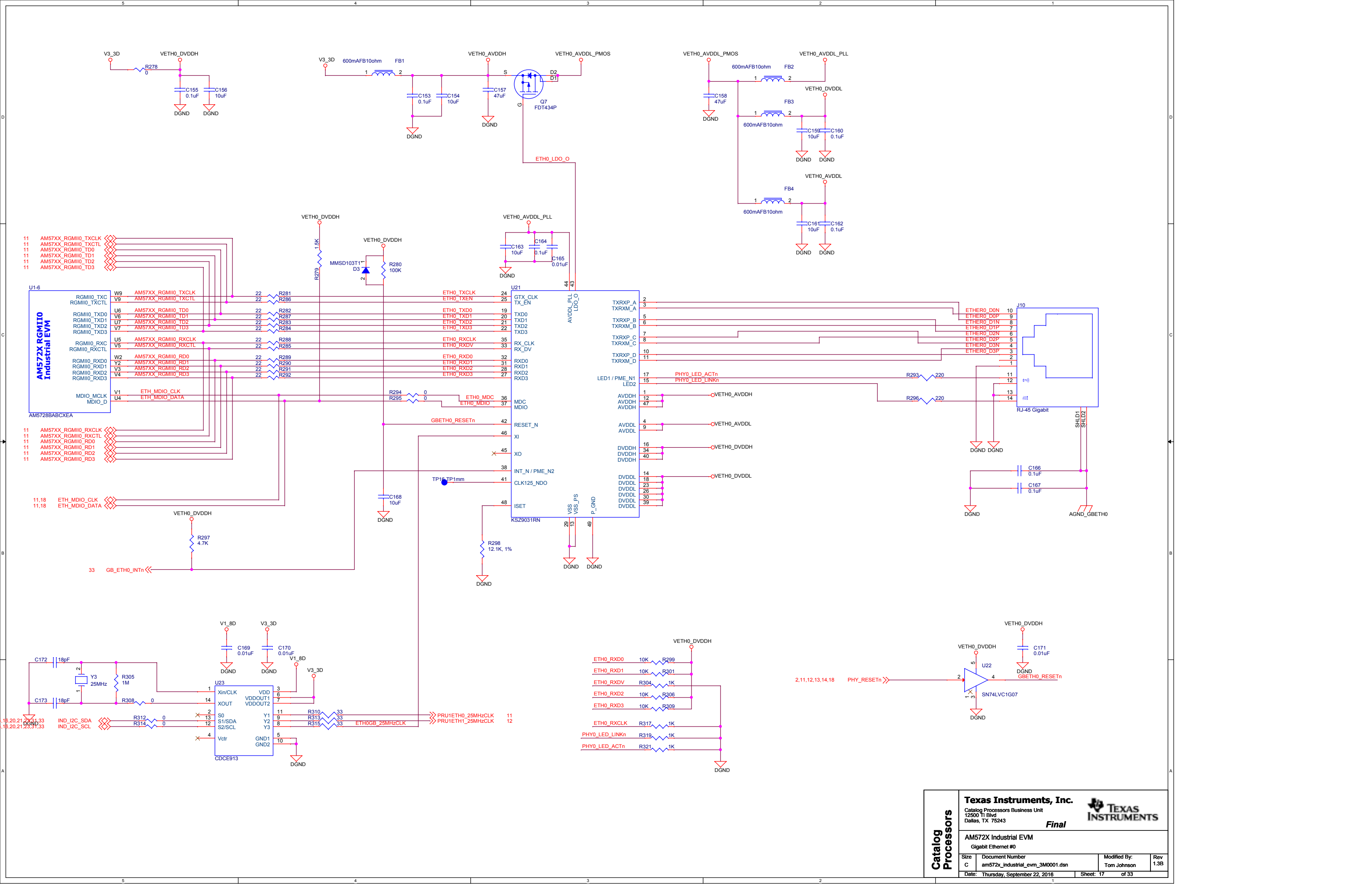
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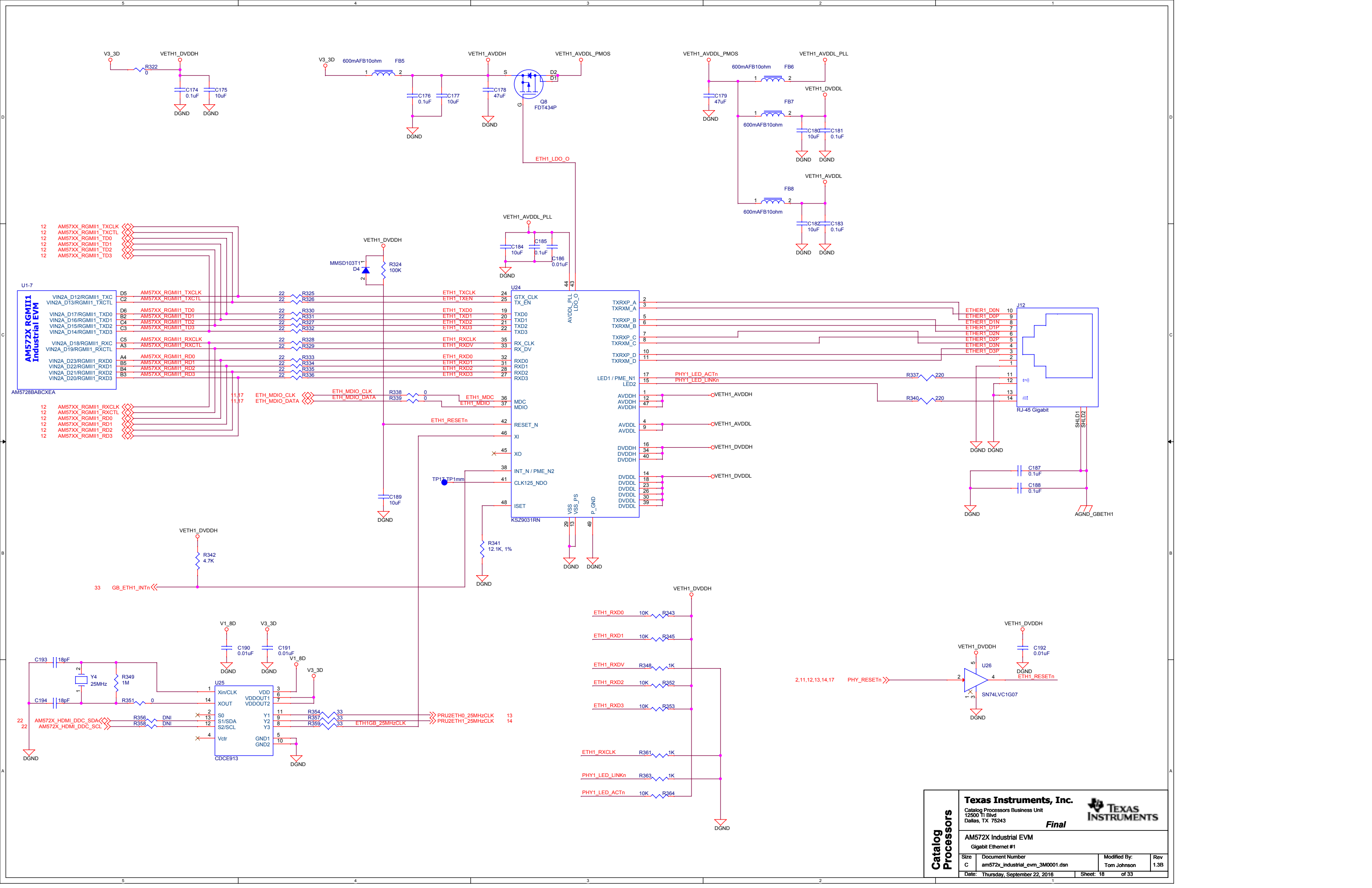
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Industrial EVM**

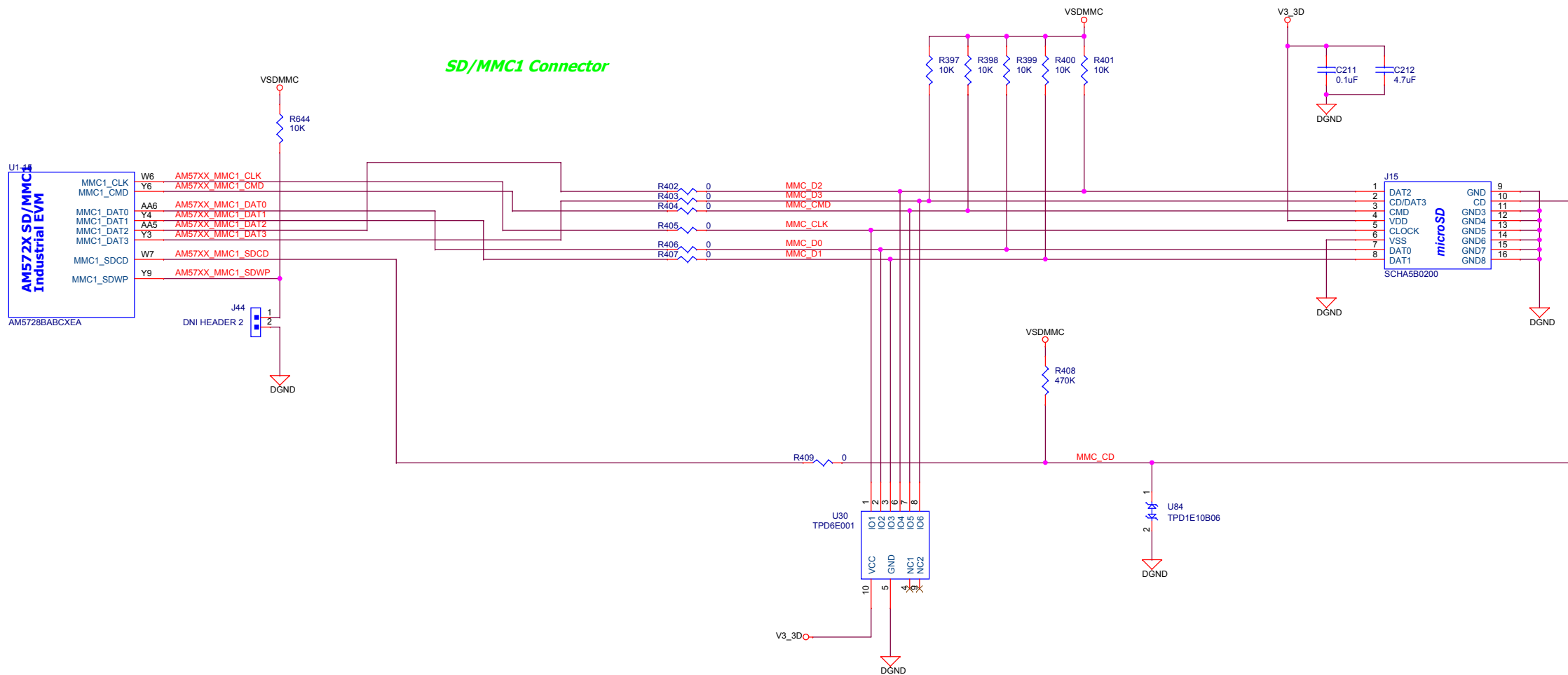
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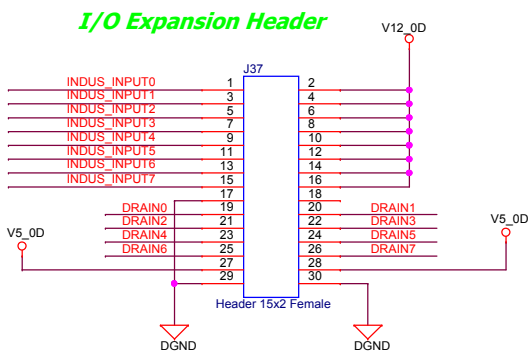
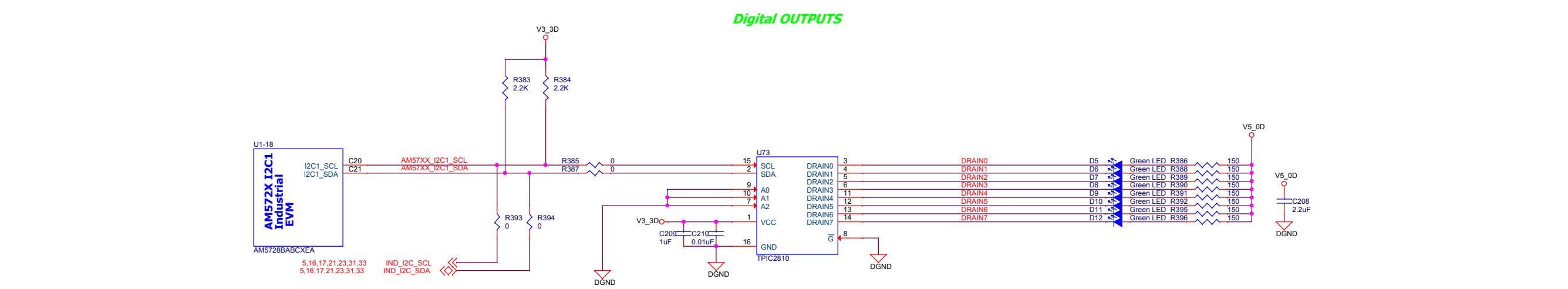
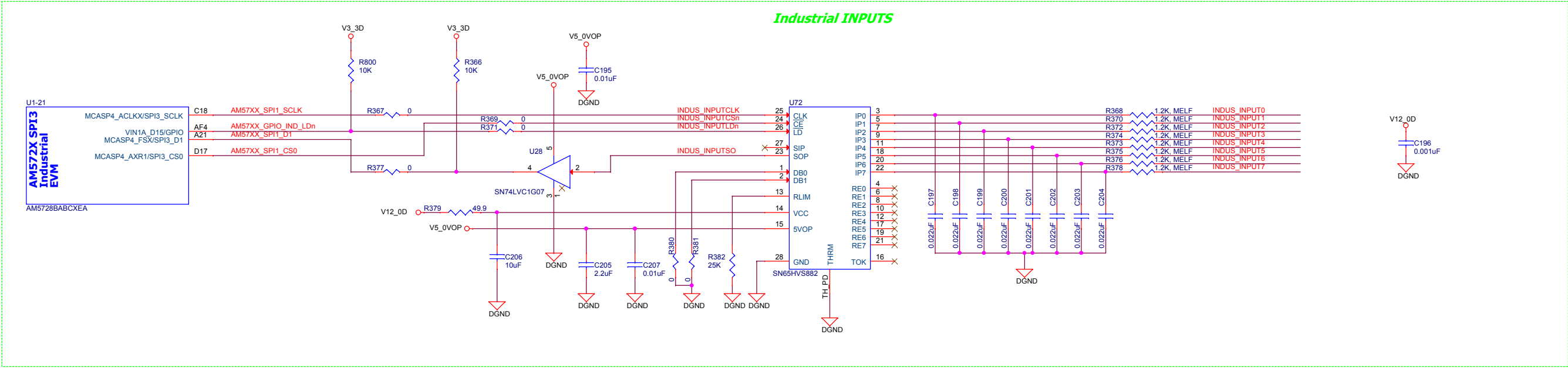
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GPMC_A8/VIN4B_HSYNC1	N7	AM572X_VIN4B_HSYNC
GPMC_A11/VIN4B_DE1	P9	AM572X_VIN4B_DE
GPMC_A12/VIN4B_FLD1	P4	AM572X_VIN4B_FLD
GPMC_A0/VIN4B_D0	R6	AM572X_VIN4B_DATA0
GPMC_A1/VIN4B_D1	T9	AM572X_VIN4B_DATA1
GPMC_A2/VIN4B_D2	T6	AM572X_VIN4B_DATA2
GPMC_A3/VIN4B_D3	T7	AM572X_VIN4B_DATA3
GPMC_A4/VIN4B_D4	P6	AM572X_VIN4B_DATA4
GPMC_A5/VIN4B_D5	R9	AM572X_VIN4B_DATA5
GPMC_A6/VIN4B_D6	R5	AM572X_VIN4B_DATA6
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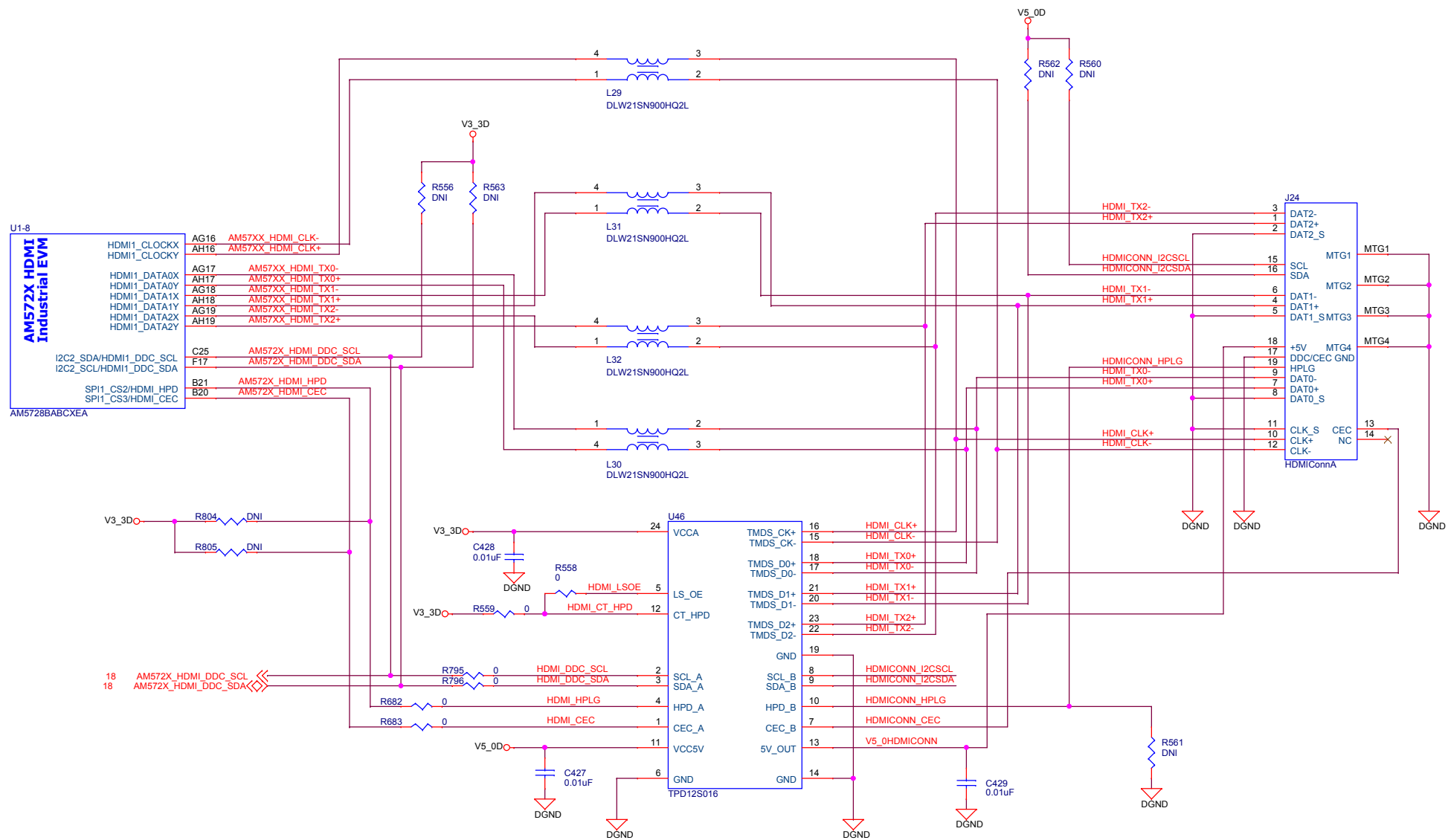














Catalog Processors

Texas Instruments, Inc.

Catalog Processors Business Unit
12500 TI Blvd
Dallas, TX 75243

Final

AM572X Industrial EVM

Debug/Configuration

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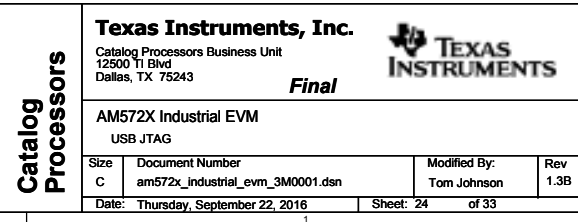
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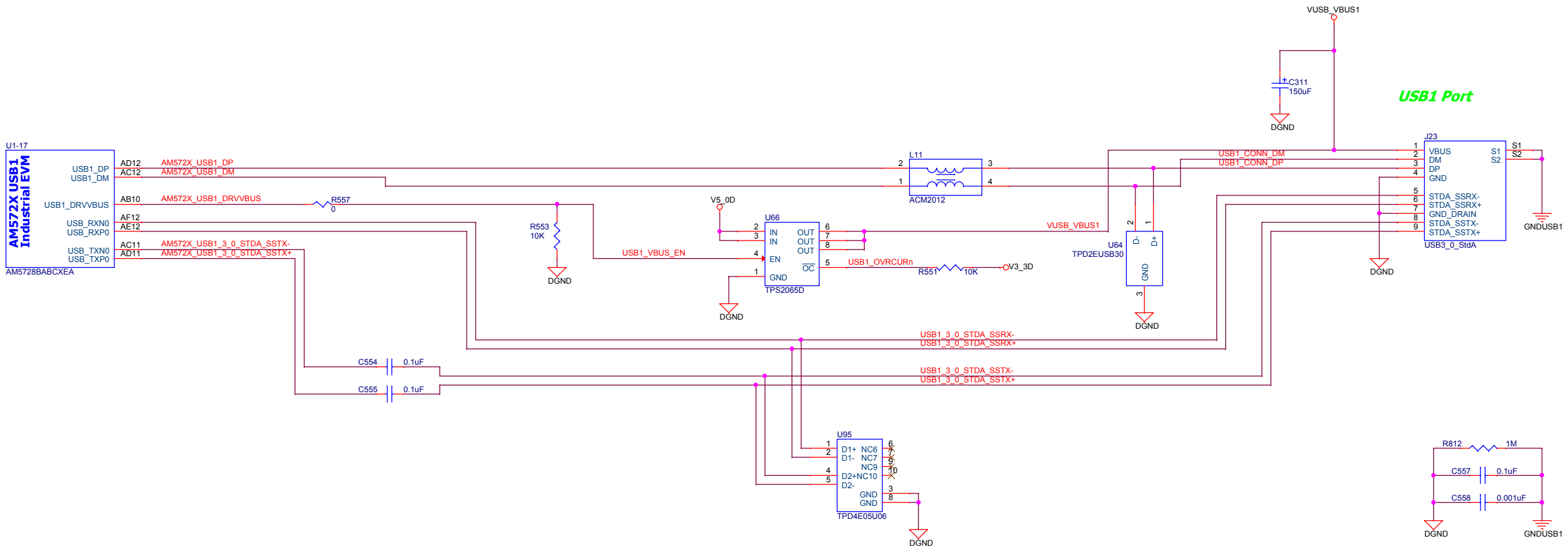
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Tom Johnson

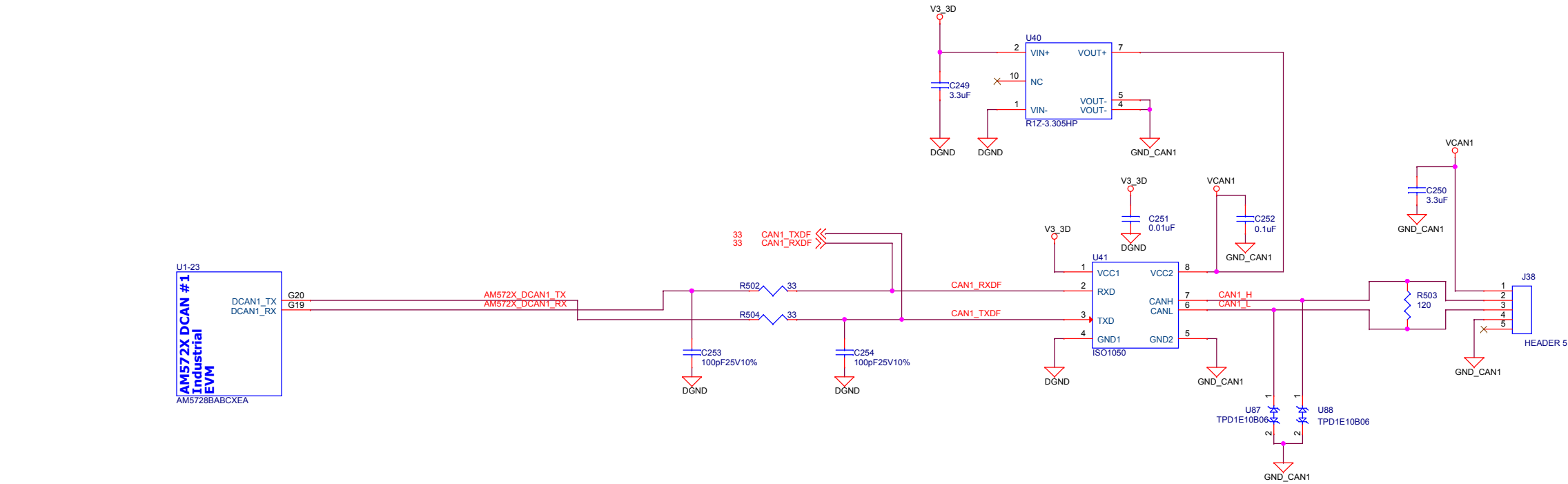
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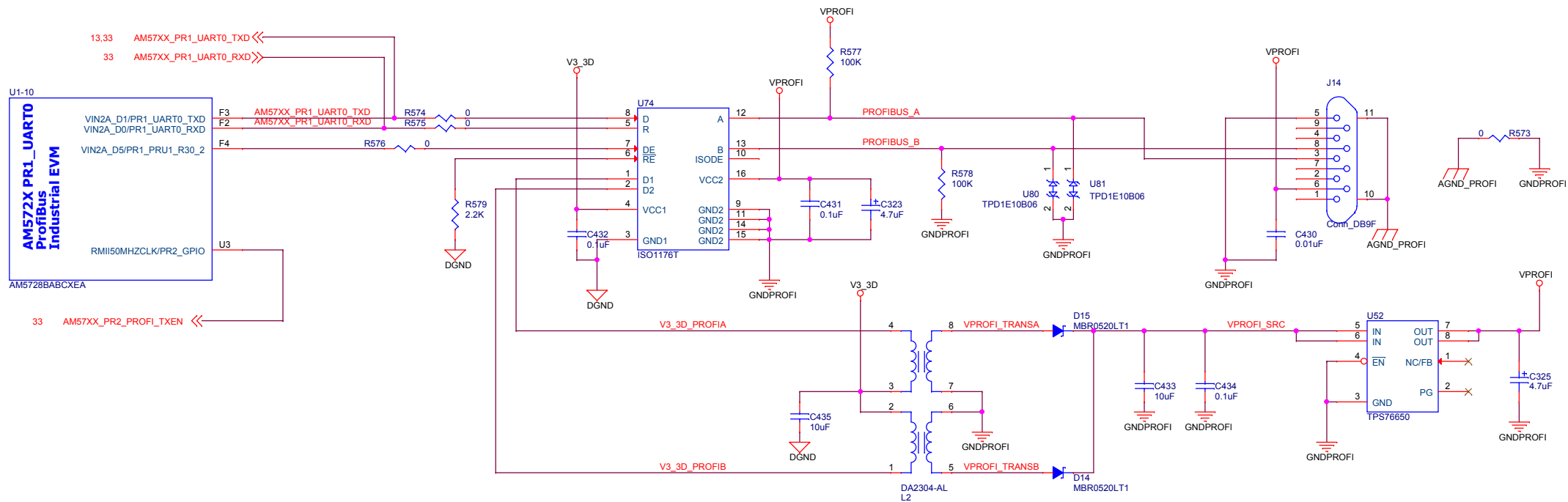
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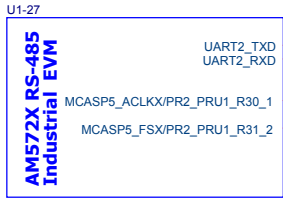




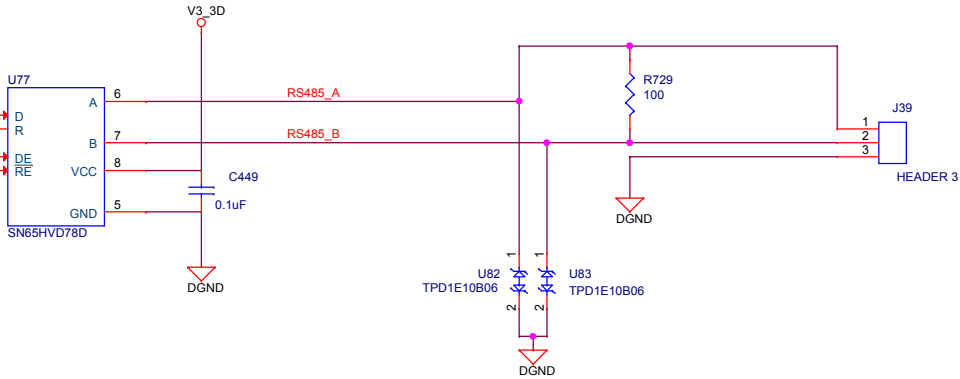




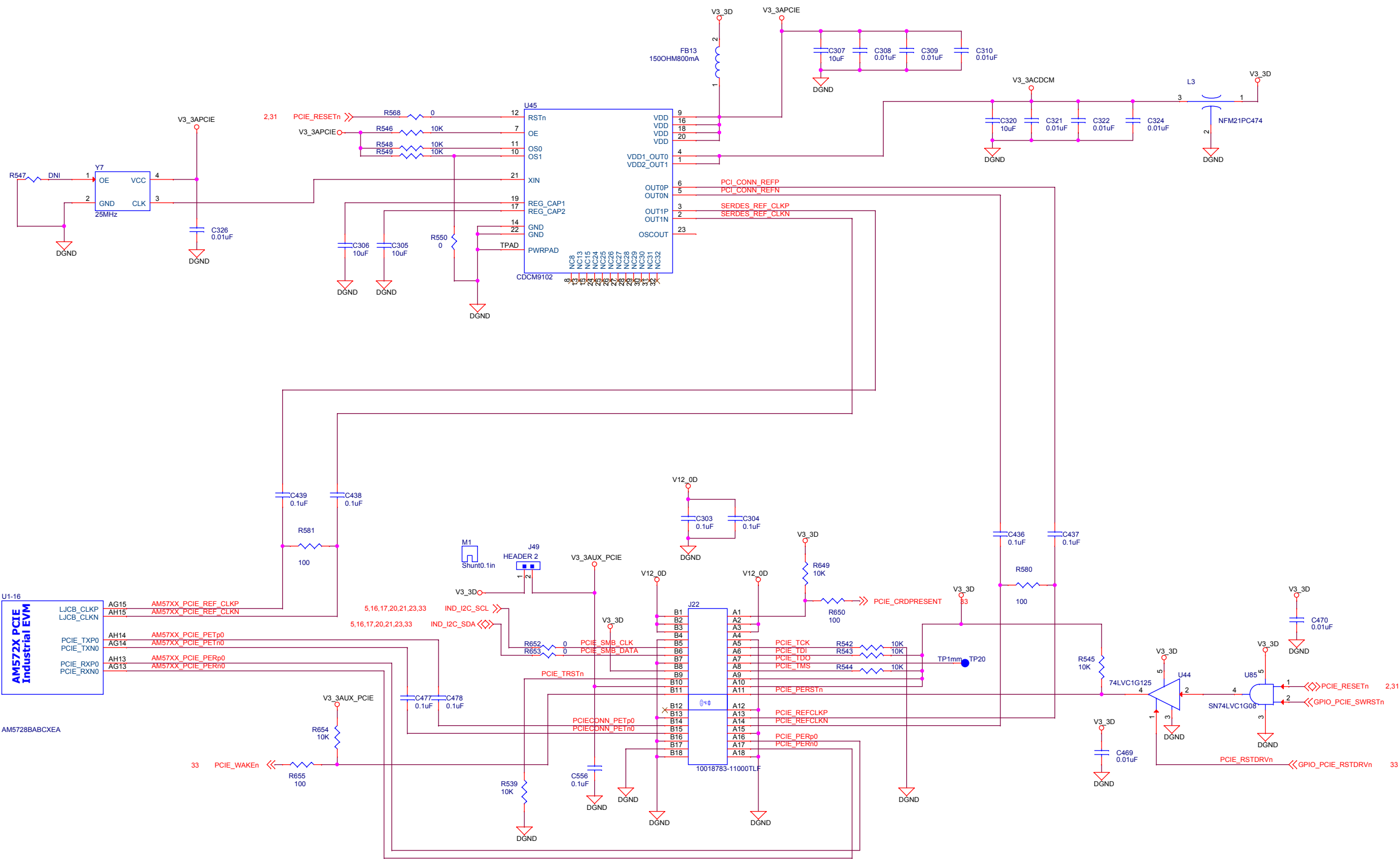


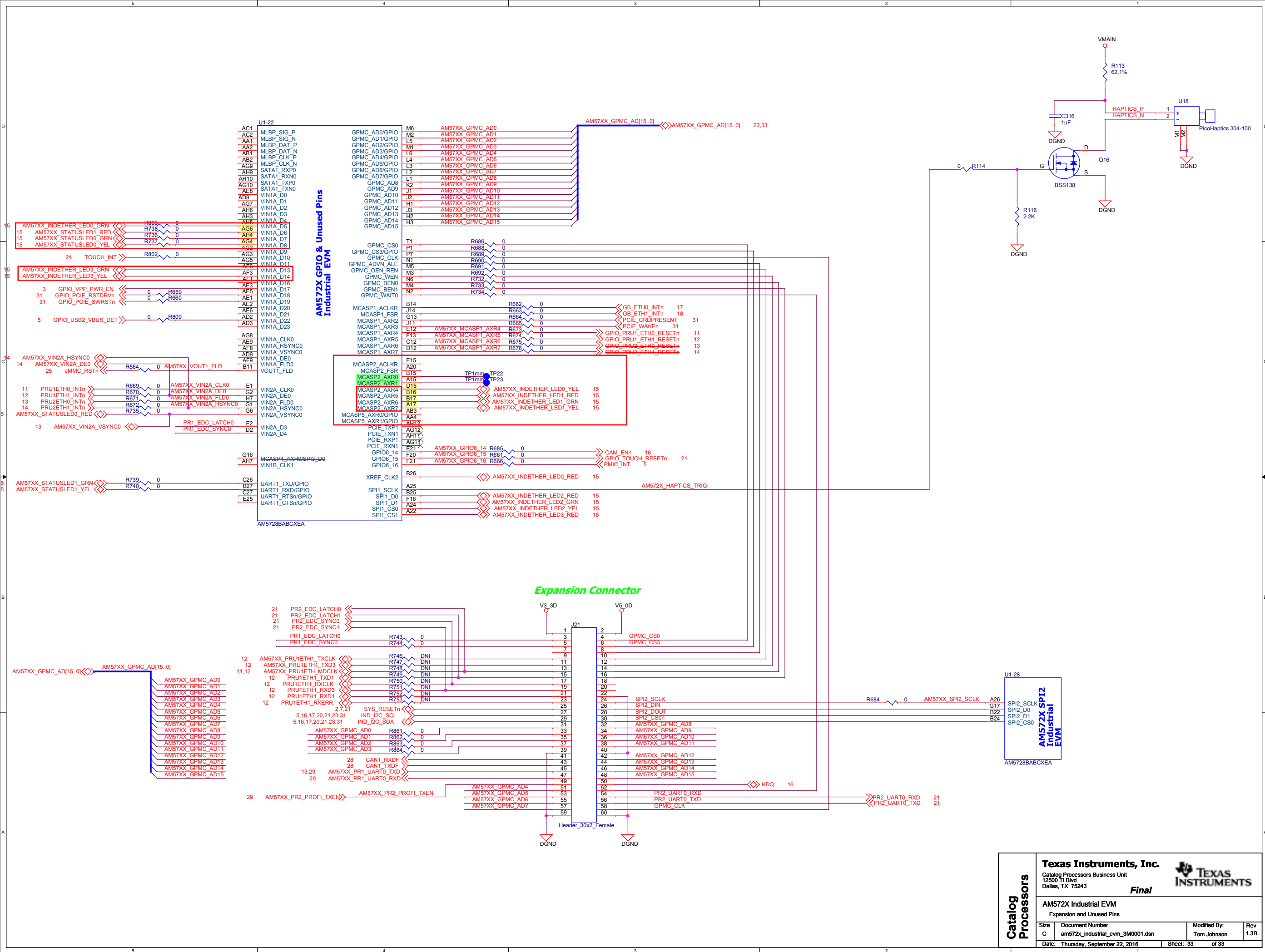


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