



AM572x IDK Modification List

This document shows the modifications that should be made to the AM572x IDK to enable 1PPS.

Revision	1.3B
Modification Descriptions	<p><i>timer16: C23 XREF CLK3</i></p> <ul style="list-style-type: none"> Remove R905, if installed (<i>should be DNI by default</i>) Solder fly-wire to the R905 pad that's closest to AM572x #Other side of wire can be connected to J21 or probed <p><i>timer16: AB10 USB1_DRVVBUS (USB1_VBUS_EN)</i> Solder fly-wire to the R553 pad that's connected to U66-4 #Other side of wire can be probed</p> <p>Timer15: AC10 (input): USB2_DRVVBUS (USB2_VBUS_EN)</p> <ul style="list-style-type: none"> Solder fly-wire to the R787 pad that's closest to AM572x #Other side of wire can be connected to J21 or probed <p><i>pr2_edc_sync0_out: E7 VOUT1_D7</i></p> <ul style="list-style-type: none"> Remove R611 Populate R908 with 0 ohm resistor to J21-17 <p><i>pr2_edc_latch0_in: F9 VOUT1_D5</i></p> <ul style="list-style-type: none"> Remove R610 Populate R910 with 0 ohm resistor to J21-13 <p><i>pr2_edc_sync1_out: E8 VOUT1_D8</i></p> <ul style="list-style-type: none"> Remove R613 Populate R909 with 0 ohm resistor to J21-19 <p><i>pr2_edc_latch1_in: F8 VOUT1_D6</i></p> <ul style="list-style-type: none"> Remove R612 Populate R911 with 0 ohm resistor to J21-15
Sync / Latch availability on headers	<ul style="list-style-type: none"> pr2_edc_sync0_out → J21, pin 17 pr2_edc_latch0_out → J21, pin 13 pr2_edc_sync1_out → J21, pin 19 pr2_edc_latch1_out → J21, pin 15