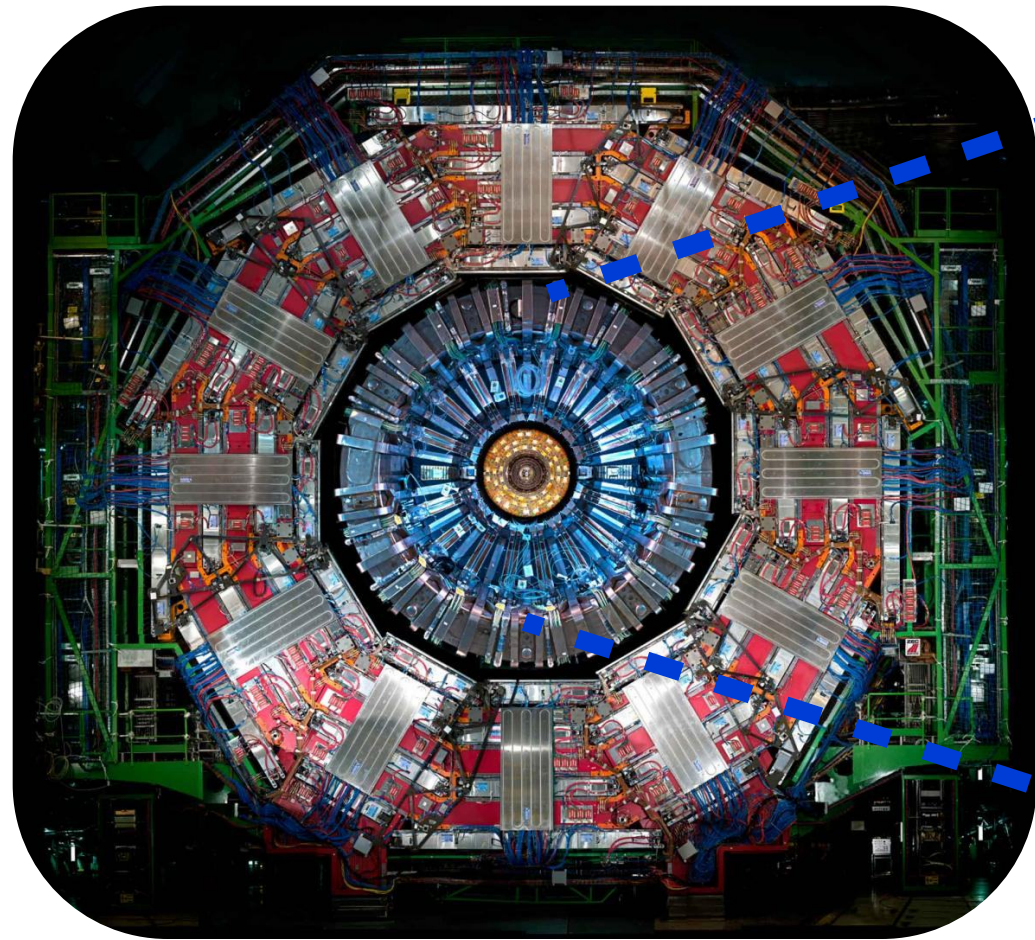
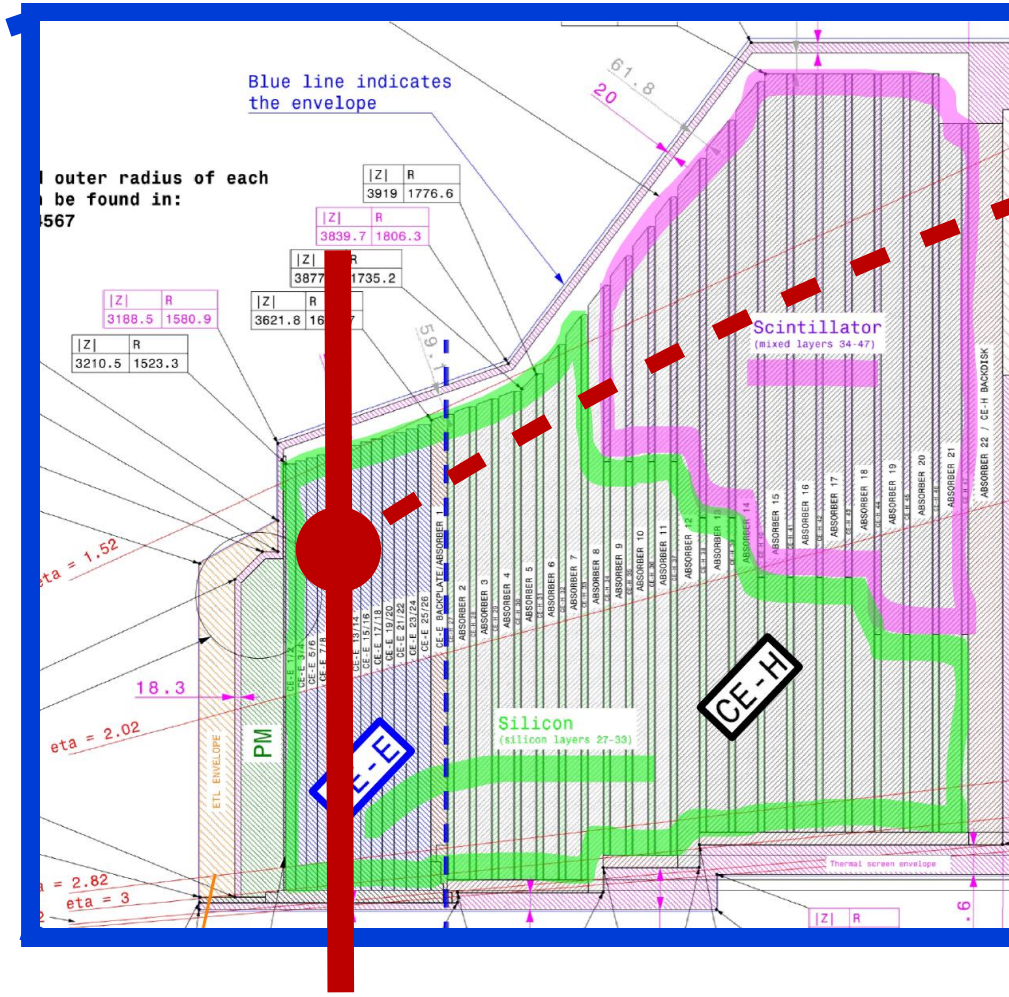


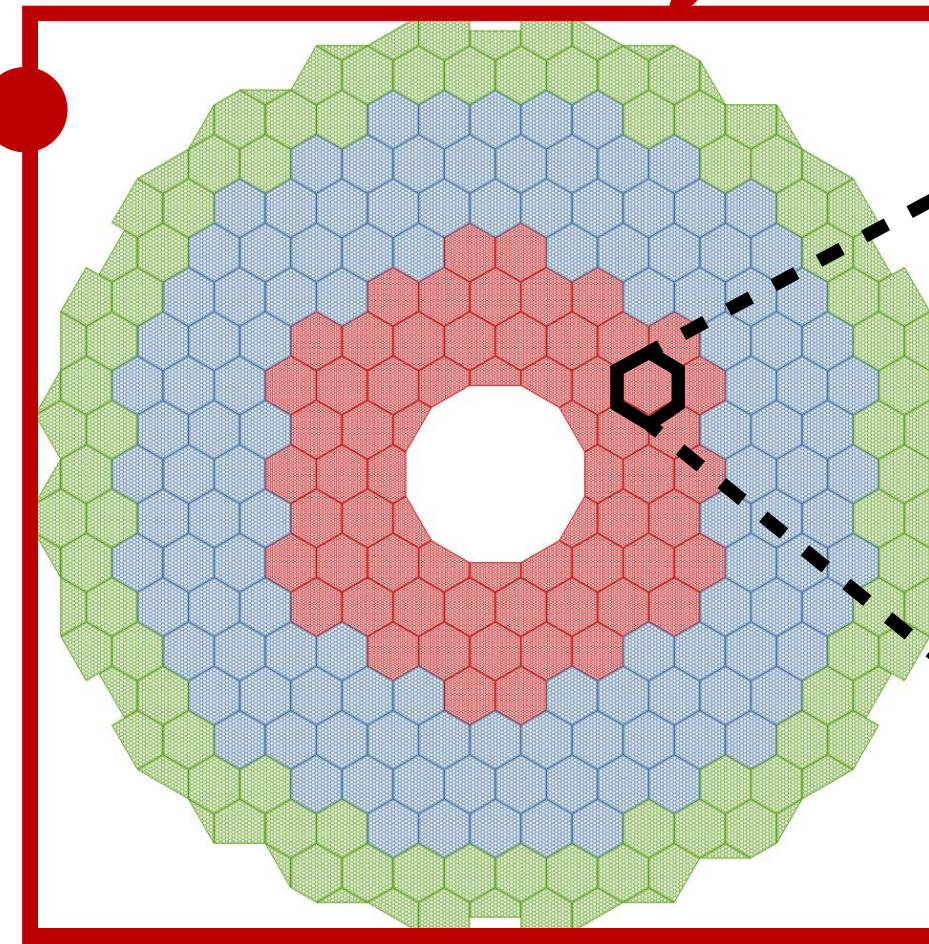
CMS



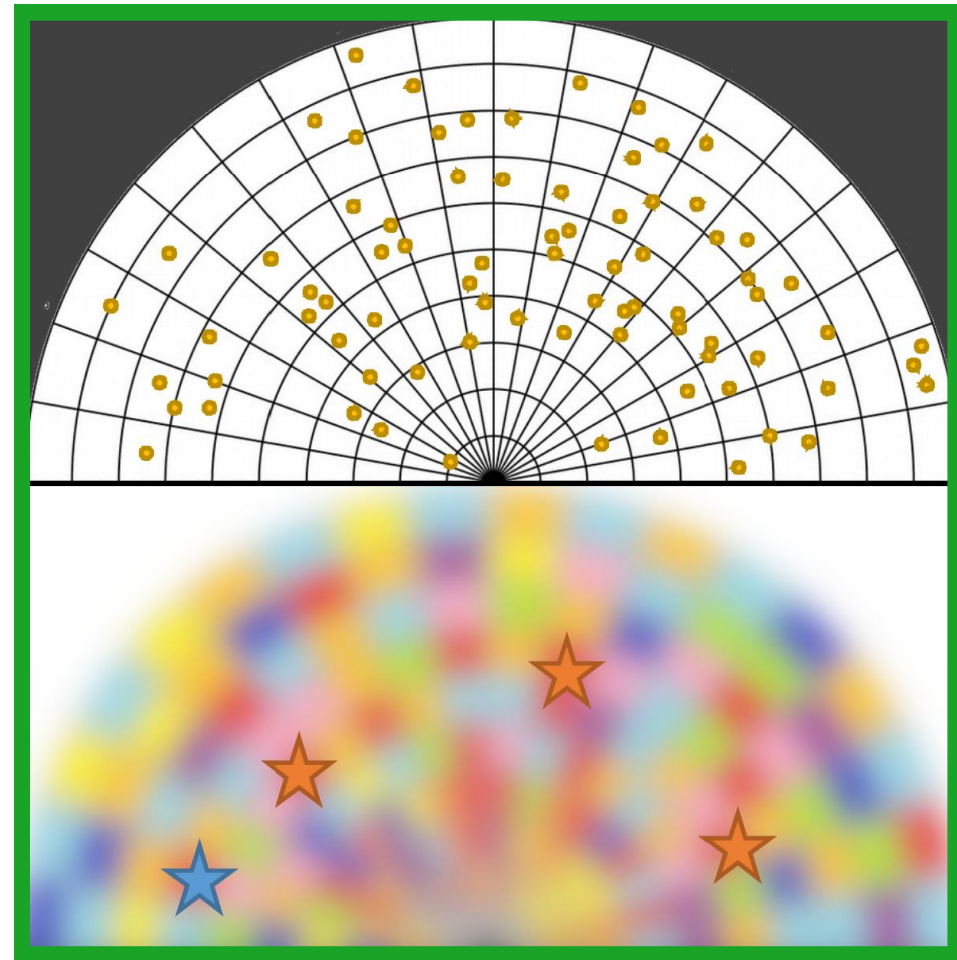
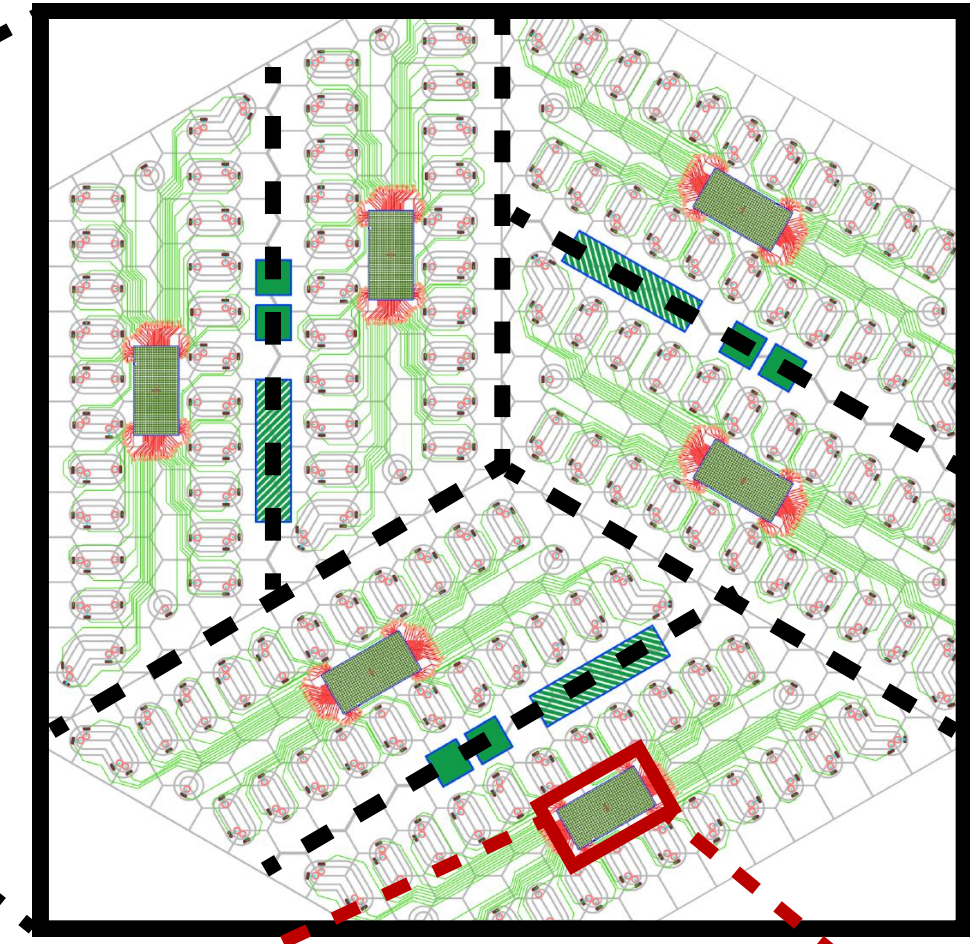
HGCAL



Silicon Layer



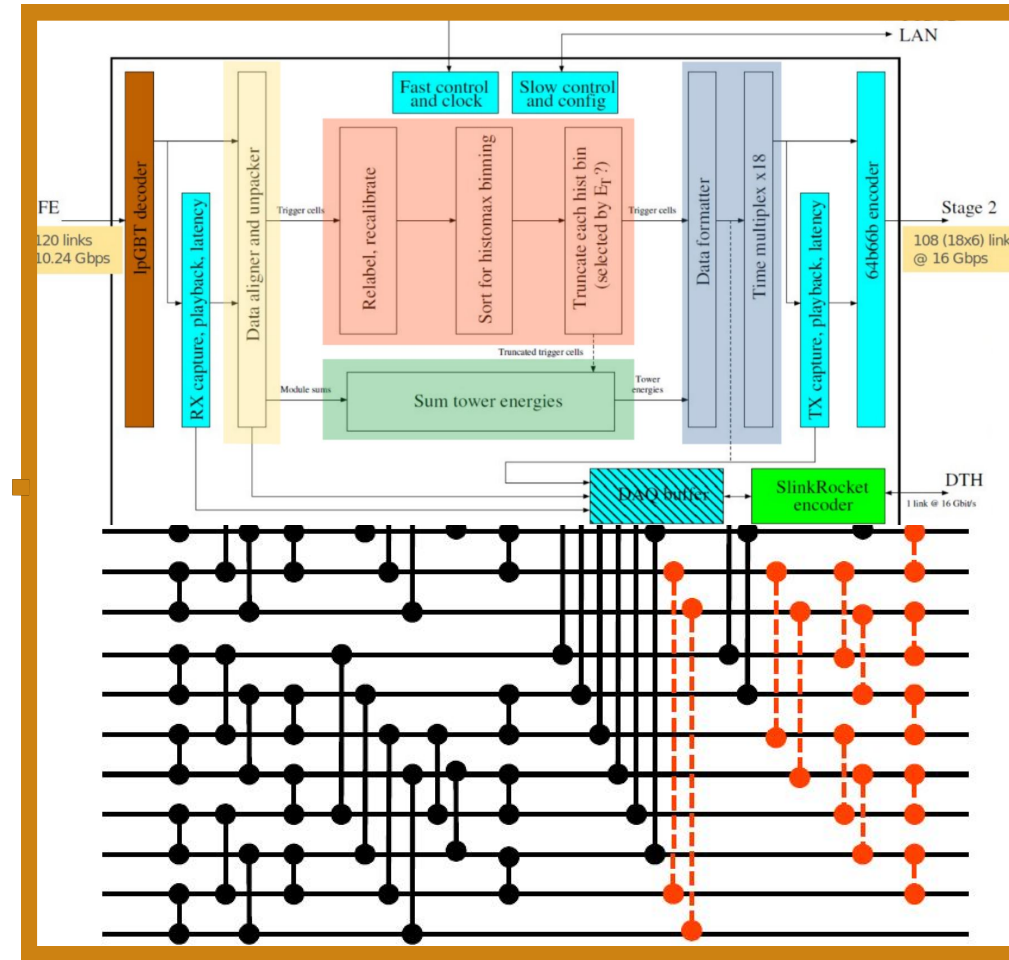
Hexaboard



Backend Stage 2

140 Tb/s

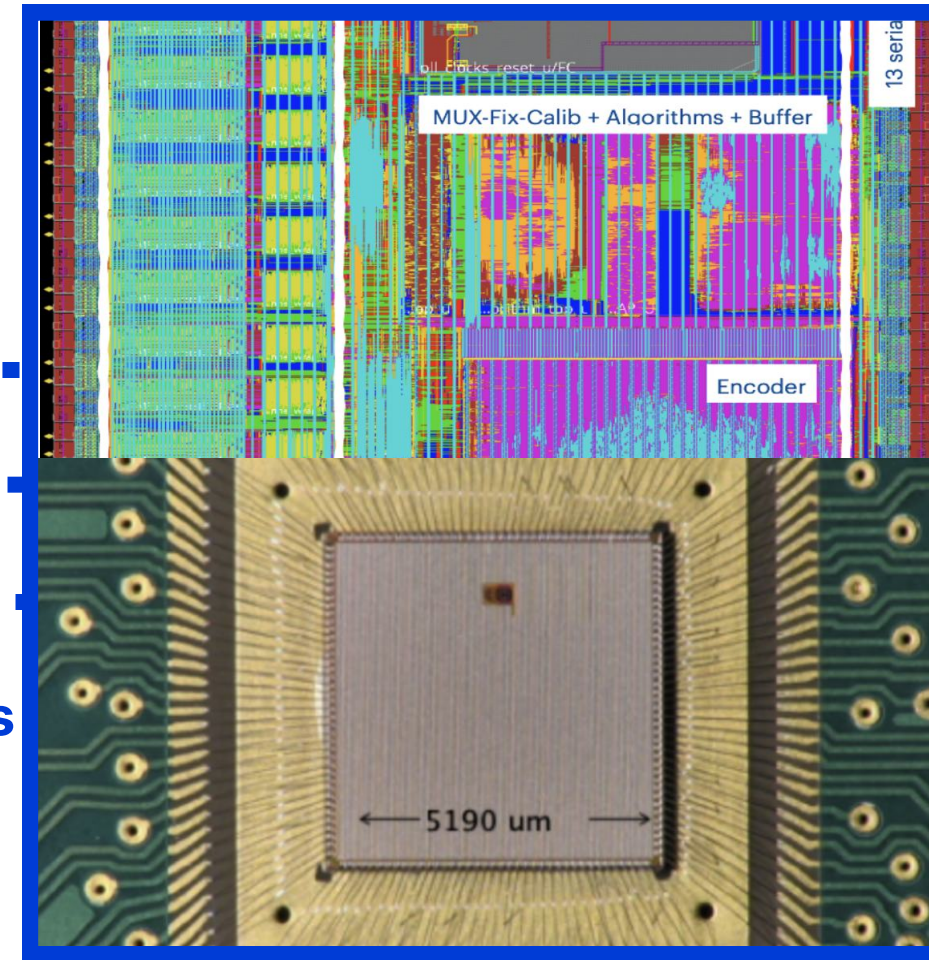
16Gb/s links



Backend Stage 1

90 Tb/s

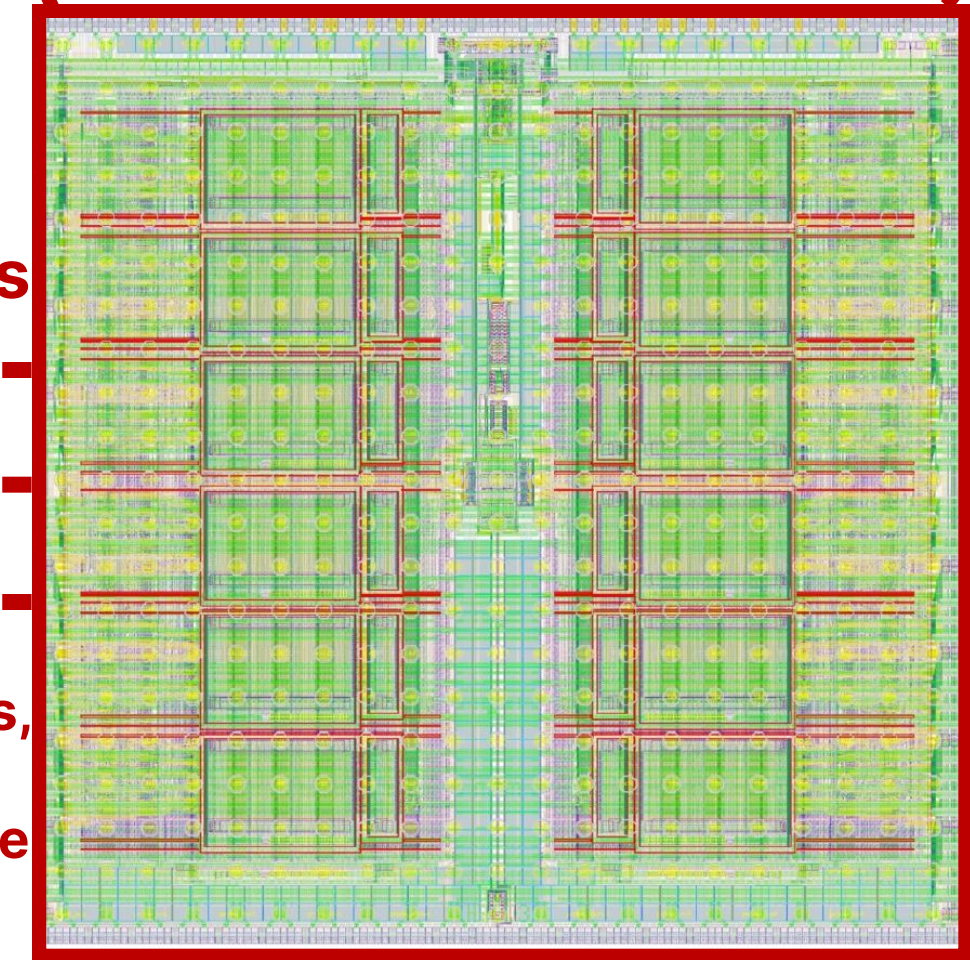
10Gb/s links



ECON-T

300 Tb/s

1Gb/s links,
15Gb/s
per module



HGCROC

trigger primitives
7 Tb/s, 25Gb/s links

Level 1 Trigger

... readout ...

HLT

Physics Analysis