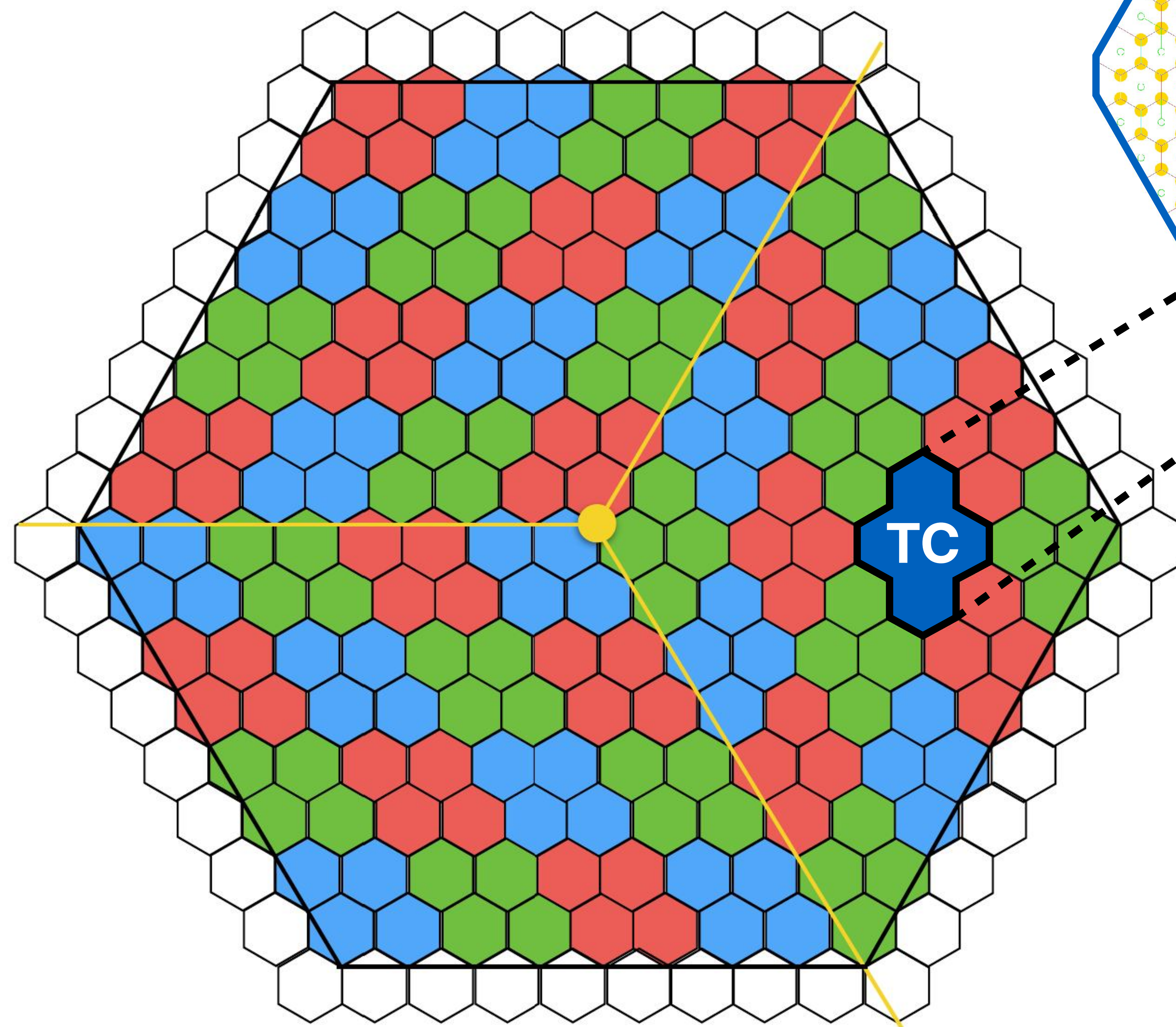
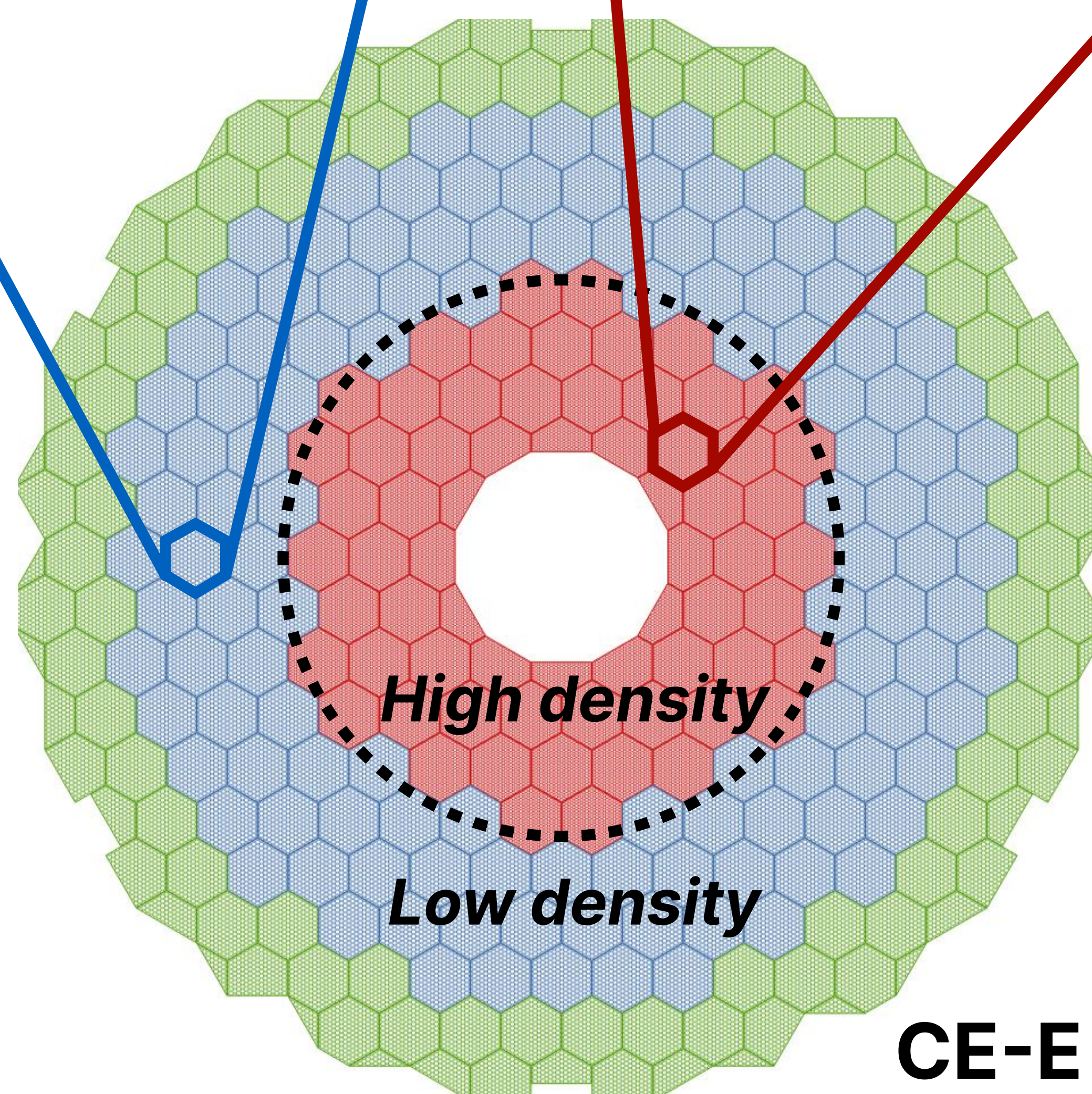
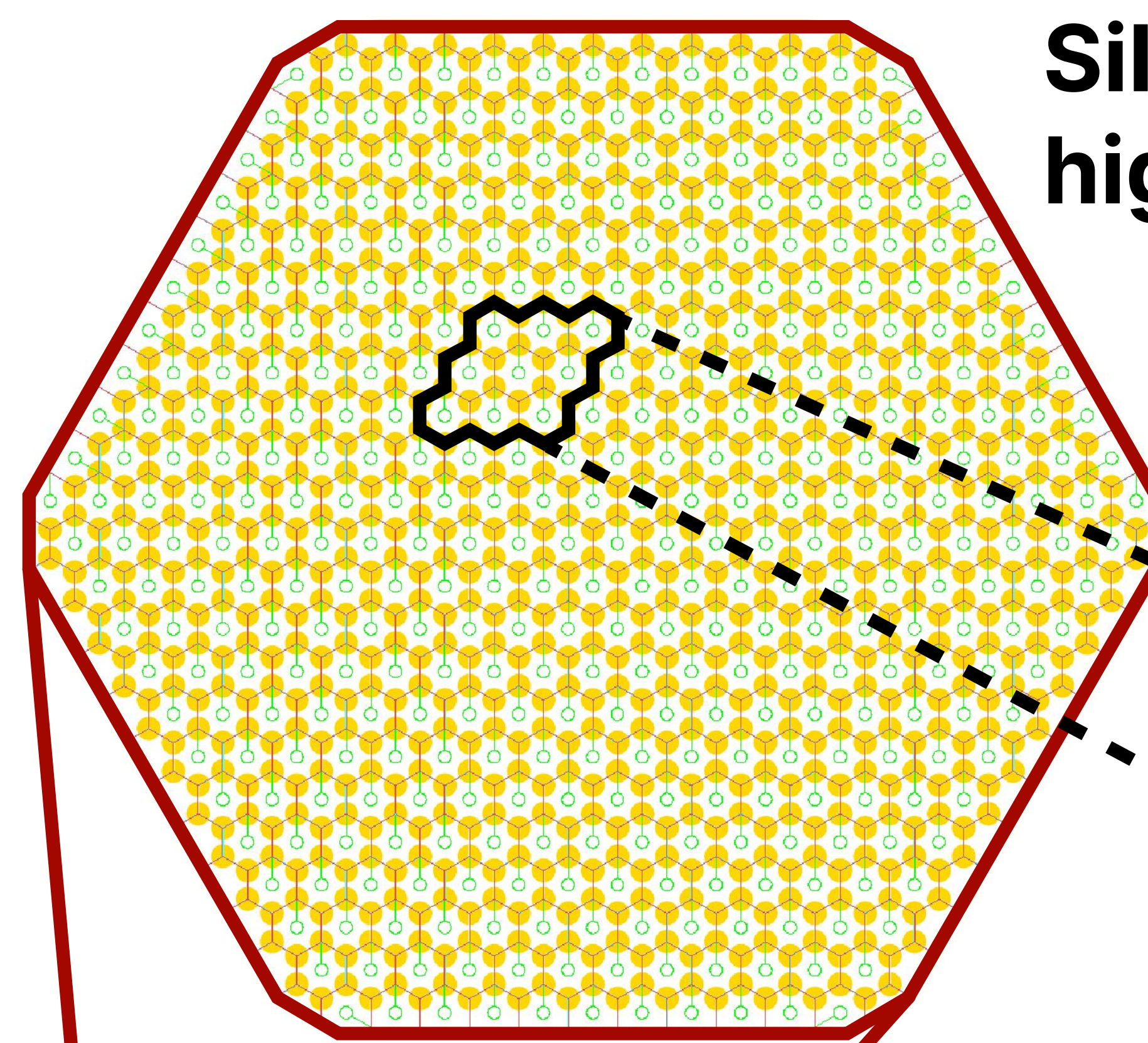
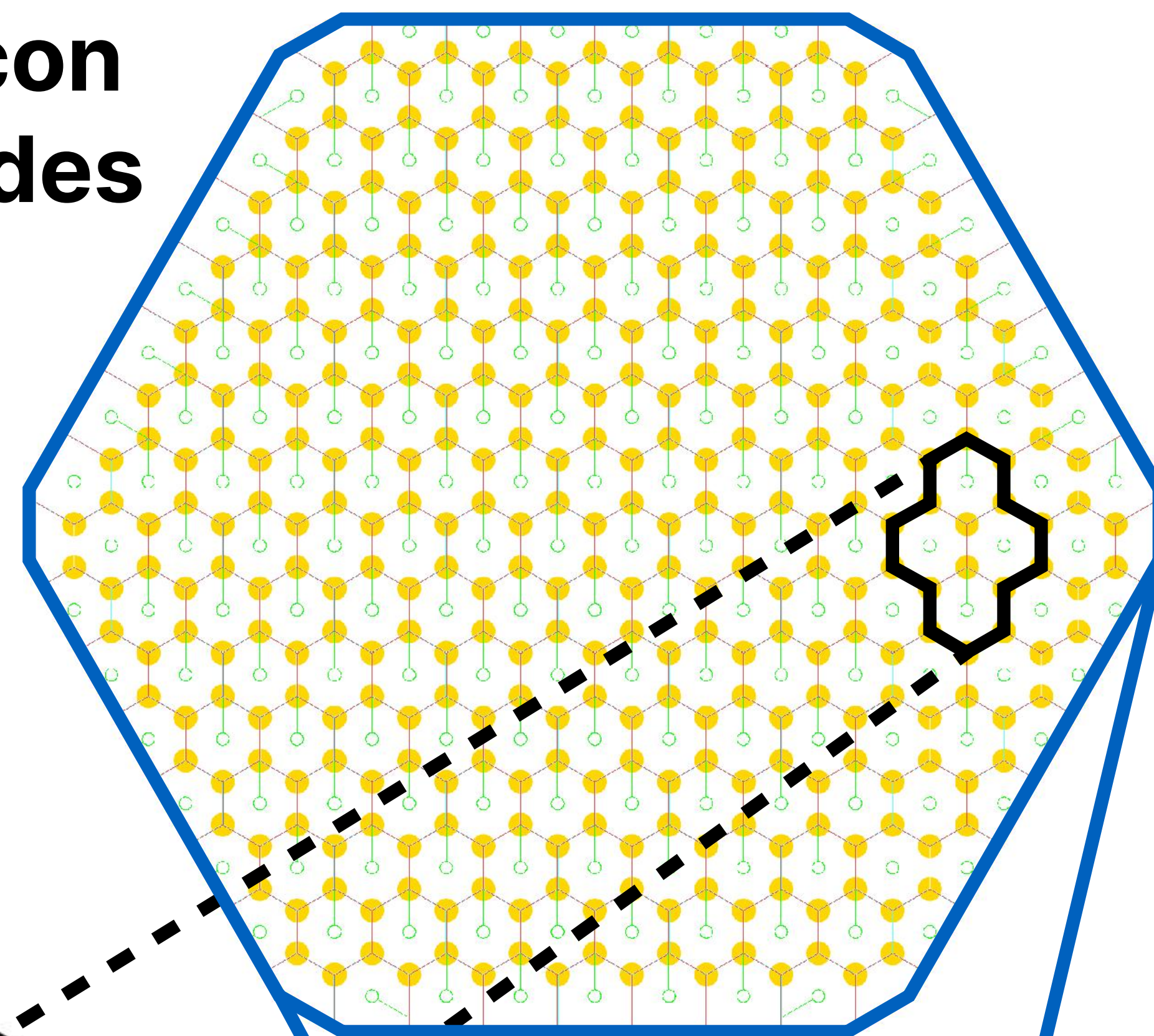


**Silicon
low-density diodes**

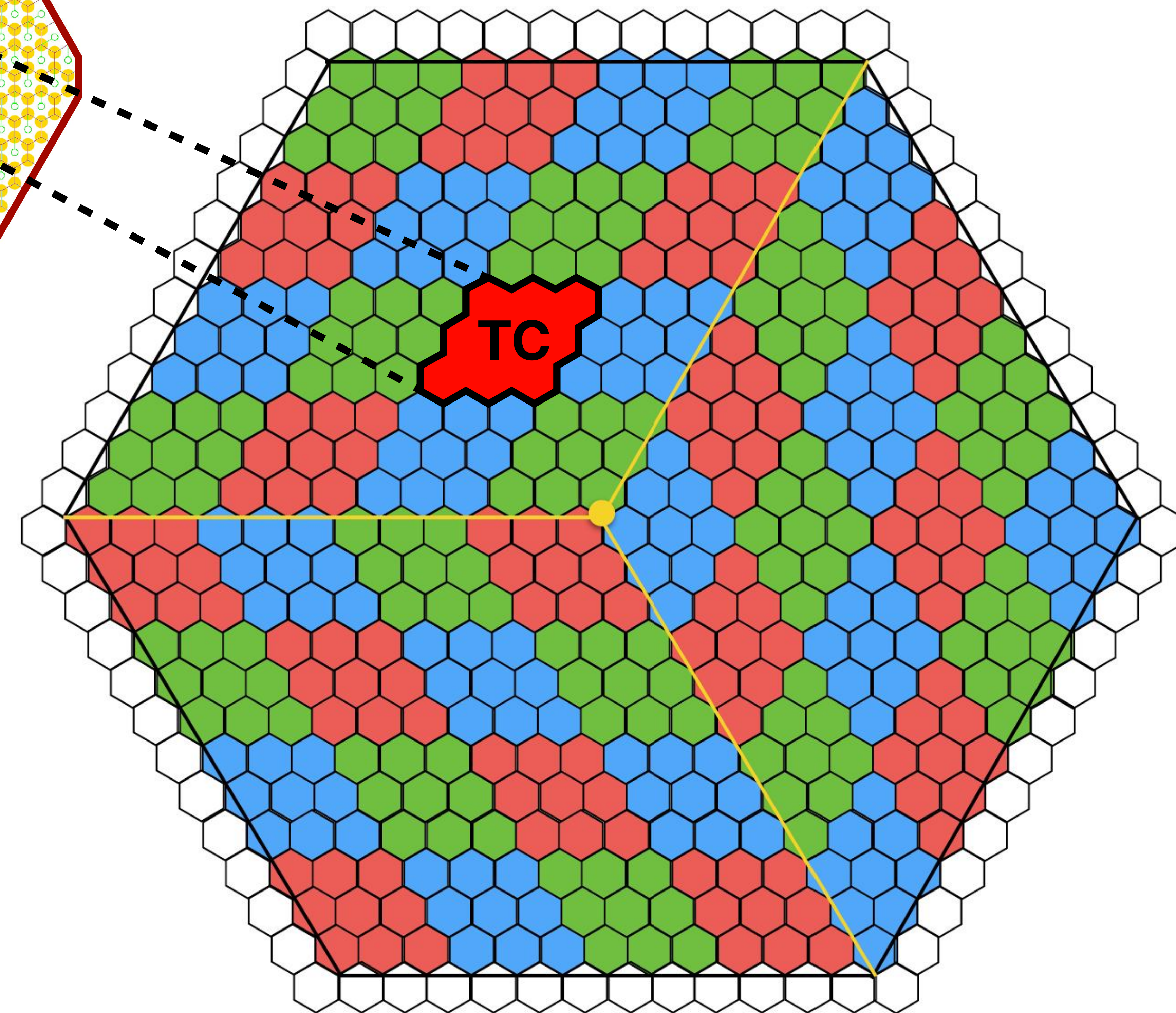
**Silicon
high-density diodes**



4 cells per TC



CE-E silicon layer



9 cells per TC