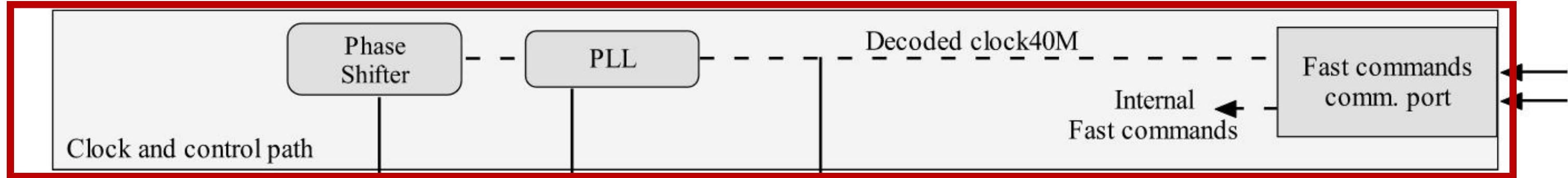
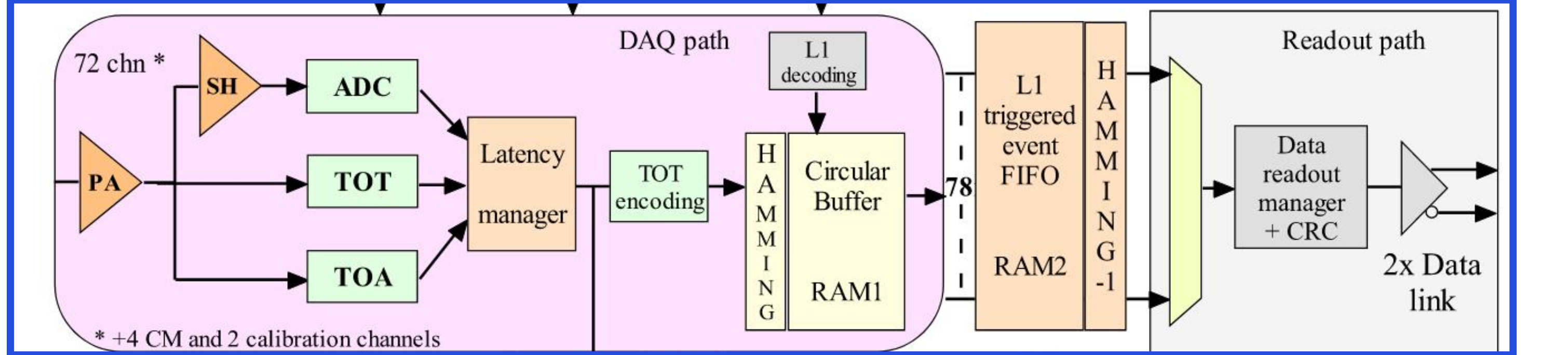


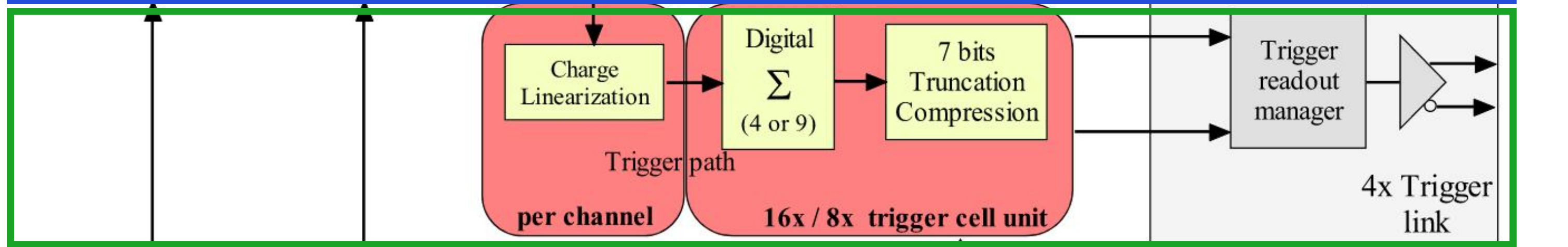
**PLL
(phase-locked
loop)**



**Data
Path**



**Trigger
Path**



**I2C
(inter-
integrated
circuit)**

