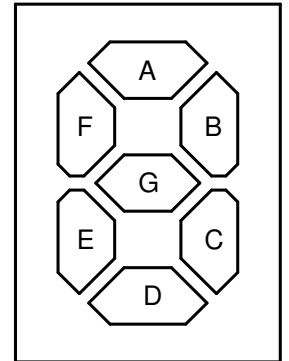
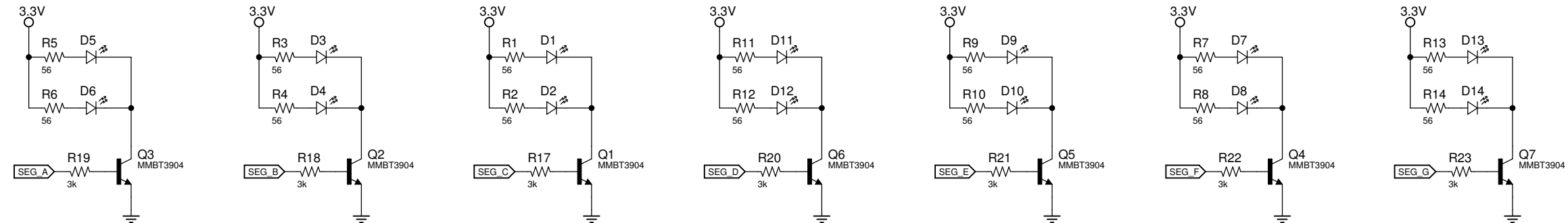
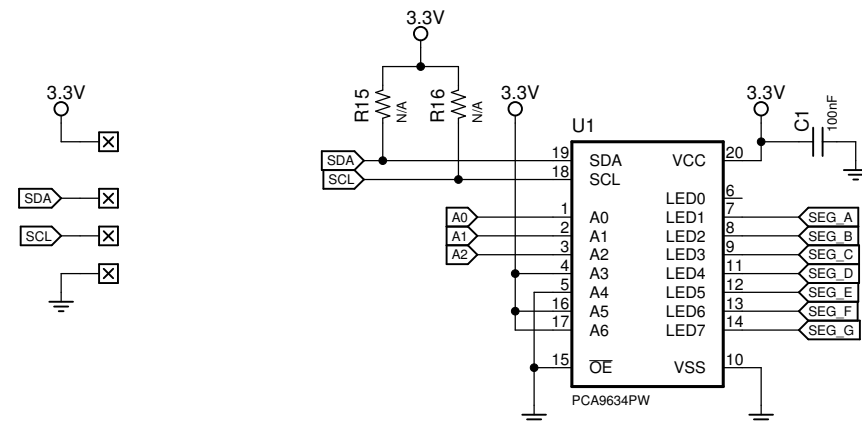
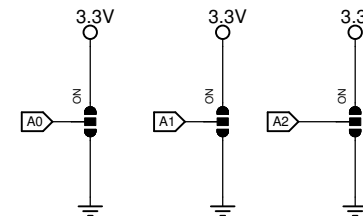


SEGMENTS



I2C ADDRESS SELECTION



					A2	A1	A0	R/W	
0xD0	1	1	0	1	0	0	0	X	DIGIT 1
0xD2	1	1	0	1	0	0	1	X	DIGIT 2
0xD4	1	1	0	1	0	1	0	X	DIGIT 3
0xD6	1	1	0	1	0	1	1	X	DIGIT 4
0xD8	1	1	0	1	1	0	0	X	DIGIT 5
0xDA	1	1	0	1	1	0	1	X	DIGIT 6
0xDC	1	1	0	1	1	1	0	X	DIGIT 7
0xDE	1	1	0	1	1	1	1	X	DIGIT 8

BENOIT FRIGON
bfrigon@gmail.com

PROJECT
Alarm Clock (Model B)

TITLE	7 Segment display
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REV
A

PART #
CLK-PC-05

DOCUMENT #
CLK-SCH-05

SIZE B

SHEET
1 OF 1

LAST MOD: not saved!

APVD BY:	>APVD
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