





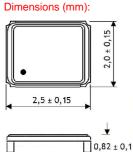


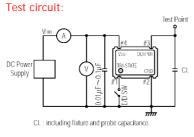


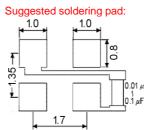
## Clock Oscillator SMD-version

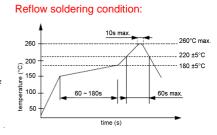
+1,8 / +2,5 / +3,0 / +3,3V

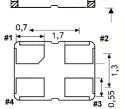
Clock Cacillator	T1,07 +2,37 +3,07 +3,37		
model	KXO-V95		
frequency range	1,0 ~ 70,0 MHz		
frequency stability at -20° ~ + 70°C at -40° ~ + 85°C	± 50 ppm ± 100 ppm		
at -40° ~ +105°C	± 120 ppm		
operating temperature	standard -20° ~ + 70°C available -40° ~ + 85°C (=KXO-V95T) available -40° ~ +105°C (=KXO-V95E)		
storage temperature	-40° ~ +85°C		
symmetry	40% ~ 60% at 50% V <sub>DD</sub> level		
rise & fall time max.	10 ns (10% $V_{DD} \sim 90\% V_{DD}$ level)		
"O" level max.	VOL: 10% V <sub>DD</sub>		
"1" level min.	VOH: 90% V <sub>DD</sub>		
input voltage V <sub>DD</sub>	+1,8 ~ +3,3V ±5%		
tri-state control voltage (Pin#1)	VIH: $V_{DD}$ x 0,7 min. VIL: $V_{DD}$ x 0,3 max.		
supply voltage	-0,5V ~ +7,0V		
input current max.	+1,8V +2,5V +3,0V/+3,3V		
	1,0 ~ 20,0MHz 3,5 mA typ., 6, 0mA max 4,0 mA typ., 6,0 mA max. 4,0 mA typ., 6,0 mA max. 20,1 ~ 50,0MHz 4,5 mA typ., 6,0 mA max 4,0 mA typ.11,0 mA max. 6,0 mA typ 11,0 mA max. 50,1 ~ 70,0MHz 6,0 mA typ.11,0 mA max. 6,0 mA typ.11,0 mA max. 9,0 mA typ.16,0 mA max.		
output load max.	15pF (HCMOS)		
start up time max.	10 ms		
disable delay time max.	150 ns		
enable delay time max.	10 ms		
stand by current max.	50 μA (Pin #1=VIL)		
jitter	deterministic jitter 5ps max. norm 1-sigma 7ps max. random jitter 7ps max. peak to peak 40ps max.		
AECQ 200	available		
contents of reel	1000 pcs. / 3000 pcs.		
part no.	12.xxxxx		





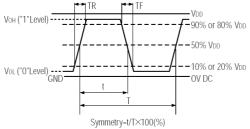






PIN	CONNECTION		
1	"L"	OPEN or "H"	
2	GND		
3	Z	OUTPUT	
4	VDD		





## Tape specification:

