

Sheet1

NV_NVDLA_CACC_CALC_int8

Unproven \$equiv \$auto\$equiv_make.cc:241:find_same_wires\$369: \out_partial_valid_gold \out_par
Unproven \$equiv \$auto\$equiv_make.cc:241:find_same_wires\$334: \out_final_valid_gold \out_final_
Unproven \$equiv \$auto\$equiv_make.cc:241:find_same_wires\$333: \out_final_sat_gold \out_final_s
Unproven \$equiv \$auto\$equiv_make.cc:270:find_same_wires\$264: \i_sat_vld_gold \i_sat_vld_gate
Unproven \$equiv \$auto\$equiv_make.cc:270:find_same_wires\$263: \i_sat_sel_gold \i_sat_sel_gate

Ignore cfg_in_en_mask

tial_valid_gate
.valid_gate
at_gate

apb2csb

Trying to prove \$equiv for lrd_trans_low: failed.

slibs

AN2D4PO4 pass
CKLNQD12 pass
CKLNQD12P(pass
BC_Pipe Tester Pass
IS_Pipe Tester Pass
MUX2D4 Tester Pass
MUX2HDD2 Tester Pass
OR2D1 Tester Pass

rams

nv_rams_rws dout failed, but pass the tester

nv_rams_rws:dout_r failed, dout failed, but pass the tester

nv_rams_rws:dout failed due to the same reason with 1&2, haven't tested on tester yet