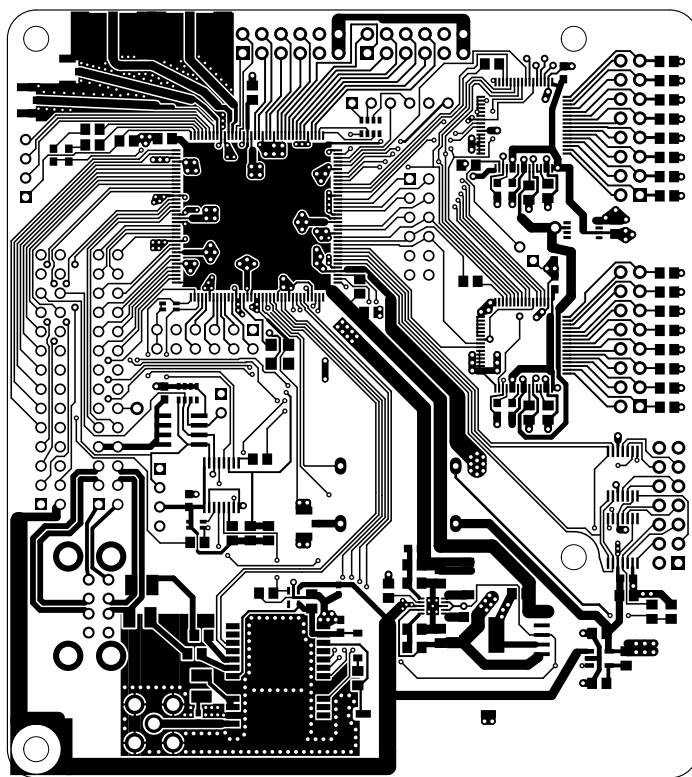
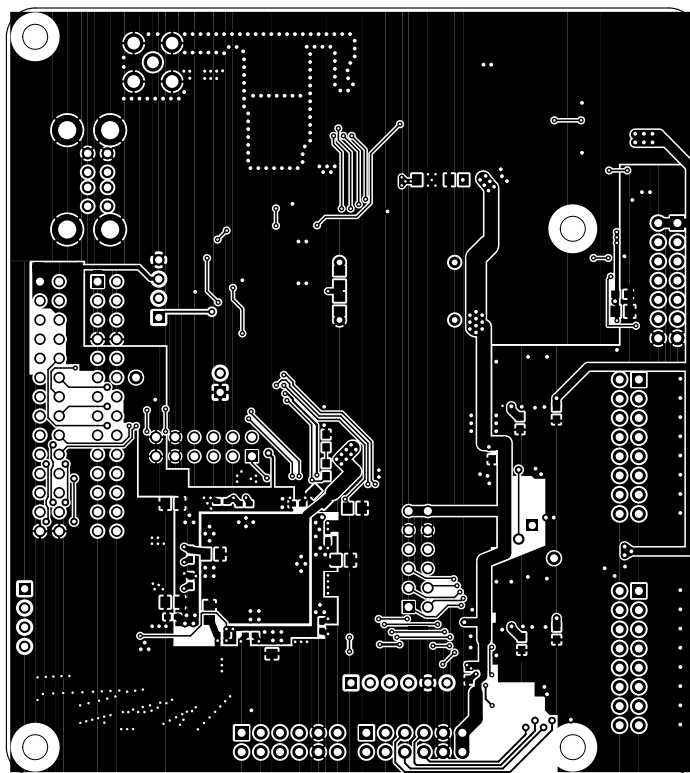
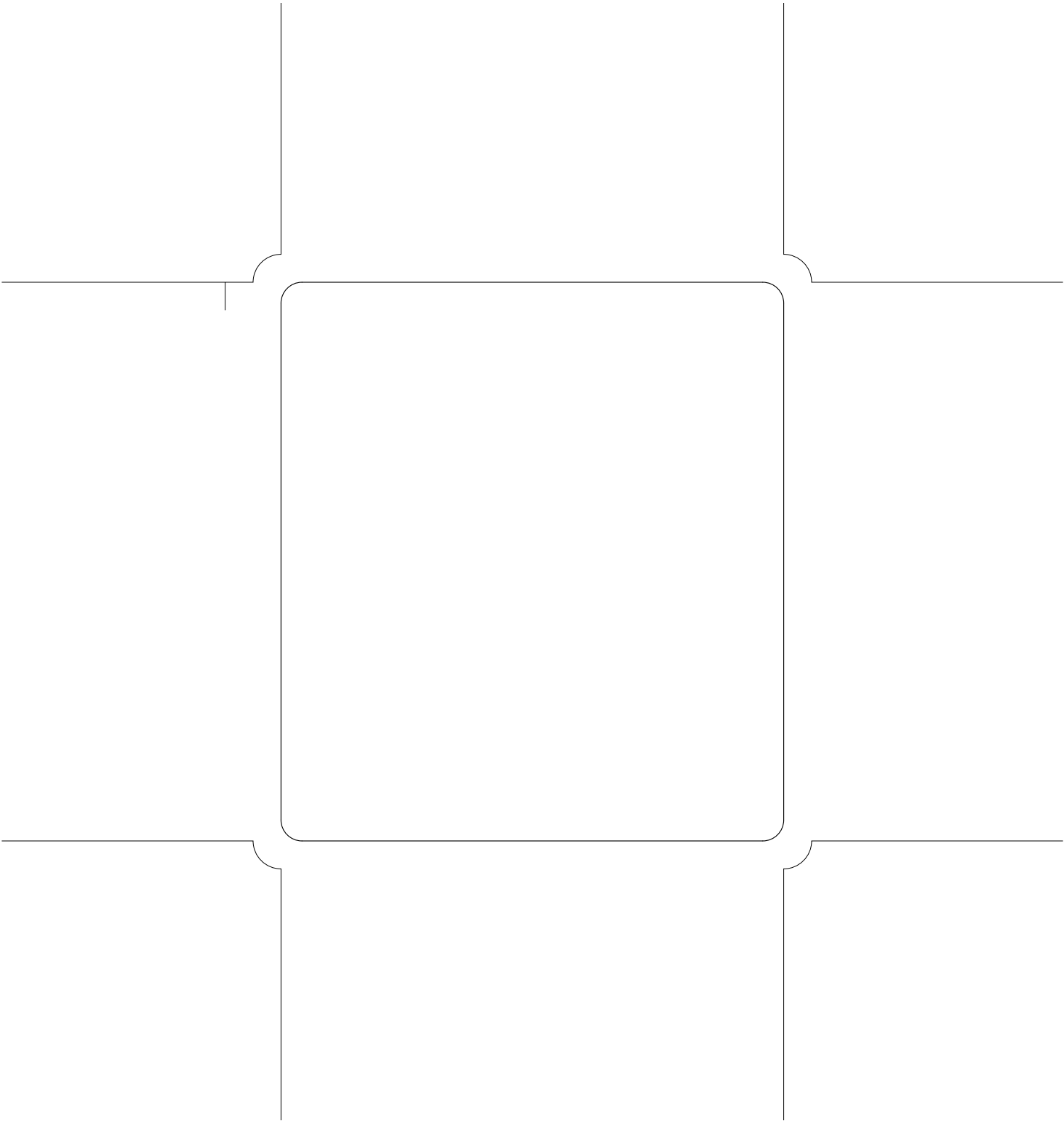
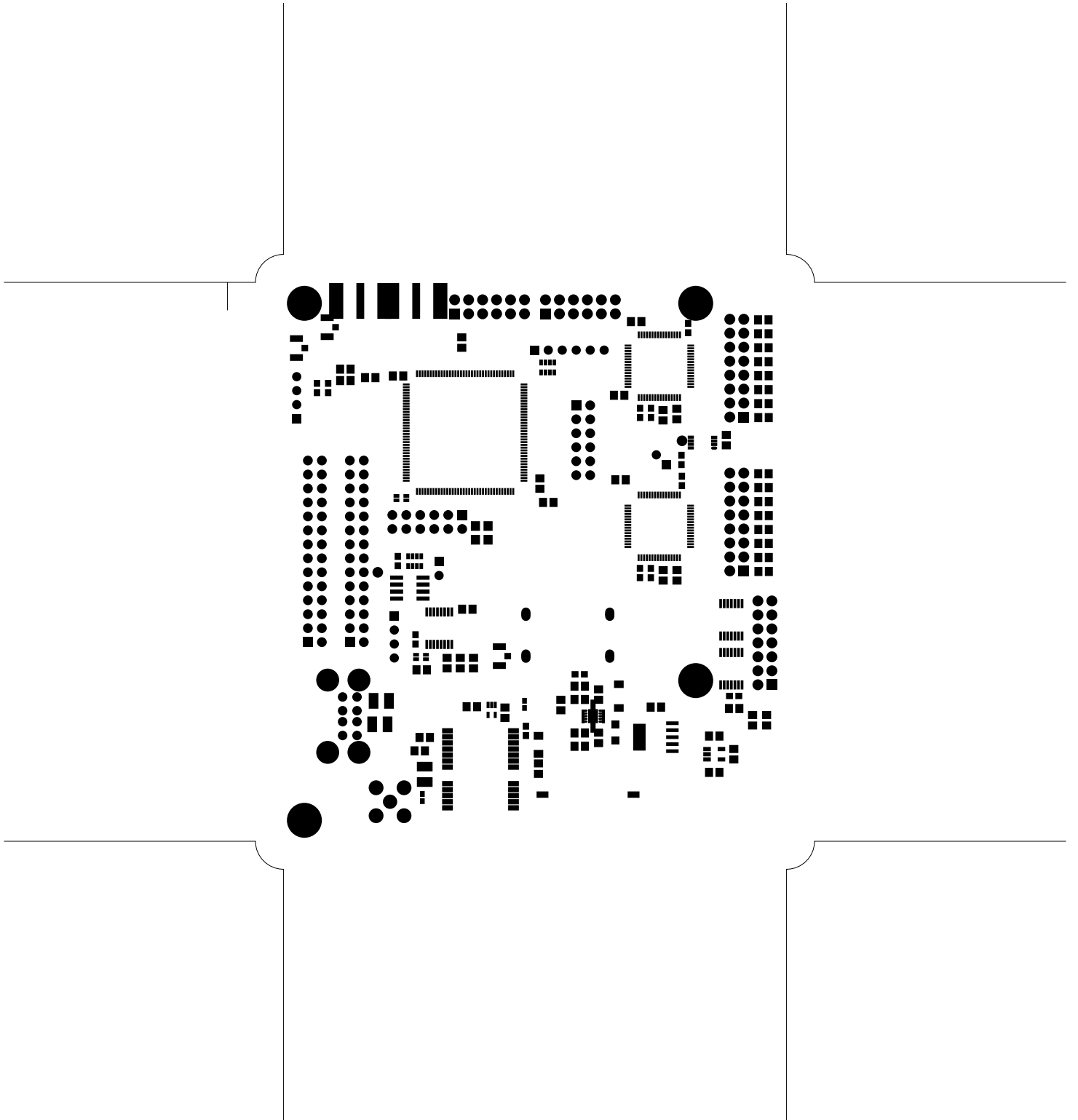


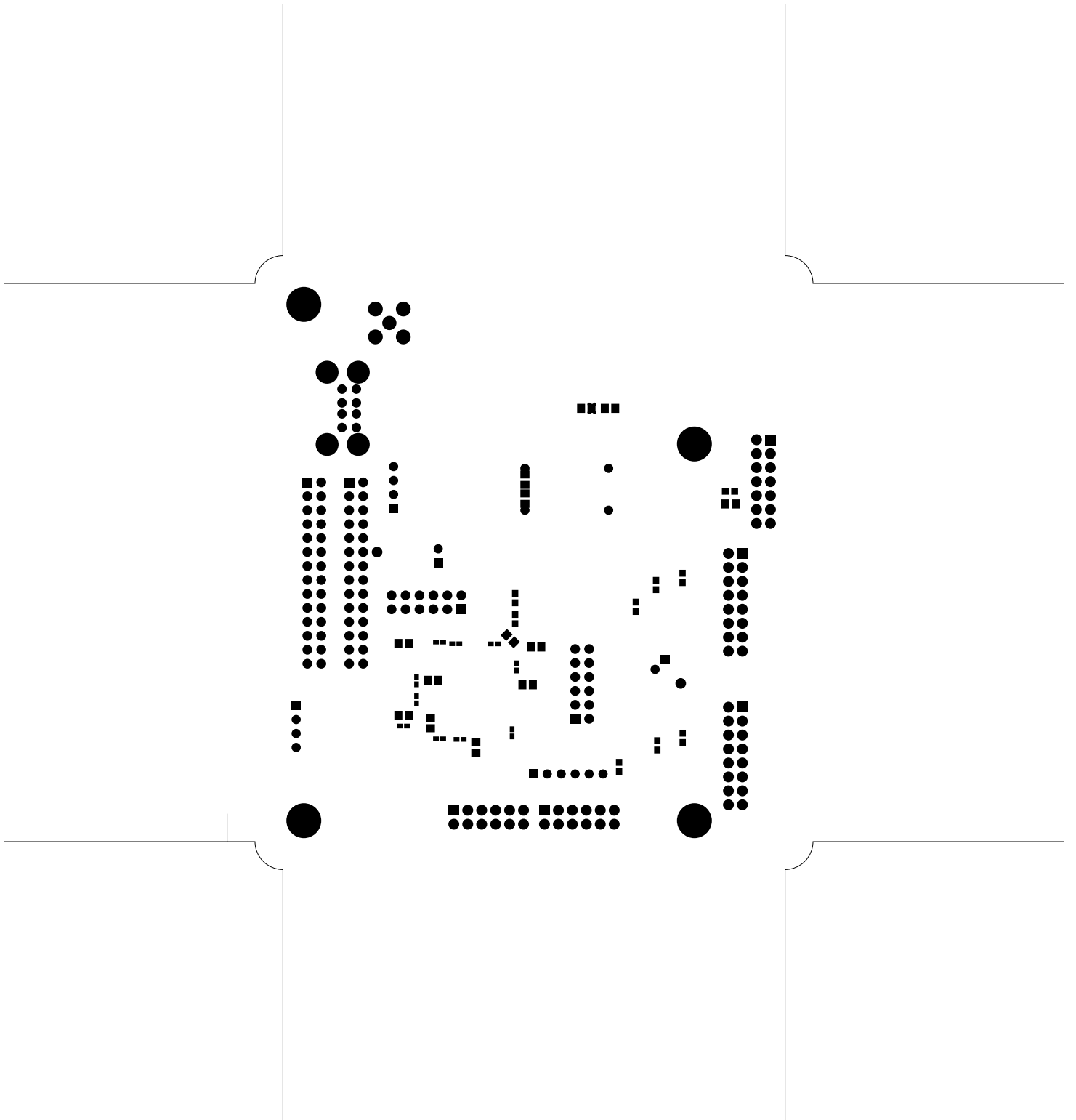
2. component
3. solder
4. outline
5. componentmask
6. soldermask
7. plated-drill
8. unplated-drill
9. topsilk
10. bottomsilk
11. bottompaste
12. toppaste
13. topassembly
14. bottomassembly
15. fab



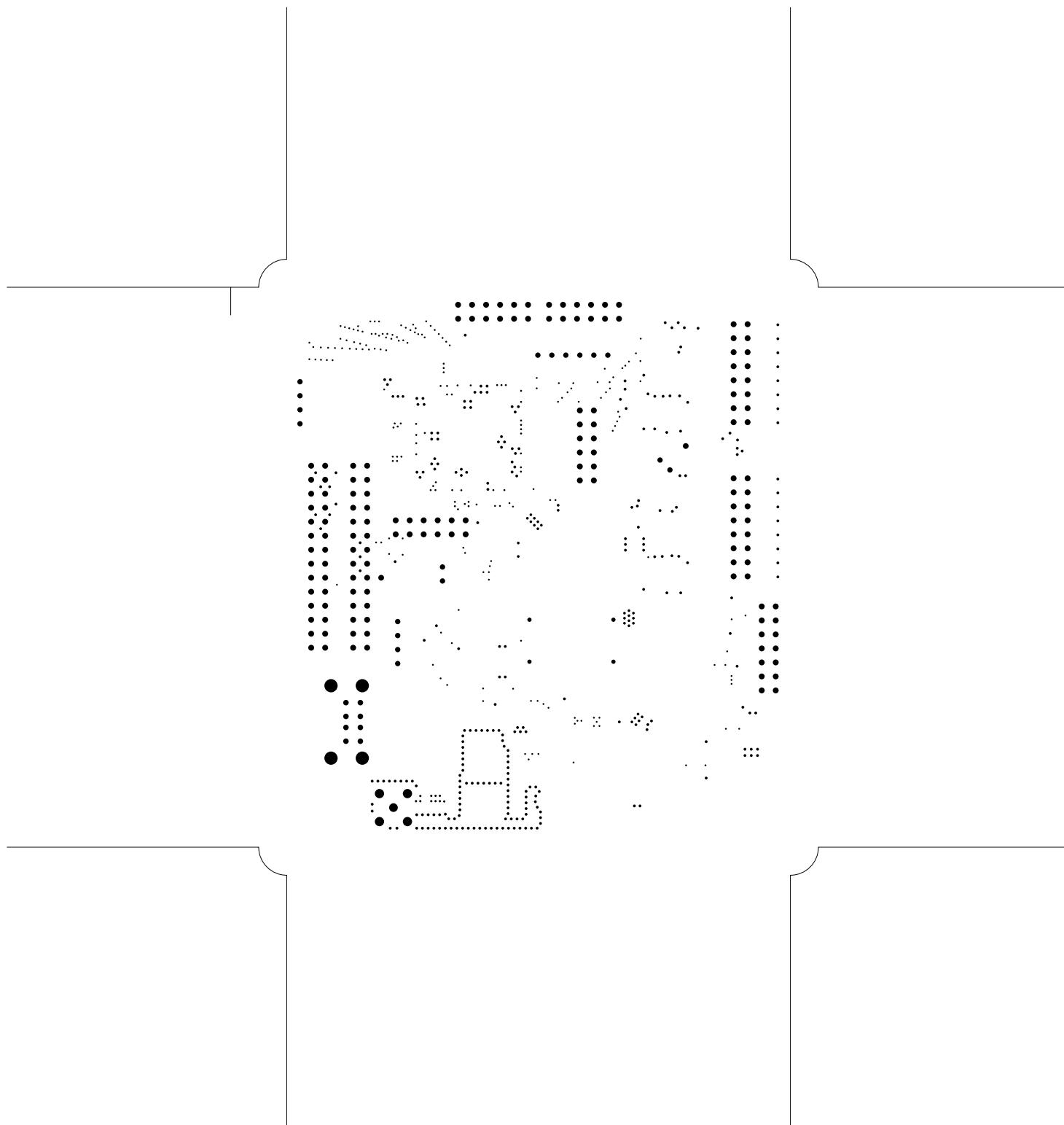


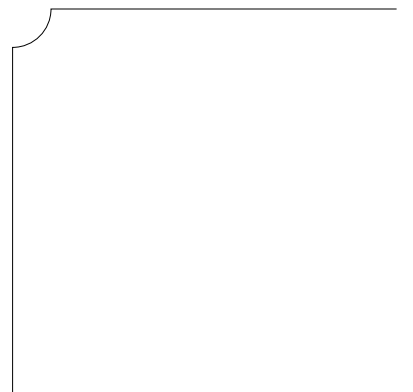
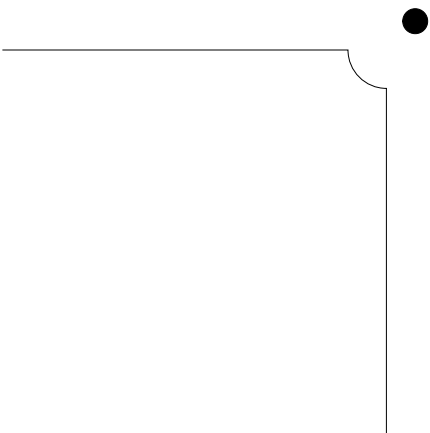
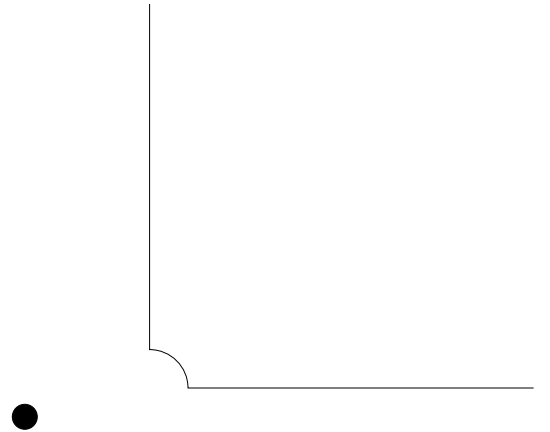
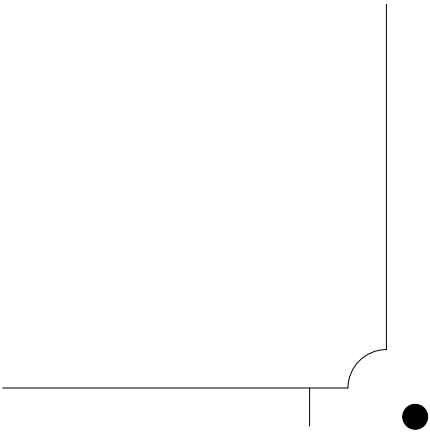


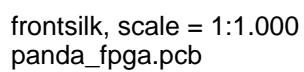


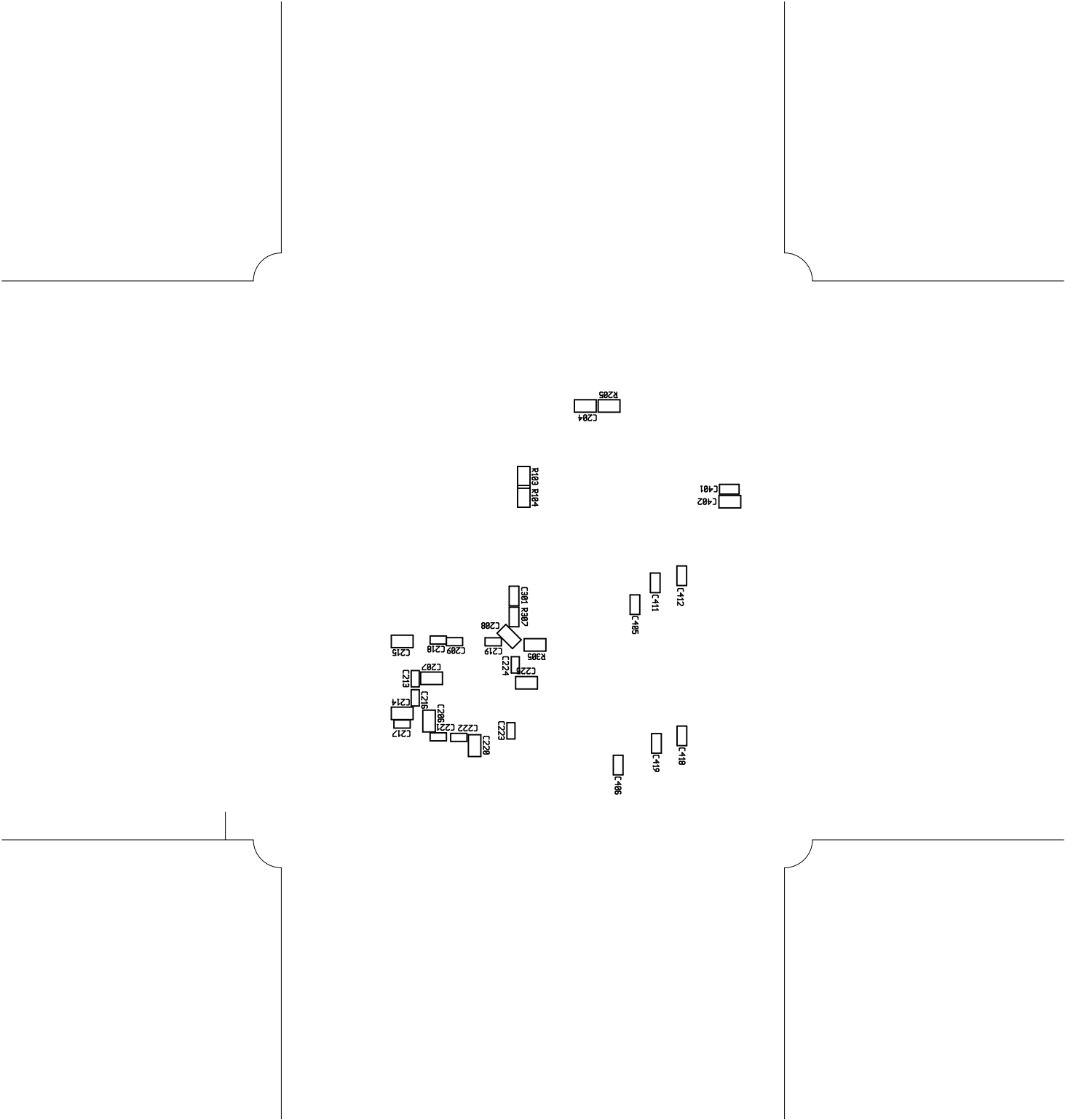


backmask (mirrored), scale = 1:1.000
panda_fpga.pcb

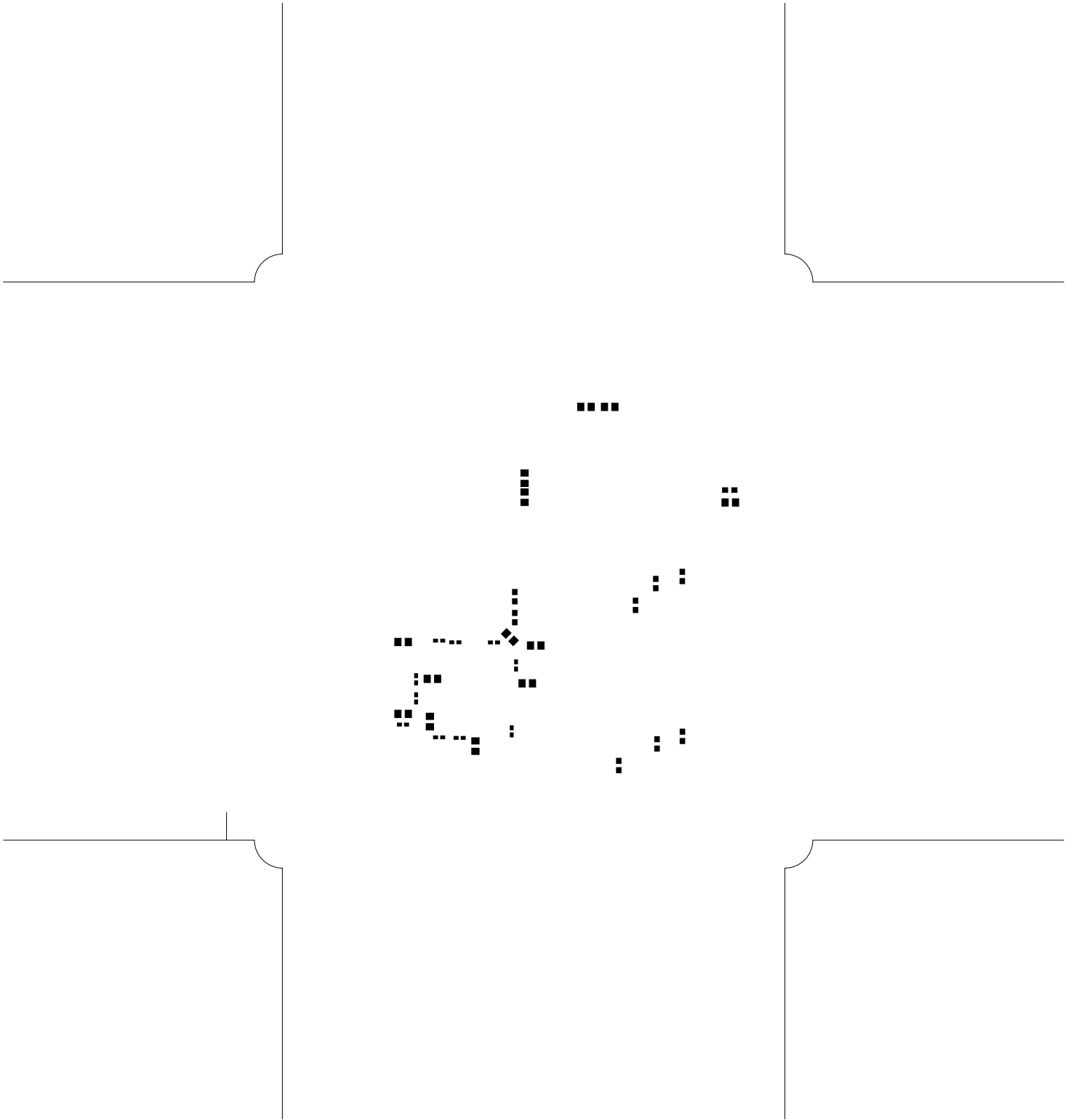


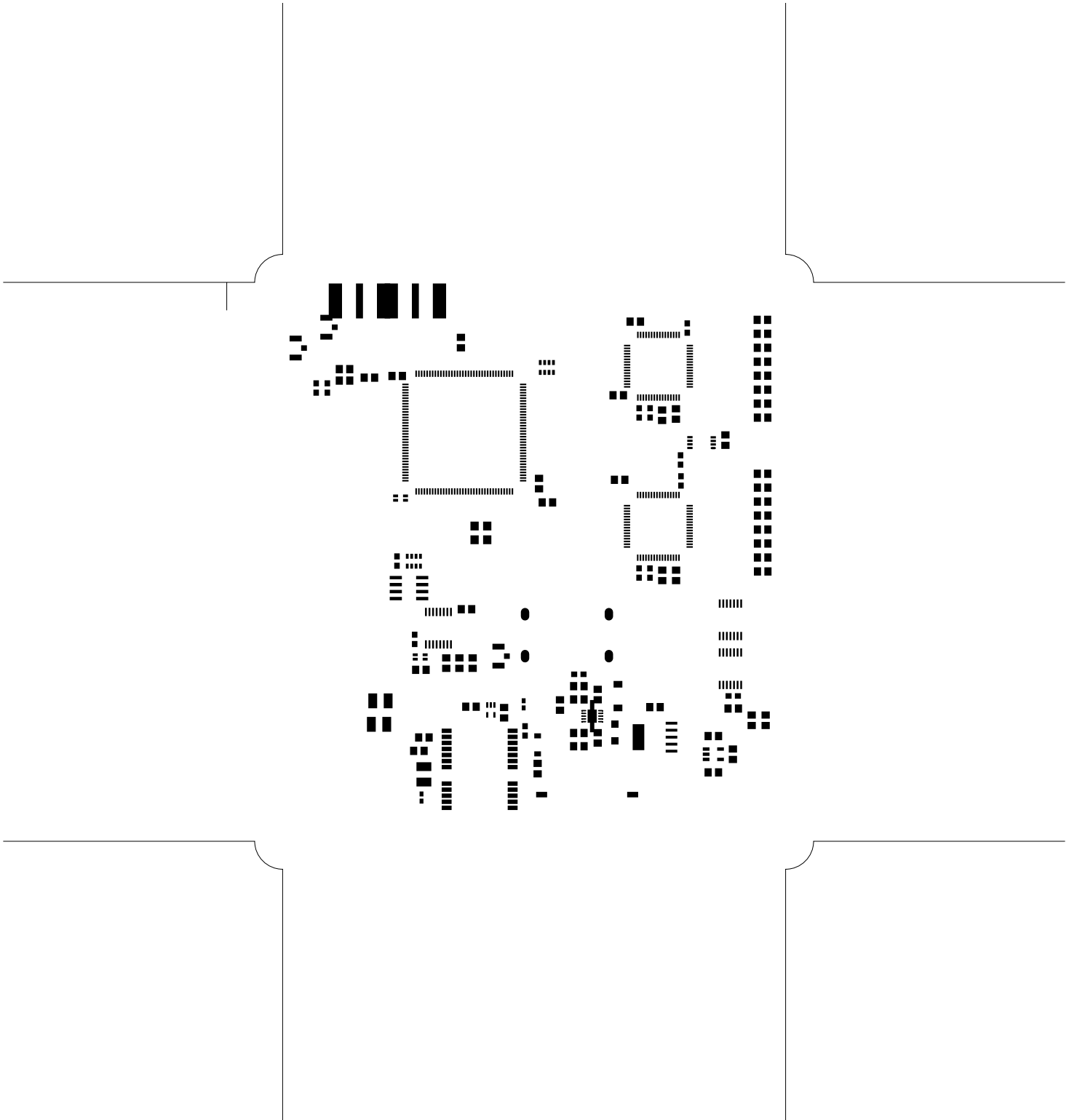


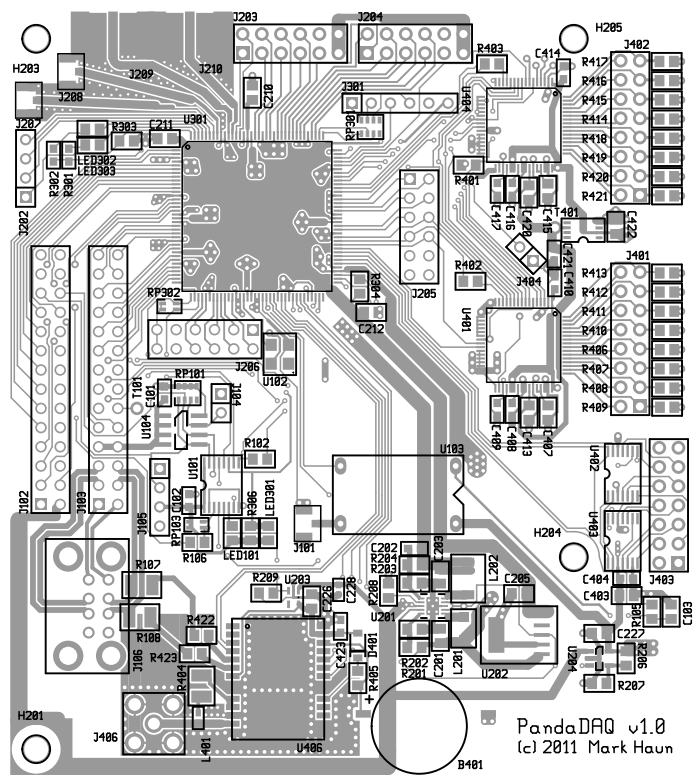




backsilk (mirrored), scale = 1:1.000
panda_fpga.pcb







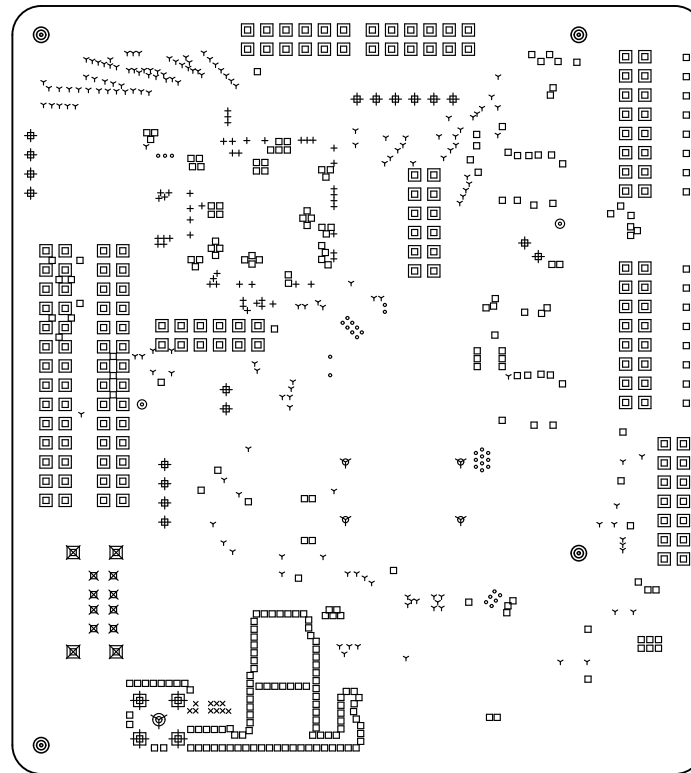
There are 14 different drill sizes used in this layout, 705 holes total

Symbol	Diam. (Inch)	Count	Plated?
˘	0.013	152	YES
+	0.014	50	YES
×	0.016	10	YES
•	0.019	31	YES
◻	0.020	267	YES
▽	0.030	4	YES
⊕	0.038	18	YES
⊗	0.039	8	YES
◉	0.041	2	YES
◻	0.042	150	YES
◉	0.052	1	YES
⊕	0.066	4	YES
⊗	0.094	4	YES
◉	0.130	4	NO

Title: (unknown) - Fabrication Drawing

Author: Mark A. Haun

Date: Mon 12 Dec 2011 01:00:58 AM GMT UTC



Board outline is the centerline of this path