# **МЕДІЛТЕК**

# MHL(HDMI) Customization Introduction for Android L











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#### **Internal Use**

# Agenda

- Overview
- Diagram
- Work Flow
  - Power Management
  - Audio/Video Path
  - Others
- Customization

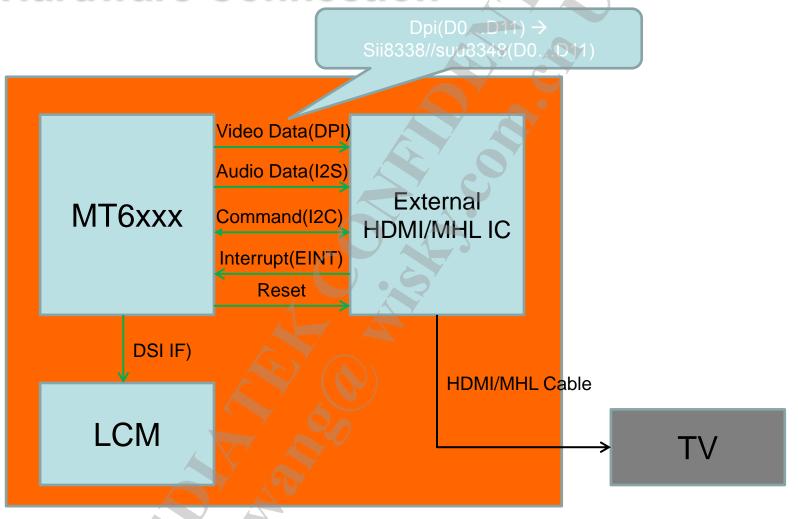


# **Overview**

CPU	Support	MHLIC	Maxium Resoluton	Audio Channel	Support Smartbook		
Ci O	Sii8338 Sii8348		Waxiam Resolution	Addio Chamilei	Support Smartbook		
MT6582	Yes	Yes			Yes		
MT6592	Yes	Yes	1080p@30fps	2	Yes		
MT6752	No	Yes	1080p@30fps	2	Yes		
MT6595	No	Yes	1080p@60fps	7.1	Yes		
MT6795	No	Yes	1080p@60fps	7.1	Yes		

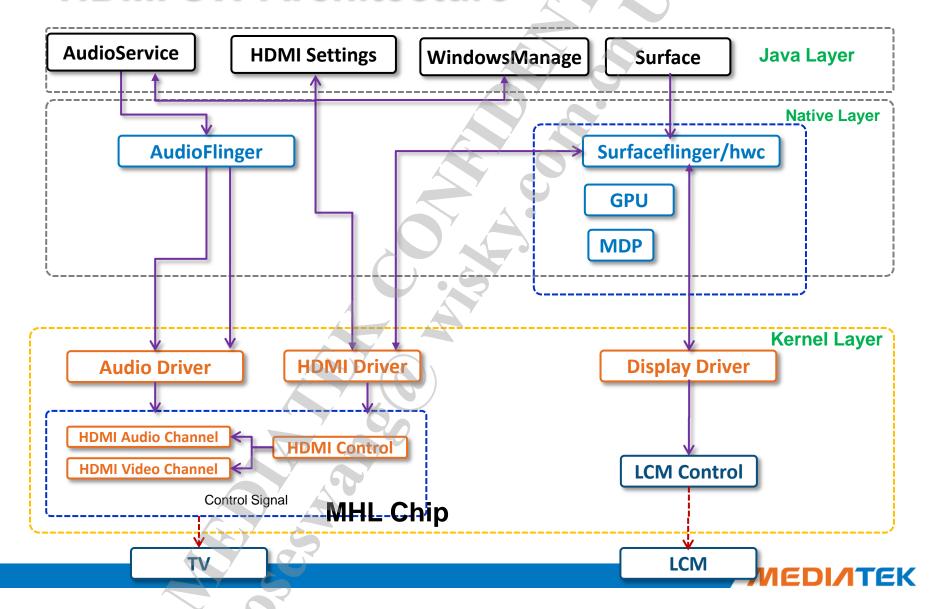


## **Hardware Connection**





### **HDMI SW Architecture**

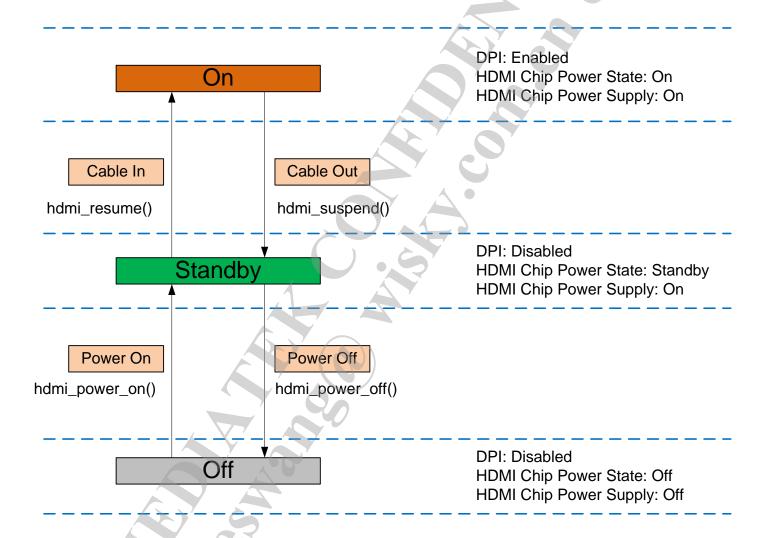


# HDMI IOCTL

	IOCTL Name	Comment						
	MTK_HDMI_POWER_ENABLE	Power on/off hdmi driver when screen off//on						
	MTK_HDMI_AUDIO_VIDEO_ENABLE	Enable/disable hdmi						
	MTK_HDMI_VIDEO_ENABLE	Config video resolution						
	MTK_HDMI_GET_DEV_INFO	Get hdmi driver information, such as currently resolution, cable type and so on.						
	MTK_HDMI_VIDEO_ENABLE	Config video resolution						
mt6582/mt6592	MTK_HDMI_PREPARE_BUFFER	Prepare a video buffer: 1. map ion buffer from hwcomposer; 2 create fence						
	MTK_HDMI_POST_VIDEO_BUFFER	Post a video buffer to hdmi from hwcomposer.						
	DISP_IOCTL_CREATE_SESSION	Create a MHL session and data path						
	DISP_IOCTL_DESTROY_SESSION	Destroy mhl data path and session						
Other platform	DISP_IOCTL_PREPARE_INPUT_BUFFER	Prepare fence index for the video buffer:						
	DISP_IOCTL_SET_INPUT_BUFFER	Set video buffer parameter						
	DISP_IOCTL_TRIGGER_SESSION	Trigger video buffer to display out						



# **Power Management Model**





# **Power Management**

- For power management, each function has defined the availability for each power state;
- Functions which can change power state could only be called from the correct state, for example, hdmi\_resume() could only be called in standby state.

API Name	On	Off	Standby		
hdmi_resume	no	no	yes		
hdmi_suspend	yes	no	no		
hdmi_power_on	no	yes	no		
hdmi_power_off	yes	no	yes		
hdmi_state_callback	yes	no	yes		
hdmi_state_reset	yes	yes	yes		
hdmi_video_enable	yes	no	no		
hdmi_audio_enable	yes	no	no		
hdmi_video_config	yes	no	no		
hdmi_audio_config	yes	no	no		

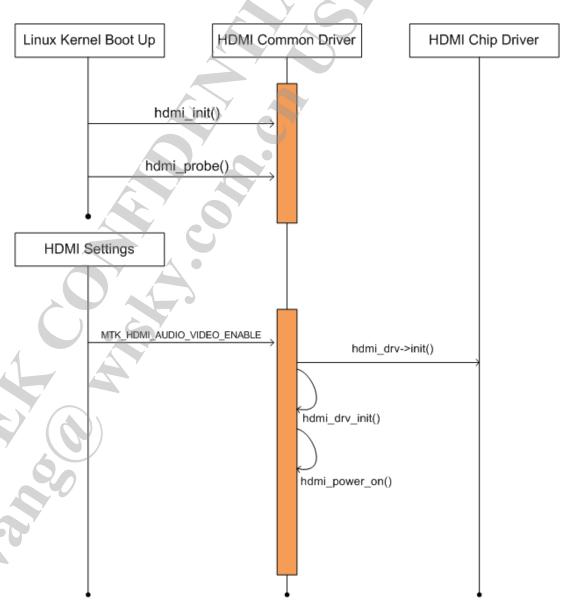


#### **Internal Use**

### **Work Flow**

#### Boot up:

- ✓ Before HDMI Settings init, the hdmi driver will stay in power off mode;
- ✓ HDMI Settings will enable
  HDMI Driver after the system
  boot up, then HDMI external
  chip is power on into standby
  mode, and waiting for hdmi
  cable plugging in;



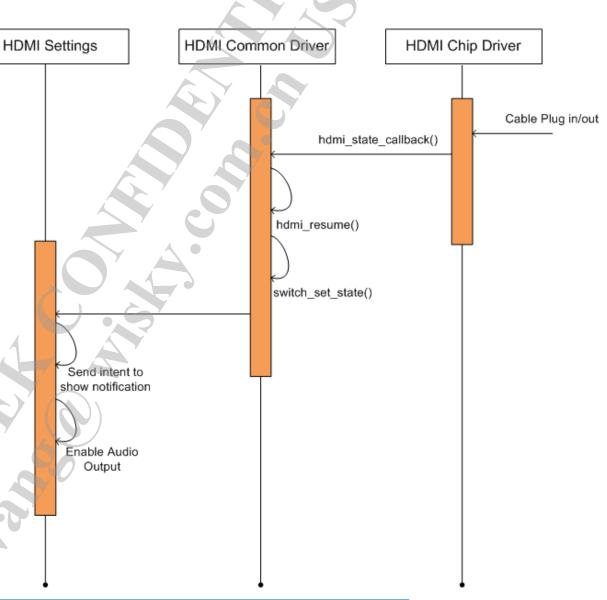


#### **Internal Use**

## **Work Flow**

#### **Cable Plug in/out:**

- √hdmi\_resume() will disable
  DPI power and output, set
  hdmi external chip into
  standby mode;
- ✓ switch\_set\_state() is used to nofity the application from kernel. It use android's switch device driver, which is based on linux uevent mechanism.

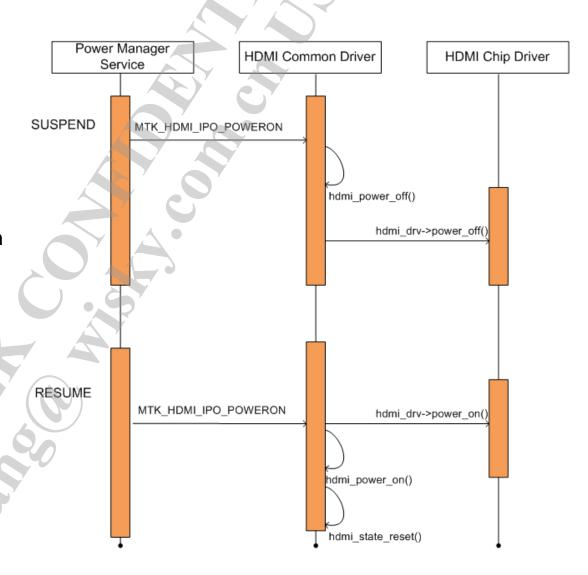




### **Work Flow**

#### Suspend/Resume

- ✓ HDMI and tvout will not follow the android's default early suspend flow, because in android's earlysuspend, it will shutdown backlight first, and then shutdown lcd. If HDMI/TVOUT suspend when lcd is powered off, there will have redundant data shown on the TV. So power manager service create an additional jni to control HDMI/TVOUT suspend/resume flow;
- √hdmi\_state\_reset() is to ensure the application's state is correct with the hdmi chip.

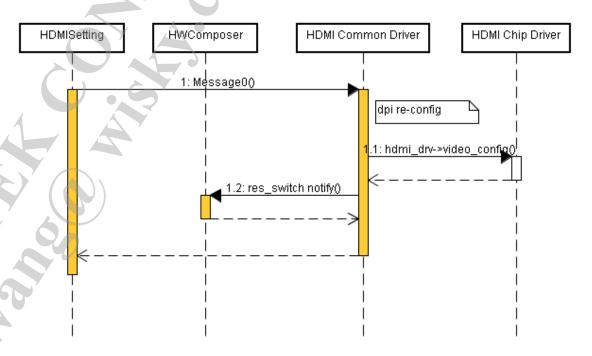




### **Work Flow**

#### **Video Configuration**

√This is only avaliable for LCD
Mirror Mode, because for DPI
Bypass mode, the output
resolution is selected by hdmi
chip itself.





# **Project Configuration**

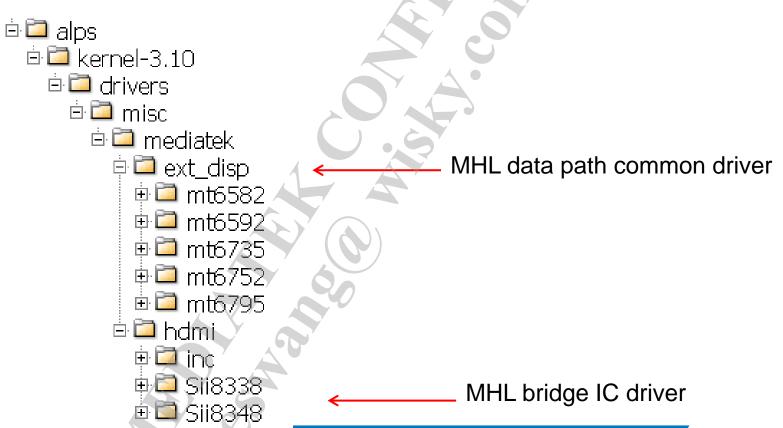
	Configuration file	Switch on	Switch off
user space	device/mediatek/ (\$project)/ProjectConfig.mk;	MTK_HDMI_SUPPORT = yes	MTK_HDMI_SUPPORT = no
kernel space	kernel- xxx/arch/armxx/configs/(\$project)_debug_ defconfig;	MI="Sii8348"	# CONFIG_MTK_HDMI_SUPPORT is not set

It can be set to "Sii8338" or "Sii8348" according to your project



# **Folder Layout**

The architecture of HDMI driver is very familiar to Display driver, which include 2 parts: common driver and chip driver.





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 The architecture of HDMI driver is very familiar to Display driver, which include 2 parts: common driver and chip driver.





- I2S GPIO customize
  - For audio i2s GPIO
    - The following name should be defined in dws: GPIO\_MHL\_I2S\_OUT\_WS\_PIN GPIO\_MHL\_I2S\_OUT\_CK\_PIN GPIO\_MHL\_I2S\_OUT\_DAT\_PIN
  - Need enable/disable i2s mode in customize file:
    - The following dct configure should be defined according to your platform and project.

	EintMode	Def.Mode	M0	M1	M2	M3	M4	M5	M6	M7	InPull En	InPull SelHigh			Def.Dir		In	Out	OutHigh	VarName1	
GPI0129		1:12S3_W •	7	7	П	Г				П				OUT		_	~	~		GPIO_MHL_I2S_OUT_WS_PIN	*
GPI0130		1:12S3 BCK	7	~										OUT			~	~		GPIO_MHL_I2S_OUT_CK_PIN	
GPI0132		1:12S3_DO_1	~	4									7	OUT			~	~		GPIO_MHL_I2S_OUT_DAT_PIN	



- I2S GPIO customize
  - mt6582/mt6592: kernel-xxx/arch/arm/marh-(&platform)/(&project)/hdmi/hdmi\_cust.c
  - others: kernelxxx/drivers/misc/mediatek/marh/(&platform)/(&project)/hdmi/hdmi\_cust.c
  - The function should be implemented according to your platform and project.

```
int cust_hdmi_i2s_gpio_on(int on)
   if(on > 0)
#ifdef GPIO MHL I2S OUT WS PIN
       mt set qpio mode(GPIO MHL I2S OUT WS PIN, GPIO MHL I2S OUT WS PIN M I2S3 WS);
       mt set qpio mode(GPIO MHL I2S OUT CK PIN, GPIO MHL I2S OUT CK PIN M I2S3 BCK);
       mt set qpio mode(GPIO MHL 12S OUT DAT PIN, GPIO MHL 12S OUT DAT PIN M 12S3 DO);
#else
       printk("%s,%d Error. GPIO MHL I2S OUT WS PIN is not defined\n", func , LINE );
#endif
    else
#ifdef GPIO MHL I2S OUT/WS PIN
       mt set qpio pull enable(GPIO MHL I2S OUT WS PIN, GPIO PULL DISABLE);
       mt_set_qpio_pull_enable(GPIO_MHL_I2S_OUT_CK_PIN, GPIO_PULL_DISABLE);
       mt set qpio pull enable(GPIO MHL I2S OUT DAT PIN, GPIO PULL DISABLE);
#endif
} ? end cust_hdmi_i2s_gpio_on ?
```



- DPI GPIO customize
  - For DPI GPIO
    - GPIO\_EXT\_DISP\_DPI0\_PIN name should be defined in dws.
  - Need enable/disable dpi mode in customize file:
    - The following dct configure should be defined according to your platform and project

	EintMode	Def.Mode	M0	M1	M2	M3	M4	M5	M6 M7	InPull En	InPull	SelHigh	Def.Dir	ln ln	Out	OutHigh	VarName1
GPI0138		1:DPI_CK •	V	~								OUT		· V	~		GPIO_EXT_DISP_DPI0_
GPI0139		1:DPI_DE	~	~								OUT		~	~		
GPIO140		1:DPI_D0	~	~								OUT		~	~		
GPI0141		1:DPI_D1	~	~							1	OUT		~	~		
GPI0142		1:DPI_D2	~	~								OUT		~	~		
GPI0143		1:DPI_D3	~	~								OUT		~	~		
GPI0144		1:DPI_D4	~	~							$\Delta$	OUT		~	~		
GPI0145		1:DPI_D5	~	~							A	OUT		~	~	П	
GPIO146		1:DPI_D6	~	~								OUT		~	~		
GPI0147		1:DPI_D7	~	~	Г				RE	Y		OUT		~	~		
GPI0148		1:DPI_D8	~	~								OUT		~	~		
GPI0149		1:DPI_D9	~	~								OUT		~	~		
GPI0150		1:DPI_D10	~	~								OUT		~	~		
GPI0151		1:DPI_D11	~	~								OUT		~	~		
GPI0152		1:DPI_HSYN0	~	~								OUT		~	~		
GPI0153		1:DPI_VSYN0	~	~				175				OUT		~	~		
							1	Υ .		Y							



- DPI GPIO customize
  - mt6582/mt6592: kernel-xxx/arch/arm/marh-(&platform)/(&project)/hdmi/hdmi\_cust.c
  - others: kernelxxx/drivers/misc/mediatek/marh/(&platform)/(&project)/hdmi/hdmi\_cust.c
  - The following function should be implemented according to your platform and project

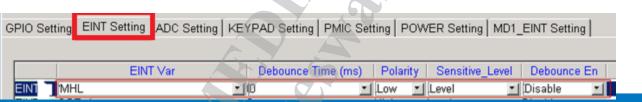
```
int cust_hdmi_dpi_gpio_on(int on)
    unsigned int dpi pin start = 0;
    if(on > 0)
#ifdef GPIO_EXT_DISP_DPIO_PIN
        for (dpi_pin_start = GPIO_EXT_DISP_DPI0_PIN; dpi_pin_start < GPIO_EXT_DISP_DPI0_PIN + 16; dpi_pin_start++)
            mt_set_gpio_mode(dpi_pin_start, GPIO_MODE_01);
       printk("%s, %d GPIO_EXT_DISP_DPIO_PIN is defined+ %x\n", __func__, __LINE__, GPIO_EXT_DISP_DPIO_PIN);
        printk("%s,%d Error: GPIO_EXT_DISP_DPIO_PIN is not defined\n", __func__, __LINE__);
#endif
≢ifdef GPIO EXT DISP DPI0 PIN
        for(dpi_pin_start = GPI0_EXT_DISP_DPI0_PIN; dpi_pin_start < GPI0_EXT_DISP_DPI0_PIN + 16; dpi_pin_start++)</pre>
            mt set qpio mode(dpi pin start, GPIO MODE 00);
            mt set qpio dir(dpi pin start, GPIO DIR IN);
           mt_set_gpio_pull_enable(dpi_pin_start, GPIO_PULL_ENABLE);
            mt set qpio pull select(dpi pin start, GPIO PULL DOWN);
        printk("%s, %d GPIO_EXT_DISP_DPIO_PIN is defined- %x\n", __func__, __LINE__, GPIO_EXT_DISP_DPIO_PIN);
} ? end cust_hdmi_dpi_gpio_on ?
```

- GPIO\_MHL\_RST\_B\_PIN
  - This pin should be defined and is used to reset MHL bridge IC when power on
  - The following dct configure should be defined according to your platform and project



- CUST\_EINT\_MHL\_NUM
  - This pin should be defined and is used to receive interrupt from MHL bridge IC.
  - The following dct configure should be defined according to your platform and project





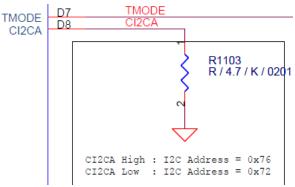


### **I2C Customization**

 For Sii8338 MHL bridge IC, the I2C address can't be customize according to CI2CA pin status and selected I2C channel

- MHL I2C can be customized in
  - mt6582/mt6592: kernel-xxx/arch/arm/marh-(&platform)/(&project)/hdmi/hdmi\_cust.h
  - others: kernelxxx/drivers/misc/mediatek/marh/(&platform)/(&project)/hdmi/hdmi\_cust.h

```
#define SII_I2C_ADDR (0x72)
#define HDHI_I2C_CHANNEL 3
```



# **МЕДІЛІЕК**

# Thank You!









