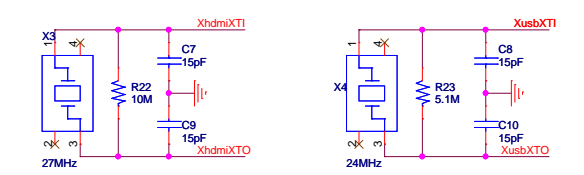
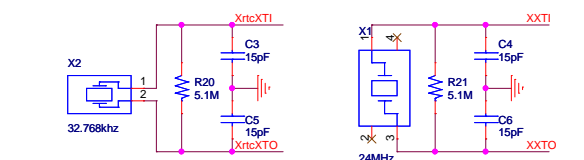
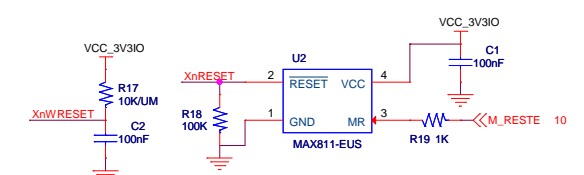
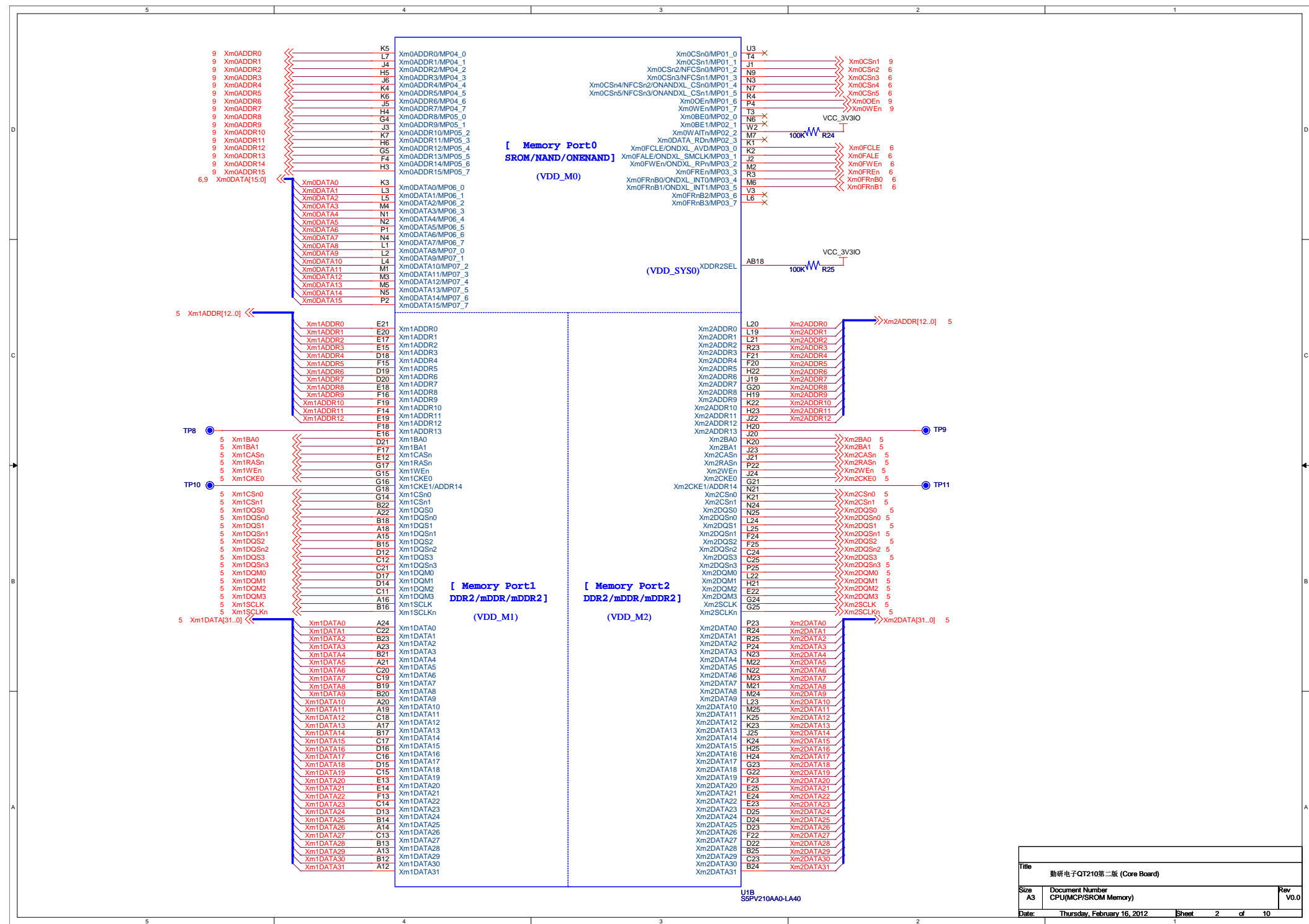


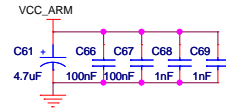
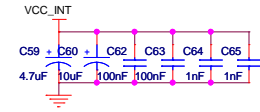
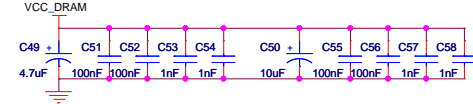
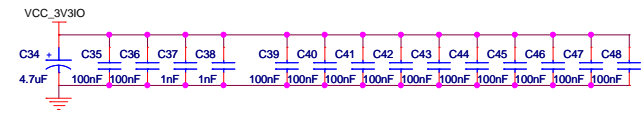
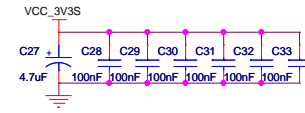
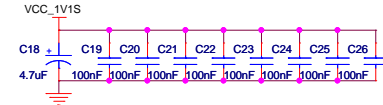
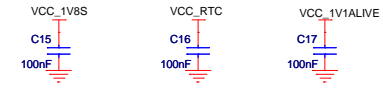
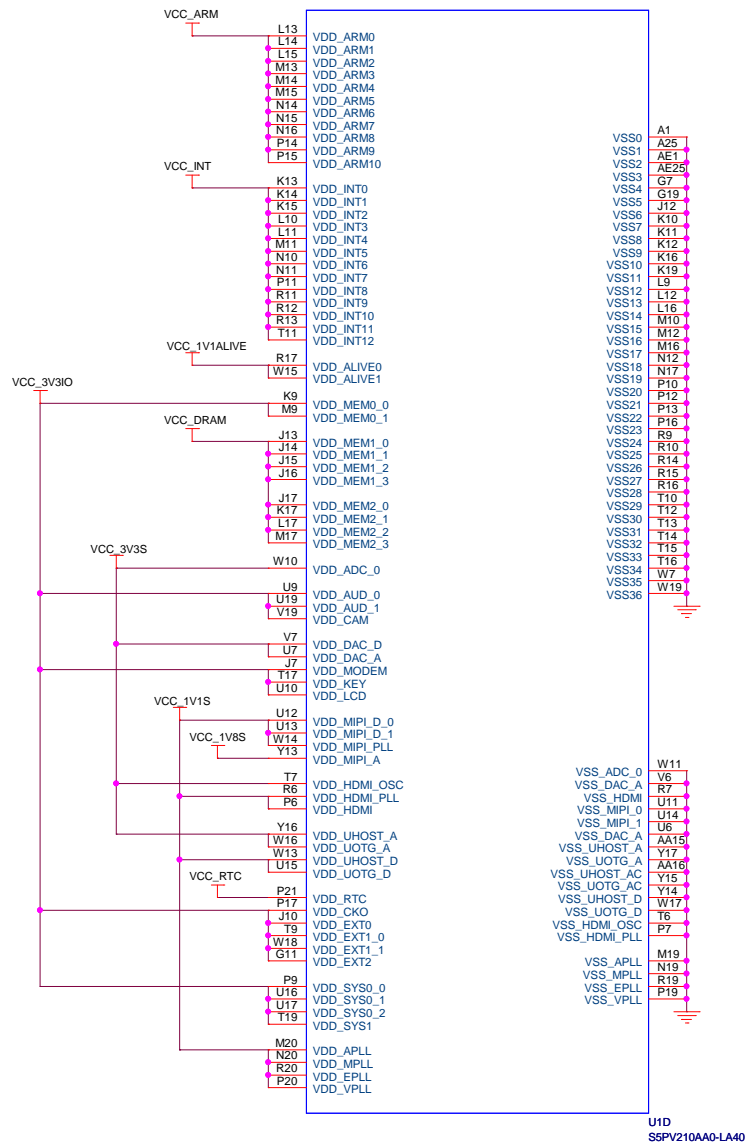
OM[5]	OM[4: 1]	OM[0]	Storage
Boot Seq.	0 0 0 0	PLL	NAND 512B-4cycle
1: USB->	0 0 0 1	Clock	NAND 2KB-5cycle
UART->	0 1 0 0	Source	NAND 4KB-5cycle 8-ECC
Storage	0 1 0 1	0: X-TAL	OneNAND Mux(Audi)
	0 1 1 0		SD/MMC
	0 1 1 1		eMMC(4bit)
Boot Seq.	1 0 0 0	1: X-TAL (USB)	Reserved
0: Storage	1 0 0 1		NAND 2KByte Page, 4cycle
	1 0 1 0		iROM NOR boot
	1 0 1 1		eMMC(8bit)



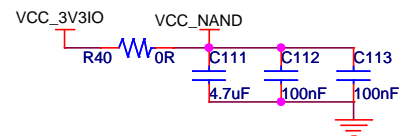
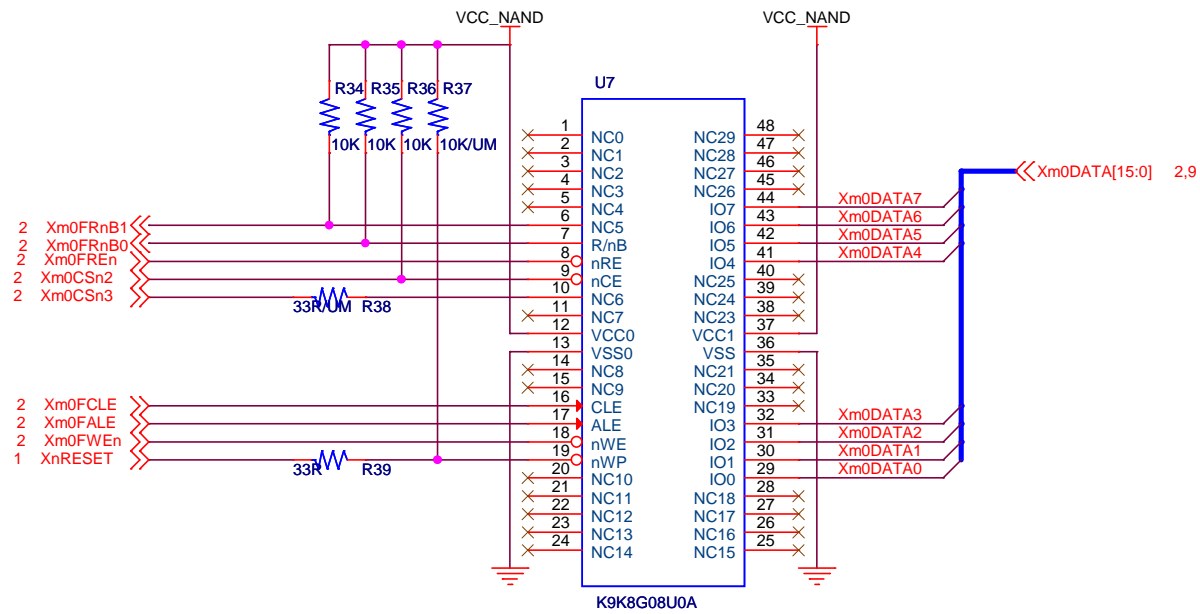


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Size	Document Number	Rev	
A3	CPU(MCP/SROM Memory)	V0.0	
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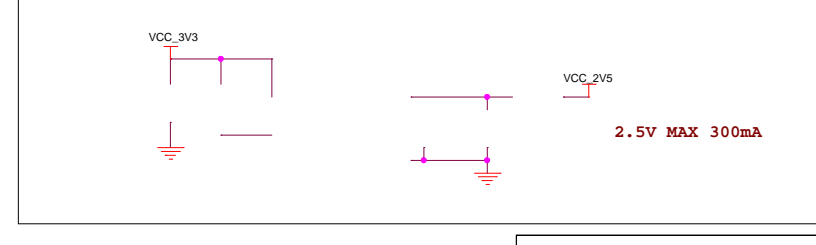
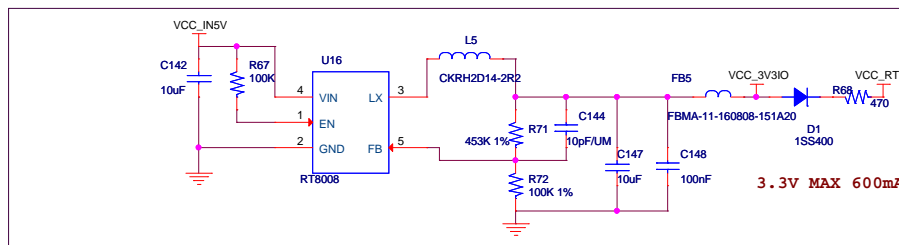
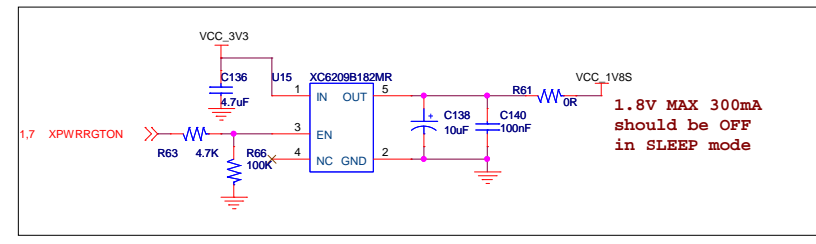
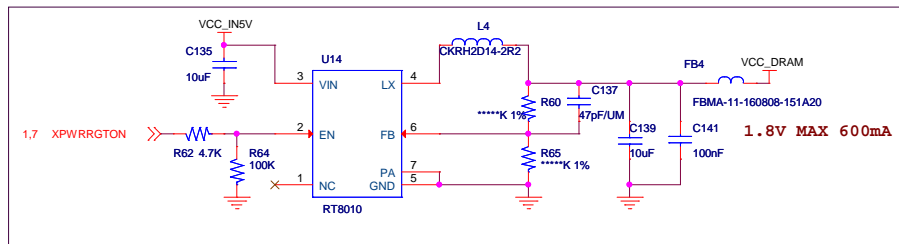
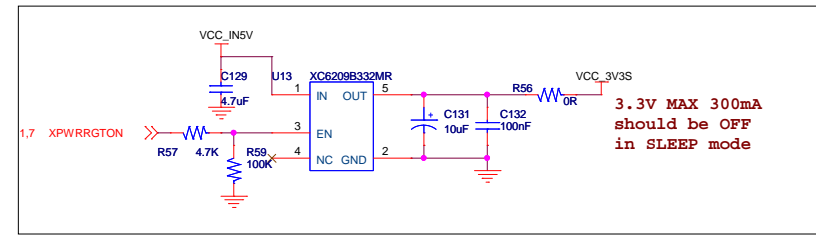
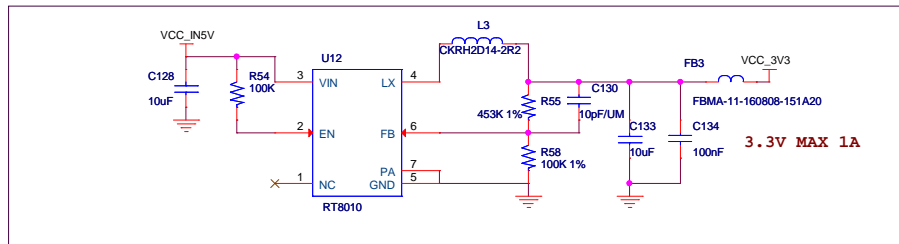
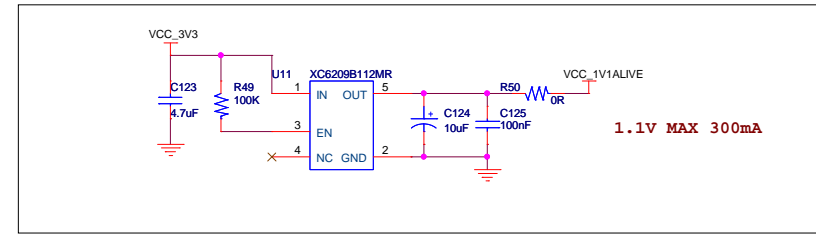
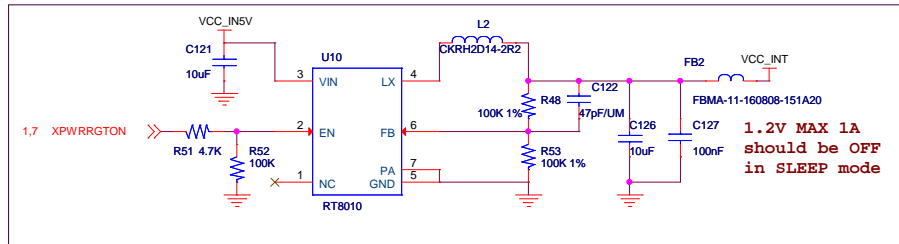
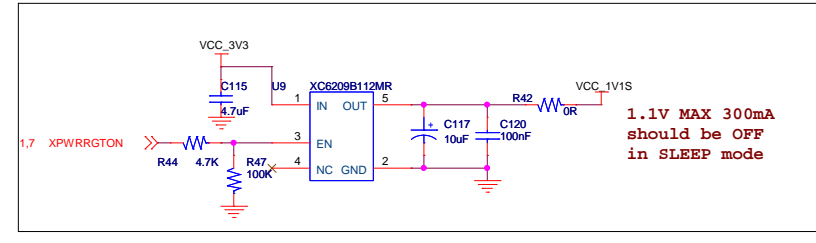
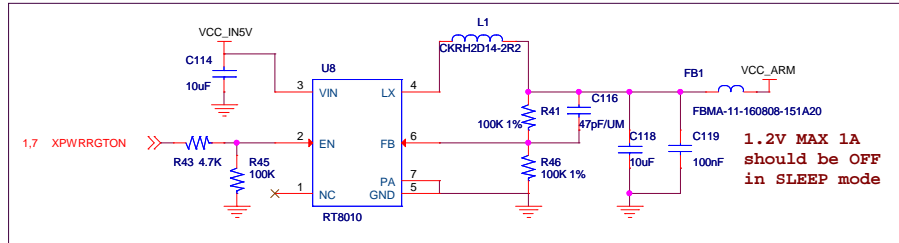




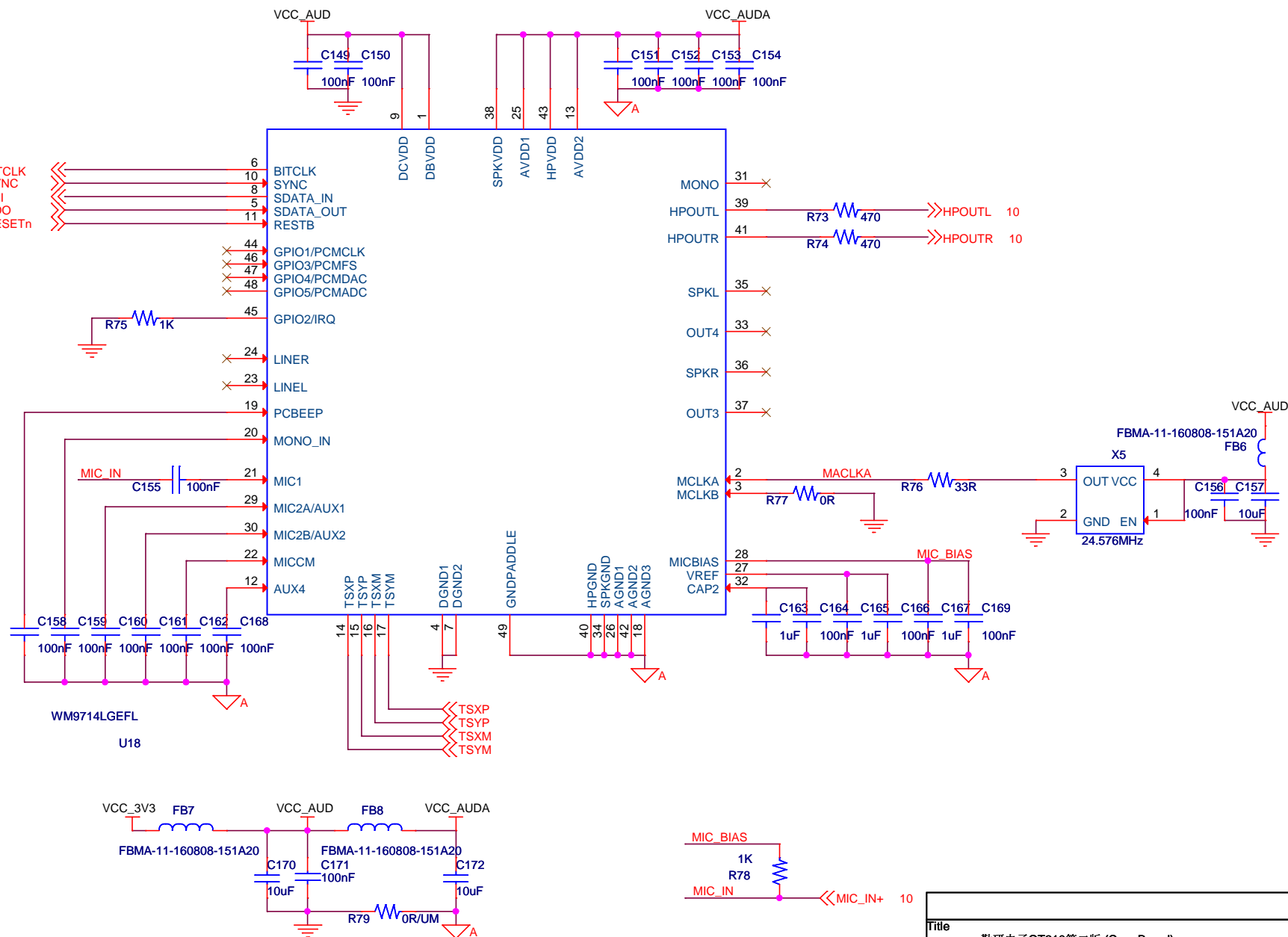
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Size	Document Number	Rev
A3	CPU(Power)	V0.0
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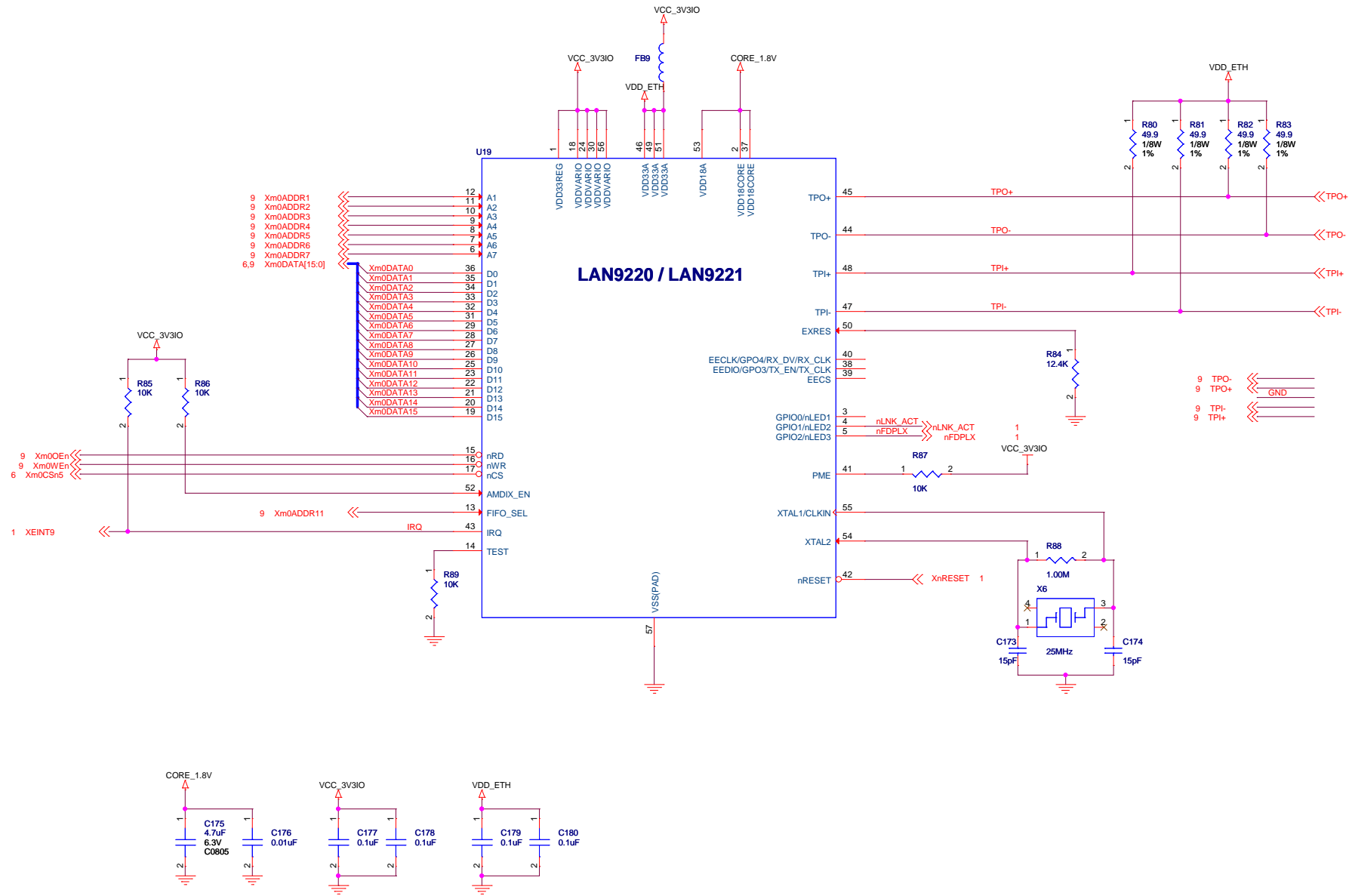
Title		
勤研电子QT210第二版 (Core Board)		
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1 AC97_BITCLK
1 AC97_SYNC
1 AC97_SDI
1 AC97_SDO
1 AC97_RESETn



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