KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY
Narayanaguda,Hyderbad-500029

FACULTY PROFILE

1. **NAME** : **Maddela Asha Jyothi**
2. **JNTUH REGISTRATION ID** : **94150406-002509**
3. **DATE OFBIRTH** : **23-06-1987**
4. **DESIGNATION** : **Assistant Professor**
5. **YEARSOFEXPERIENCE** : **10.5 years**
6. **UGDEGREE** : **B.Tech(ECE)**
7. **PGDEGREE** : **M.Tech(VLSI System Design)**
8. **Ph.D(AREAOFTHEPH.DWORK):**
9. **SUBJECTSTAUGHT** :

- VLSI
- Digital Logic Design
- Switching thoery and Logic Design
- Microprocessors and Microcontrollers
- Microprocessors and Interfacing
- IC Applications
- Electronics Devices and Circuits.
- Linear IC Applications.
- Digital IC Applications.
- Computer organization.
- Digital Signal Processing.
- Analog and Digital Electronics
- Computer organization
- Satellite communications.

10. PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS:

- Simulink design of AGC for audio &video applications.

- Fpga implementation of modified genetic algorithm for four phase code design.

- Fpga implementation and design of arithmetic coder

- Receiving fm/am signals using software defined radio and usrp

- A design on low-power adder operating on effective dynamic data ranges

11. PAPERPUBLICATIONSININTERNATIONALCONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	Simulink design of AGC for audio &video applications.	EISTCON-2012	ISBN 978-93-81693-76-6
2	Receiving fm/am signals using software defined radio and usrp	ICRIEAT-2017	Indian J.Sci.Res. 17(2): 247-250, 2018
3	Fpga implementation and design of arithmetic coder	ICRIEAT-2017	Indian J.Sci.Res. 17(2): 380 - 384, 2018

12. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL:

LICA lab incharge

LAB Supervisor for 3 AYs (17-18,18-19 & 19-20).

Coursefiles Department verification from 2014 till date

Nptel mentor for IOT

Nptel mentor for INTRODUCTION TO MACHINE LEARNING

Maintained departmental profiles file for NAAC.

Incharge criteria 2 for NBA.

Served as mentor and Class teacher.

Subject co-coordinator twice for Computer organization, Digital logic design, IC applications.

13. CONTRIBUTIONS AT THE COLLEGE LEVEL:

- Innovation ambassador for IIC.
- Incharge IEEE WOMEN IN ENGINEERING (WIE) AFFINITY
- Course file incharge.
- IQAC Committee incharge
- SPOC for SIH (19-20,20-21).
- College maintenance committee member
- Feedback committee member
- Anti ragging Committee member
- Conducted internal hackathon for Smart India hackathon 2020.
- Proctor for IEEE XTREME coding competition.

14. REFRESHERCOURSES/WORKSHOPS/CONFERENCESATTENDED:

	Attended:			
	Name of the Course / Summer School	Place	Duration	Sponsoring Agency
1	Webinar on "5g technology & its applications"	Online	1 day(28-05-2020)	Sri vasavi institute of engineering and technology
2	FDP on "embedded systems & IOT applications"	Online	04-06-2020 to 06/06/2020	Vignans institute of engineering for women
3	Workshop on "refresher course on python"	Online	08-06-2020 to 12-06-2020	Narsimha reddy engineering college
4	Webinar on "publishing research articles in scopus indexed journal"	Online	24-06-2020	Chaitanya bharathi institute of technology.
5	Workshop on " IMPACT OF IOT ON EMBEDDED SYSTEMS"	Hyderabad	12-03-2016	ISM
6	Training on "IPR & technology transfer"	Hyderabad	6-7th February 2020	MHRD,IIC
7	Seminar on"VLSI design using FINFET"	Hyderabad	25-03-2017	KMIT
8	Lecture on "Future trends in wireless communications"	Hyderabad	01-11-2017	G.Narayanamma institute of technology & science

9	Workshop on "GNSS & its Applications"	Hyderabad	19-10-2016	TEQIP-II OSMANIA UNIVERSITY
10	Workshop on "research trends in VLSI 7 embedded systems"	Hyderabad	05-11-2016	TEQIP-II JNTUH
11	Section student congress	Hyderabad	22-23 october 2016	Osmania university
12	Workshop on "latest advances in VLSI & Implementation using CAD tools"	Hyderabad	20-21 february 2015	KMIT

15. MEMBERSHIPOFPROFESSIONALBODIES: **Member OF IEEE**