

KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY
Narayanaguda, Hyderabad-500029

FACULTY PROFILE

photo



1. NAME : Padma Balakrishna
2. JNTUHREGISTRATIONID : 9801-150426-135713
3. DATE OF BIRTH : 11-07-1988
4. DESIGNATION : ASSISTANT PROFESSOR
5. YEARS OF EXPERIENCE : 9 YEARS
6. UG DEGREE : B. TECH ECE
7. PG DEGREE : VLSI SD
8. Ph.D (AREA OF THE PH.D WORK) : Communication Medium with VLSI
9. SUBJECTS TAUGHT :
- Digital Signal Processing
 - Very large Scale System Integration
 - Digital System Design
 - Principles of Electronics communication systems
 - Embedded System Design
 - Satellite Communication

10. PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS:

- i. FPGA Implementation of Polyphase Filter Bank and Packetized Signal Processor
Publisher: International Journal of Innovative Technology (IJIT) Vol-01 Issue-05 ISSN 2321-8665, 2013

11. PAPER PUBLICATIONS IN INTERNATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	FPGA Implementation & Design of Arithmetic Coder	ICRIEAT-2017	978-93-86647-85-6
2	A Low Noise Triple Phase Sleep Signal Slew Rate Modulation used in MTCMOS Circuits	ICRIEAT-2017	978-93-86647-85-6
3	A Design of Low Power Adder operating on Effective Dynamic Data Ranges	ICRIEAT-2017	978-93-86647-85-6

12. PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	Quick Exchange of Extrinsic Information for Fast Data Throughput Machine-Jet Polyglot -	Advances in Engineering & Technology	978-93-82829-87-4

13. RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY: ---

14. RESEARCH GUIDANCE:--

15. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL: Department Coordinator

16. CONTRIBUTIONS AT THE COLLEGE LEVEL:--

17. REFRESHERCOURSES/WORKSHOPS/CONFERENCESATTENDED:

	<u>Attended:</u>			
	Name of the Course / Summer School	Place	Duration	HELD
1	IOT & Cloud Computing Techniquesfor Data Analysis withMOdern AI Tools	Hyd	3 Days	St.Martins Eng College
2	Emerging Trends in Engineering Research	Hyd	One week	Sphoorthy Engineering College
3	Introduction to financial Markets	Online	4 weeks	Course era
4	Programming for Everybody	online	5 Weeks	Course Era
5	Block chain Technology	Hyd	2days	KMIT-IT
6	Biomedical Imaging	Hyd	2days	KMIT-CSE
7	Formal Verification: Significance in VLSI Desing Flow & System Verilog Asssertions(SVA)	Hyd	1 Day	GPREC
8	Recent trends in VLSI	HYD	1 Week	GRIET
9	5G Technology & Its Applications	Hyd	1 Day	Sri Vasavi Institute of engineering & Technology
10	Recent communication Tools and its Applications	Hyd	5days	St.Martins Engineering College
11				

12				
13				
14				

18. WORKSHOPS/ CONFERENCES/SEMINARS/FDP'sORGANIZED:

WORKSHOPS/ CONFERENCES/SEMINARS/FDP	Place	Duration	Sponsoring Agency

19. MEMBERSHIP OF PROFESSIONAL BODIES: ----

20. ANY OTHER CONTRIBUTION: