KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY
Narayanaguda, Hyderbad-500029

FACULTY PROFILE

Photo

1. **NAME**   : K.SUSHMA
2. **JNTUH REGISTRATION ID**   : 87150405-102437
3. **DATE OF BIRTH**   : 23/10/1983
4. **DESIGNATION**   : ASSISTANT PROFESSOR
5. **YEARS OF EXPERIENCE**   : 8YEARS
6. **UG DEGREE**   : B.TECH(ECE)
7. **PG DEGREE**   : M.TECH(VLSI SYSTEM DESIGN)
8. **Ph.D(AREA OF THE PH.D WORK)** :
9. **SUBJECTS TAUGHT**   :

> ➢ Digital Logic Design
>
> ➢ Switching Theory and logic design
>
> ➢ Computer Organisation
>
> ➢ Electronic devices and circuits
>
> ➢ Digital design through verilog

10. PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS:

11. PAPER PUBLICATIONS IN INTERNATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1			

12. PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.

13. RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY:

14. RESEARCH GUIDANCE:

15. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL:

16. CONTRIBUTIONS AT THE COLLEGE LEVEL:

17. REFRESHER COURSES/ WORKSHOPS/CONFERENCES ATTENDED:

	Attended:			
	Name of the Course / Summer School	Place	Duration	Sponsoring Agency
1	Channel Coding Techniques	GNITS	6Days	AICTE
2	Advance Front End VLSI System Prototyping Using FPGA's	KMIT	1week	IETE
3	Research Methodology	SVIT	2Days	SVIT
4				
5				
6				

18. WORKSHOPS/ CONFERENCES/SEMINARS/FDP's ORGANIZED:

WORKSHOPS/ CONFERENCES/SEMINARS/FDP	Place	Duration	Sponsoring Agency

19. MEMBERSHIP OF PROFESSIONAL BODIES: **Member OF CSI**

20. ANY OTHER CONTRIBUTION: