

KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY

Narayanaguda,Hyderabad-500029

FACULTY PROFILE

photo



- 1.NAME :K RAJESH KUMAR
- 2.JNTUHREGISTRATIONID : 42150406-162206
3. DATE OFBIRTH :02/11/1983
4. DESIGNATION : Assistant Professor
5. YEARSOFEXPERIENCE : 5 years 6 months
6. UGDEGREE : B.Tech(Electronics and Communication Engineering)(2005)
7. PGDEGREE : M.Tech(Systems and Signal Processing)(2015)
8. Ph.D(AREAOFTHEPH.DWORK):
9. SUBJECTS TAUGHT :
- Electronic devices and circuit
 - Electronic circuit analysis
 - Principles of communications
 - Switching Theory and logic design
 - Basic electrical engineering
 - Analog communications

10. PAPERPUBLICATIONS IN INTERNATIONAL JOURNALS:

- i. Design of folded cascode operational amplifier using 1.8micron CMOS technology January-2018
www.jetir.org,ISSN:2349-5162, Vol.5, Issue 1, page no.464-466, 1.K RAJESH KUMAR 2.V PRAVEENA
3 J KAMAL VIJETHA 4.G HARIKRISHNA 5.BHAVANI KINNAR.
- ii. Advanced Design and Comparison of Multi-Channel UART. © 2017 IJAREEIE |Vol. 6, Issue 8, August
2017| ISSN (Print) : 2320 – 3765, ISSN (Online): 2278 – 8875 1. A.N.Sai Chakravarthy, 2.K.Anil Kumar,
3. K.Rajesh Kumar,4.S.Gangadhar.
- iii. A Novel Image Super-Resolution,IJIRCCE,May 2016,Vol. 4, Issue 5, PAGE NO.9798 -9804 ,
ISSN(Online): 2320-9801, ISSN (Print) : 2320-9798 1.K RAJESH KUMAR 2. A N SAI
CHAKRAVARTHY,
- iv.

11. PAPERPUBLICATIONS IN INTERNATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.

1			
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12. PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.

13. RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY:

14. RESEARCH GUIDANCE:

15. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL:

16. CONTRIBUTIONS AT THE COLLEGE LEVEL:

17. REFRESHER COURSES/WORKSHOPS/CONFERENCES ATTENDED:

	<u>Attended:</u>			
	Name of the Course / Summer School	Place	Duration	Sponsoring Agency
1	Programming for Everybody (Getting Started with Python)	Online	7 weeks	COURSERA (University of Michigan)
2	Digital circuits	online	12 weeks	NPTEL IIT Kharagpur
3	Microelectronics : Devices to circuits	online	12 weeks	NPTEL IIT Roorkee
4	Digital Electronic circuits	online	12 weeks	NPTEL IIT Kharagpur

18. WORKSHOPS/ CONFERENCES/SEMINARS/FDP'sORGANIZED:

WORKSHOPS/ CONFERENCES/SEMINARS/FDP	Place	Duration	Sponsoring Agency
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19. MEMBERSHIPOFPROFESSIONALBODIES:

20. ANY OTHERCONTRIBUTION: