

KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY

Narayanaguda,Hyderabad-500029

FACULTY PROFILE

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- 1. NAME : THUMMANAPALLY MANUSHA**
- 2. JNTUH REGISTRATION ID : 9596-150512-210600**
- 3. DATE OF BIRTH : 22-08-1991**
- 4. DESIGNATION : ASSISTANT PROFESSOR**
- 5. YEARS OF EXPERIENCE : 5 YEARS 5 MONTHS**
- 6. UG DEGREE : B.TECH (ECE)**
- 7. PG DEGREE : M.TECH (WIRELESS AND MOBILE COMMUNICATIONS)**
- 8. Ph.D (AREA OF THE PH.D WORK):**
- 9. SUBJECTS TAUGHT :**
 - ANALOG COMMUNICATIONS
 - CELLULAR MOBILE COMMUNICATIONS
 - PRINCIPLES OF COMMUNICATIONS
 - WIRELESS COMMUNICATIONS
 - ELECTRONIC DEVICES AND CIRCUITS
 - ELECTRONIC CIRCUIT ANALYSIS
 - ANALOG AND DIGITAL ELECTRONICS
 - CONTROL SYSTEMS
 - COMPUTER ORGANIZATION
- 10. PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS:**
 - i.** Analysis of F-OFDM in 5G Wireless Communication
 - ii.** Design of low voltage low power high gain full swing operational amplifier
 - iii.** Quantifying the Quality of Facial Image in a Real Time Environment

11. PAPER PUBLICATIONS IN INTERNATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	Analysis of F-OFDM in 5G Wireless Communication	AUG-2019 IJRASET	ISSN:2321-9653
2	Design of low voltage low power high gain full swing operational amplifier	JUNE-2017 IJEDR	ISSN:2321-9939
3	Quantifying the Quality of Facial Image in a Real Time Environment	JUNE 2016 IJESC	ISSN 2321-3361

PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.

12. RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY:**13. RESEARCH GUIDANCE:****14. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL:** Subject Co-Ordinator, Alumni Committee**15. CONTRIBUTIONS AT THE COLLEGE LEVEL:** Member of National service scheme Committee**16. REFRESHER COURSES/WORKSHOPS/CONFERENCES ATTENDED:**

<u>Attended:</u>				
	Name of the Course / Summer School	Place	Duration	Sponsoring Agency
1	Programming For Everybody (Getting Started With Python)	Hyderabad	8 Weeks	KMIT
2	Digital circuits (NPTEL)	Hyderabad	12 Weeks	KMIT
3	Channel coding techniques	Hyderabad	1 Week	GNITS
4	MAT LAB EXPO 2018	Hyderabad	1 Day	HCC
5	Advanced Front End VLSI systems prototyping using FPGA'S	Hyderabad	1 Week	KMIT

6	VLSI Design using FinFET	Hyderabad	1 Week	KMIT
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17. WORKSHOPS/ CONFERENCES/SEMINARS/FDP's ORGANIZED:

WORKSHOPS/ CONFERENCES/SEMINARS/FDP	Place	Duration	Sponsoring Agency
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18. MEMBERSHIP OF PROFESSIONAL BODIES: Member of IETE

19. ANY OTHER CONTRIBUTION:

