

KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY

Narayanaguda, Hyderabad-500029

FACULTY PROFILE



1. NAME : Pidathala Sagar Babu
2. JNTUH REGISTRATION ID : 73150405-132630
3. DATE OF BIRTH : 01-07-1985
4. DESIGNATION : Assistant Professor
5. YEARS OF EXPERIENCE : 9 years
6. UG DEGREE : B.Tech-ECE
7. PG DEGREE : M.Tech- VLSI & ES
8. Ph.D (AREA OF THE PH.D WORK): ---
9. SUBJECTS TAUGHT :
 - Analog Electronics
 - Microprocessors & Microcontrollers
 - VLSI Design
 - Analog and Digital Electronics
 - Basic Electrical and Electronics Engineering
 - Etc..

10. PAPER PUBLICATIONS IN INTERNATIONAL JOURNALS:

1. P.Sagar Babu, Dr.B.K.Madhavi, Dr.K.Lal Kishore "A Survey on alternative to MOS Device structures beyond 32nm Technology", International Journal of VLSI and Embedded Systems(IJVES), April 2014, Volume-5, Article 04298

11. PAPER PUBLICATIONS IN INTERNATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	<i>Implementation of High speed parallel VLSI Architecture for Circular Convolution based on FNT</i> , Volume-II, page no.s: 299-303	International Conference on Nanoscience, Engineering & Advanced Computing (ICNEAC-2011), June 2011	---
2	<i>Design and Implementation of Digital Down Converter (DDC) with CIC filters for SDR Applications</i> , Volume-III, page no.s: 318-322	International Conference on Nanoscience, Engineering & Advanced Computing (ICNEAC-2011), June 2011	----

12. PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	<i>Design and Implementation of beat to beat blood pressure measurement using PWTT method</i>	National Conference on Emerging trends in signal processing & Embedded Systems on 9 th and 10 th Feb. 2012	---

13. RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY:-**14. RESEARCH GUIDANCE: ---****15. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL:** Professional Activities Committee Member**16. CONTRIBUTIONS AT THE COLLEGE LEVEL:** IEEE Student Branch Counselor**17. REFRESHER COURSES/WORKSHOPS/CONFERENCES ATTENDED:**

	<u>Attended:</u>			
	Name of the Course / Summer School	Place	Duration	Sponsoring Agency
1	Nanoscale Devices and Circuits	NIT Warangal	One week	E & CT Academy
2	Advanced Front End VLSI Systems Prototyping Using FPGAs	KMIT, Hyderabad	One week	KMIT

18. WORKSHOPS/ CONFERENCES/SEMINARS/FDP's ORGANIZED:

WORKSHOPS/ CONFERENCES/SEMINARS/FDP	Place	Duration	Sponsoring Agency

19. MEMBERSHIP OF PROFESSIONAL BODIES: ---**20. ANY OTHER CONTRIBUTION: ----**