KESHAV MEMORIAL INSTITUTE OF TECHNOLOGY
Narayanaguda,Hyderbad-500029

FACULTY PROFILE

photo

1. **NAME**   :M.Rajitha
2. **JNTUH REGISTRATION ID**   :60150406-160447
3. **DATE OF BIRTH**   :17-08-1983
4. **DESIGNATION**   :ASSISTANT PROFESSOR
5. **YEARSOFEXPERIENCE**   :8 years4 Months
6. **UGDEGREE**   :B.TECH(ELECTRONICS AND COMMUNICATION ENGINEERING)

7. **PGDEGREE**   :M.TECH(EMBEDDED SYSTEM DESIGN)
8. **Ph.D(AREAOFTHEPH.DWORK):**
9. **SUBJECTS TAUGHT**   :

   - ANALOG AND DIGITAL ELECTRONICS
   - LINEAER INTEGRATED CIRCUIT APPLICATIONS
   - COMPUTER ORGANIZATION
   - LINEAR DIGITAL IC APPLICATIONS
   - PULSE AND DIGITAL CIRCUITS
   - DIGITAL LOGIC AND PULSE CIRCUITS
   - SWITHCING THEORY AND LOGIC DESIGN
   - DIGITAL LOGIC DESIGN
   - CELLULAR MOBILE COMMUNICATION
   - ELECTRONIC DEVICES AND CIRCUITS

10. **PAPERPUBLICATIONS IN INTERNATIONAL JOURNALS:**

11. PAPER PUBLICATIONS IN INTERNATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.
1	Low Power VLSI design of 4-Bit CEPAL adder using Cadence 0.18μM CMOS Full adder, **Volume 6**	**2018 IJCRT**, **1 March 2018**	**ISSN: 2320-2882**

12. PAPER PUBLICATIONS IN NATIONAL CONFERENCES:

S. No.	Title with Page Nos.	Details of Conference Publication	ISSN/ISBN No.

13. RESEARCH PROJECTS UNDERTAKEN WITH NAME OF THE SPONSORING AGENCY:

14. RESEARCH GUIDANCE:

15. CONTRIBUTIONS AT THE DEPARTMENTAL LEVEL: Class Teacher, Subject Co-Ordinator, Student mentor, Incharge of Comprehesive ViVa(2018-2019)

16. CONTRIBUTIONS AT THE COLLEGE LEVEL: Member of Self Learning Commitee

17. REFRESHER COURSES/ WORKSHOPS/ CONFERENCES ATTENDED:

	Name of the Course / Summer School	Place	Duration	Sponsoring Agency
1	Programming For Everybody(Getting Started With Python)	Hyderabad	8Weeks	KMIT
2	Opamp Applications(Nptel)	Hyderabad	12 Weeks	KMIT
3	Digital Electronic Circuits(Nptel)	Hyderabad	12 Weeks	KMIT
4	WIPRO MISION 10X	Hyderabad	1 WEEK	SVIT

18. WORKSHOPS/ CONFERENCES/SEMINARS/FDP'sORGANIZED:

WORKSHOPS/ CONFERENCES/SEMINARS/FDP	Place	Duration	Sponsoring Agency
National level quiz in Machine Learning	HYDERABAD	6/4/2020	KMIT
AN EMERGING PARADIGM OF LOW POWER COMPUTATIONAL VLSI DESIGN	HYDERABAD	16/06/2020	KMIT
REFRESHER COURSE ON PYTHON	HYDERABAD	08.06.2020-12.06.2020	KMIT
Online quiz on Basic Microprocessor and Micro Controller	HYDERABAD	6/1/2020	KMIT
AN EMERGING PARADIGM OF LOW POWER COMPUTATIONAL VLSI DESIGN	HYDERABAD		KMIT
Cellular Mobile Communication Networks	HYDERABAD	25-06-2018 to 30-06-2018	SELF
Advanced Front End VLSI Systems Prototyping Using FPGAs	HYDERABAD	05-07-2017 to 11-07-2017	KMIT
Low power VLSI Design of 4-bit CEPAL using Cadence 0.18um CMOS	HYDERABAD	01-03-2018	KMIT
VLSI Design Using FinFET	HYDERABAD	25-03-2017	KMIT
Embedded system design using ATMEL XMEGA A3BUXPLD and ARM based SAMD20XPLD	JNTUH	05-08-2014 to 06-08-2014	KMIT
Latest Advances in VLSI and Implementation using CAD Tools	HYDERABAD	20-02-2015 to 21-02-2015	KMIT
Advanced VLSI Program	K.L.UNIVERSITY(Vijayawada)		SELF
Computerized Tomography Medical and industrial Applications	K.L.UNIVERSITY(Vijayawada)	10-08-2013	SELF
Signal Processing and Communication Engineering Systems	K.L.UNIVERSITY(Vijayawada)	02-01-2015 to 03-01-2015	SELF

19. MEMBERSHIP OF PROFESSIONAL BODIES:

20. ANY OTHER CONTRIBUTION: