**PABBALA RAJASHEKAR**

Contact No. **:** +91-8125892467 E-mail: raja72shekar94@gmail.com

**CAREER OBJECTIVE**

To succeed in an environment of growth and excellence and self-development and help me to achieve organizational goal.

**EDUCATIONAL QUALIFICATION**

|  |  |  |  |
| --- | --- | --- | --- |
| **Qualification** | **Board/University** | **Year** | **Percentage** |
| B.Tech (ECE)  Intermediate  SSC | Vaageswari college of Engineering  Trinity Junior College  Sri Vidya Bharathi High School | 2011-2015  2009-2011  2008-2009 | 62%  63.4%  79.5% |

**TECHNICAL SKILLS**

* **Operating System**: Windows Server administration 2012
* **Programming Language:** C language
* Good knowledge in CCNA, MCSE, Linux (RHCE), Computer Hardware,ehce.

**CO-CURRICULAR ACTIVITIES**

* N.S.S volunteer.
* Web Casting Volunteer in 2014-elections.

**ACHIEVEMENTS**

* Participated in District level science fair and got second prize in SSC.
* Participated in National Level fest Abhyudaya-13 in Project Expo and Got 2nd price.
* Participate in EMBLAZON’13 National Level Technical Symposium Conducted at JNTUH Nachupally.

**INTERPERSONAL SKILLS**

* + Ready to Accept New Challenges, willing to learn.
  + Ability to rapidly build relationship and set up trust.
  + Hard worker and punctual.
  + Positive and Polite Attitude, Quick Learner.

**ACADEMIC PROJECTS**

**MINI - PROJECT:**

**Title :** Gas Alerting System in Industries.

**Project Type :** Embedded

**Description :** Gas which used in many applications because of its desirable properties like home, hostels, industries, vehicles so we can use this device if there is any gas leakage exceeds certain level this system automatically alert the people at home and any domestic places by activating the LED, Buzzer and take the necessary action of preventing the gas leakage.

**MAJOR - PROJECT:**

**Title :** Design of High speed DDR3 SDRAM Controller.

**Project Type :** VLSI

**Description :** In this project we have designed a High speed DDR3 SDRAM Controller with 64-bit data transfer which synchronizes the transfer of data between DDR RAM and External peripheral devices like host computer, laptops and so on. The advantages of this controller compared to SDR SDRAM, DDR1 SDRAM and DDR2 SDRAM is that it synchronizes the data transfer, and the data transfer is twice as fast as previous, the production cost is also very low.

**PERSONAL DETAILS**

* **Father’s Name :** Pabbala Ilaiah
* **Present Address :** H.No. 10-5-167/a, Kisan nagar,

Karimnagar, Telagana State, 505001.

* **Date of Birth :** 05-03-1994
* **Language Known :** English, Telugu.
* **Marital Status :** Single
* **Nationality :** Indian
* **Interest & Hobbies :** e-learning, solving puzzles, playing chess.

**DECLARATION**

I do here by declare that the particulars of information mentioned and the facts stated here in above are true, correct and complete to the best of my knowledge and belief**.**

**Place: Hyderabad,**

**Date: 06/09/2015. ( P.RAJASHEKAR)**