**RESUME**

**Y. Leela Kishore**

**E-mail : Yallanki.leelakishore@gmail.com**

**Contact no: +91- 9493141357**

**CAREER OBJECTIVE :**

To work in a competitive environment that effectively utilizes my analytical, leadership and organizational skills to conceive and achieve solutions. The solutions which help the organization in not only meeting its targets, but also allowing it to grow, thereby, enhancing my own skills as an individual and as a key player in the organization's development.

**ACHIEVEMENTS:**

* Received certificate in science talent test at school level.

**STRENGTHS:**

* Good communication and interpersonal skills
* Good team player
* Hard work is always promised

**ACADEMIC PROFILE:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **DEGREE** | **NAME OF INSTITUTION** | BOARD/  UNIVERSITY | **YEAR OF PASSING** | **AGGREGATE**  **(%)** |
| Btech  (ECE) | Audisankara college of Engineering & Technology | JNTUA  University | 2015 | **63** |
| Intermediate | Satyam Junior College | Board of Intermediate Education | 2011 | **67** |
| S.S.C | Teja High School | Board of Secondary Education | 2009 | **84.6** |

**TECHNICAL PROFILE:**

* Operating Systems : Windows 7,8, XP
* Programming Languages : J2SE, Basics in c
* Testing Tools : Manual Testing, Automation QTP
* Networking : Windows(MCSE)
* Other Applications **:** MS Office.

**PROJECT PROFILE:**

|  |  |
| --- | --- |
| PROJECT TITLE | A high speed binary floating point multiplier using DADDA multiplier |
| DOMAIN | VLSI |
| ENVIRONMENT | Xilinx |
| TEAM SIZE | 5 |
| ROLE | Involved Everywhere |
| BRIEF DESCRIPTION | This paper presents a high speed binary floating point multiplier based on dadda algorithm. To improve speed multiplication of mantissa is done using dadda multiplier replacing carry save multiplier. To give more precision rounding is not implemented for mantissa application. The multiplier is implemented using VHDL, HDL and it is targeted for Xilinx virtex-5 FPGA. The multiplier is compared with Xilinx floating point multiplier core. |

**PERSONAL PROFILE:**

**Name** : Y. Leela Kishore

**Gender :** Male

**Father’s Name** **:** Y. Hanumantha Rao

**Date of Birth :** 29th March 1994

**Nationality :** Indian

**Languages Known :**  English, Telugu

**Permanent Address :** Sullurupeta,

SPSR Nellore, 524121,

Andhra Pradesh.

**DECLARATION:**

I, Y. Leela Kishore, do hereby confirm that the information given above is true to the best of my knowledge.

**Date:**  **(Y. Leela Kishore)**

**Place:**