

**RESUME**

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**Career Objective:** I wish to work in a great working environment where I can widen my expertise, learn every day about new skills and share my knowledge and experience to serve the organization.

**Technical Skills:**

1. Computer Networking Softwares Worked with: **Putty, Hyperterminal, Minicom**.
2. Networking Protocols Worked with: **IP, ARP, ICMP, TCP, UDP, SNMP**.
3. Routing Protocols Worked with: **Static, Default, Dynamic(RIP, EiGRP, OSPF)**.
4. Lab Experience of configuring Cisco Routers with: **Static NAT, PAT, HSRP**.
5. Lab Experience of configuring Cisco Switches with: **VLAN, VTP, STP, Etherchannel**.
6. Worked in **Xilinx ISE, Altera Quartus** with **VHDL & Verilog coding**.
7. Worked in **AVR Studio 4** with **Embedded C coding**.
8. Computer Fundamentals: **Microsoft Office, 2003, 2007, 2010, Outlook.**
9. Work Environment: **Windows XP, Windows 7, 8, Ubuntu.**
10. Programming Languages: **C, C++, Core JAVA**
11. DBMS: **RDBMS**
12. Machine Level Language: **Matlab**

**Trainings Undertaken:**

1. Undergone **CCNA** Training in Zoom Technologies, Hyderabad in Nov,2015.
2. Three months **Diploma in RF Drive Test** course from Istep, Bhubaneswar from July, 2011 to October, 2011.
3. One month **VLSI design** course from CTTC, Bhubaneswar in July, 2010.

**Experience:**

1. Worked as Network Engineer at In2IT Technologies Pvt. Ltd.([www.in2ittech.com](http://www.in2ittech.com)) from 09.04.2014 to 09.10.2015.

**Project:** OSWAN(Odisha State Wide Area Network).

**Service Area: ITES**(Information Technology Enabled Services)

**Client:** Government of Odisha

**Period:** 9th April, 2014 to 9th October, 2015

**Technical Environment: Hyperterminal, Putty**

**Work Details:**

* Configured **Cisco Routers(1900, 2600, 2800)**.
* Worked with **Cisco Switches(2950, 3550, Catalyst 500 Series)**.
* Worked with Modems**(CSU/DSU Devices) i.e. (V.35 modems and G.703 modems)**.
* Installed and Managed **LAN, WAN on Leased Line Connection**.
* Installed and Managed **LAN on OFC Connection**.
* Worked on BSNL **Multiplexers, PCMs, MLLN Circuits**.

**Job Description:**

* **Up gradation and restoration of IOS** and Performing **Password Recovery in Routers and Switches**.
* Worked with the **protocols** like **IP, ARP, ICMP, TCP, SNMP** on work field.
* Worked with **Routing Protocols** like **Default, OSPF** on work field.
* Performed **Sub netting** for organization. Have knowledge of **VLSM, FLSM**.
* Configuring **Access Control Lists** giving access to inbound and outbound internet traffic.
* **Installed, Configured and supported Local and Wide Area Networks(LAN, WAN)** to maintain maximum network uptime.
* **Monitoring** Network Maintenance activities and ensuring prompt trouble shooting of network problems to achieve maximum up time of network.
* Have worked on project (OSWAN- Odisha State Wide Area Network) in **Multi-Vendor Environment**.
* Have worked in **Frame Relay Circuits** of the Telecom vendor BSNL.
* Analyze and diagnose malfunctions and problems related to computer equipment and software.
* Maintained computers, installed operating systems and programs on the computers and reassembled computer hardware devices and the other components that are needed to make these equipment work.
* Learning, installing, assembling, troubleshooting and configuring new software, hardware and other technical equipment, adapt to changes in technology.

**Acheivement:**

* Awarded as the best team in the first phase among the three phases of implementation of the network.

1. One year Graduate Apprentice Training from Hindustan Aeronautics Limited, Sunabeda (<http://hal-india.com/Engine%20Division%20Koraput/M__138>) from 01.12.2012 to 30.11.2013.
2. **Project:** Drawing circuit of BPR-88 Cards

**Service Area:** Electronics Circuit Design

**Team Size: 2**

**Client:** Hindustan Aeronautics Limited

**Period:** 1st July, 2013 to 30th Nov, 2013

**Working Environment: Proteus 7.4 Sp3**

**Work Details:**

* Helped Digitization of BPR-88 through Proteous 7.3SP4 Software.
* Checked the circuits for different input values needed to see he outputs at different altitudes of engine during flight.

**Job Description:**

* To draw circuit diagrams of BPR-88 Cards.
* Perform Maintenance of the circuits’ cards of BPR-88. i.e. Soldering and de-soldering.

1. **Project:** Inventory for small parts and their preservation

**Service Area:** Financial Budget Planning

**Team Size: 15**

**Client:** Hindustan Aeronautics Limited

**Period:** 1st May, 2013 to 30th June, 2013

**Work Details:**

* Inventory of material parts of Aero-Engines R-25, RD-33, R-29 B.
* Preservation of material parts of Aero-Engines R-25, RD-33, R-29 B.

**Job Description:**

* Finding out the consumption of a part in a definite period of time. Parts are counted by means of both electronic weighing machine and manually counted by man power. The consumption so found is compared with the previous period of inventory.
* The corroded parts undergone through special thermal treatment in oil bath and are cleaned by thermal vibration. Then they are put in to preservation oil tank like Glycerin for reuse.

1. **Project:** Pre-stripe Survey Report preparation

**Service Area:** Repair Services

**Team Size: 2**

**Client:** Hindustan Aeronautics Limited

**Period:** 7th Dec, 2012 to 30th Apr, 2013

**Work Details:**

* Preparing the PSSR Report of R-25 Aero engines.
* Maintaining the status data of the parts of engine in ERP Software.

**Job Description:**

* Taking an overview of the R-25 Engines’ outer and inner side.
* Feed the faults found on the parts of engine in ERP software and transmit the same data to relevant shops for necessary repairing.
* To instruct the View room person to precisely check the parts after repair.
* To instruct the Assembly Bay for the Final integration of the Aero Engine Parts.

**Academic Projects:**

1. **Project:** PM DC motor speed control using classical PI controller

**Team Size: 4**

**Submitted To:** Satyasai Engineering College, Balasore

**Period:**1st May, 2011 to 15th June, 2011

**Working Environment:** AVR Studio 4

**Language Used: Embedded C**

**Work Details:**

DC series motors are used for traction, electric locomotive, trolley system, crane hoists and conveyor. All these work require frequent speed control for preparation of job. Among all, the PMW (Pulse Width Modulation) method is the best electronics method for speed control. A DC source is used to run DC motors. In this project a PMW modulator is designed using simple operational amplifiers.

**Job Description:**

* Write the embedded C Codes that run the Motor at different speeds with different input values.
* Optimize the outputs for different input values.

1. **Project :** Over & under voltage relay protection system

**Team Size: 4**

**Submitted To:** Satyasai Engineering College, Balasore

**Period:**1st Jan, 2011 to 28th Feb,2011

**Working Environment:** AVR Studio 4

**Language Used: Embedded C**

**Work Details:**

Industrial gadget safety is a major concern in all the industries. As all the human labor are being replaced by automated system of monitoring and safe guarding the entire operational processes in major industries, over and under voltage relay has been a very good tool for the safety maintenance. Relay circuits are implemented before every gadget which needs to be secured with a bridge rectifier.

**Job Description:**

* Prepare the Embedded C Code to run the setup of Over and Under Voltage Relay Protection System.
* Test the logic enabled security to secure house hold electricity run devices by the project.

1. **Project:** Traffic light controller using Verilog coding

**Team Size: 7**

**Submitted To:** Central Tool room &Training Center, Bhubaneswar

**Period:**1st July, 2010 to 31st July, 2010

**Working Environment:** Xilinx ISE

**Language Used: Verilog**

**Work Details:**

The job was to implement a 4 Lane traffic light controller. The use of hardware description language –Verilog has made it easy to make with just a 555 timer and a ULN28xx Driver (High voltage high current Darlington array suited for interfacing between low-level logic circuitry and multiple peripheral power loads). The coding is then done using the Xilinx ISE software.

**Job Description:**

* Perform the Verilog Coding for the Four lane Traffic Light Controller and its Simulation.
* Check the performance of the actual outcomes and optimize it.

**Educational Qualification:**

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| --- | --- | --- | --- |
| **Course** | **Board/University** | **Year of Passing** | **Percentage/CGPA** |
| Bachelor of Technology in Electronics and Telecommunication Engineering | Biju Patnaik University of Technology, Rourkela | 2011 | 7.25 CGPA or 67.50 % |
| Intermediate | Council of Higher Secondary Education, Orissa | 2007 | 50.07% |
| SSC | Board of secondary Education, Orissa | 2004 | 59.33% |

**Personal Information**

Permanent Address : At/Po- Tarbha, Dist- Subarnapur, State- Odisha, Pin-767016.

D.O.B. : 05-11-1988

Marital Status : Single

Religion : Hindu

Hobbies : Movies, Music

**Declaration**

I hereby declare that all the facts given above are true to the best of knowledge.