## Lecture 22: Memory

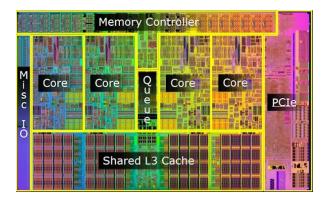
# CS4787/5777 — Principles of Large-Scale Machine Learning Systems

- Last time we talked about parallel computing in machine learning, which allows us to take advantage of the parallel capabilities of our hardware to substantially speed up training and inference.
- This is an instance of the general principle: Use algorithms that fit your hardware, and use hardware that fits your algorithms.
- But compute is only half the story of making algorithms that fit the hardware.
- How data is stored and accessed can be just as important as how it is processed.
- This is especially the case for machine learning tasks, which often run on very large datasets that can push the limits of the memory subsystem of the hardware.

## Today, we'll be talking about how memory affects the performance of the machine learning pipeline.

#### How do modern CPUs handle memory?

CPUs have a deep cache hierarchy. In fact, many CPUs are mostly cache by area.



The motivation for this was the ever-increasing gap between the speed at which the arithmetic units on the CPU could execute instructions and the time it took to read/write data to system memory.

Without some faster cache to temporarily store data, the performance of the CPU would be bottlenecked by the cost of reading and/or writing to RAM after every instruction.

#### A simplified view of memory on a CPU

This is what the "shared memory" programming model sees.



#### But CPUs also have caches

Caches are small and fast memories that are located physically on the CPU chip, and which mirror data stored in RAM so that it can be accessed more quickly by the CPU.



### The usual setup of memory on a CPU

- a fast L1 cache (typically about 32KB) on each core
- a somewhat slower, but larger L2 cache (e.g. 256 KB) on each core
- an even slower and even larger L3 cache (e.g. 2 MB/core) shared among cores
- DRAM off-chip memory
- Persistent storage a hard disk or flash drive

### A model of a multi-socket computer

Multiple CPU chips on the same motherboard communicate with each other through physical connections on the motherboard.



### The full view across multiple machines

Multiple CPU chips on the same motherboard communicate with each other through physical connections on the motherboard.



One important thing to notice here:

As we zoom out, much more of this diagram is "memory" boxes than compute boxes.

Hand-wavy consequence: as we scale up, the effect of memory becomes more and more important.

Another important take-away:

#### Memory has a hierarchical structure

- Memories lower in the hierarchy are faster, but smaller
- Memories higher in the hierarchy are larger, but slower, and are often shared among many compute units

# Two ways to measure performance of a part of the memory hierarchy.

- Latency: how much time does it take to access data at a new address in memory?
- **Throughput** (a.k.a. bandwidth): how much data total can we access in a given length of time?

We saw these metrics earlier when evaluating the effect of parallelism.

Ideally, we'd like all of our memory accesses to go to the fast L1 cache, since it has high throughput and low latency.

What prevents this from happening in a practical program?

Result: the hardware needs to decide what is stored in the cache at any given time.

It wants to avoid, as much as possible, a situation in which the processor needs to access data that's not stored in the cache—this is called a **cache miss**.

Hardware uses two heuristics:

- The principle of temporal locality: if a location in memory is accessed, it is likely that that location will be accessed again in the near future.
- The principle of spatial locality: **if a location in memory is accessed, it is likely that other nearby locations will be accessed in the near future.**

#### **Memory Locality**

Temporal locality and spatial locality are both types of **memory locality**.

 We say that a program has good spatial locality and/or temporal locality and/or memory locality when it conforms to these heuristics.

• When a program has good memory locality, it makes good use of the caches available on the hardware.

• In practice, the throughput of a program is often substantially affected by the cache, and can be improved by increasing locality.

#### **Prefetching**

A third important heuristic used by both the hardware and the compiler to improve cache performance is **prefetching**.

Prefetching loads data into the cache **before it is ever accessed**, and is particularly useful when the program or the hardware can predict what memory will be used ahead of time.

Question: What can we do in the ML pipeline to increase locality and/or enable prefetching?

- Access training exmaples in the order they appear in memory
  - Prefetch the training examples (prefetch the ones we're going to be using next)
- Efficient matrix multiplications

DEMO

A matrix multiply of  $A\in\mathbb{R}^{m\times n}$  and  $B\in\mathbb{R}^{n\times p}$ , producing output  $C\in\mathbb{R}^{m\times p}$ , can be written by running

$$C_{i,k} += A_{i,j} \cdot B_{j,k}$$

for each value of  $i \in \{1, \dots, m\}$ ,  $j \in \{1, \dots, n\}$ , and  $k \in \{1, \dots, p\}$ . The natural way to do this is with three for loops. But what order should we run these loops? And how does the way we store A, B, and C affect performance?

```
In [1]: using Libdl # open a dynamic library that links to the C code
         tm lib = Libdl.dlopen("demo/test memory.lib");
         function test_mmpy(loop_order::String, Amaj::String, Bmaj::String, Cmaj::String
In [2]:
             @assert(loop_order in ["ijk","ikj","jki","jik","kij","kji"])
             @assert(Amaj in ["r","c"])
             @assert(Bmaj in ["r","c"])
             @assert(Cmaj in ["r","c"])
             f = Libdl.dlsym(tm_lib, "test_$(loop_order)_A$(Amaj)B$(Bmaj)C$(Cmaj)")
             ccall(f, Float64, (Int32, Int32, Int32, Int32), m, n, p, num_runs)
         end
Out[2]: test_mmpy (generic function with 1 method)
In [11]: d = 512;
         num runs = 10;
         measurements = []
         for loop order in ["ijk","ikj","jki","jik","kij","kji"]
```

for Am in ["r","c"]

time elapsed: 0.295000 seconds time elapsed: 0.283000 seconds time elapsed: 0.299000 seconds time elapsed: 0.277000 seconds time elapsed: 0.278000 seconds time elapsed: 0.276000 seconds time elapsed: 0.283000 seconds time elapsed: 0.278000 seconds time elapsed: 0.273000 seconds time elapsed: 0.276000 seconds average time: 0.281800 seconds digest: 1.549011e+27 time elapsed: 0.439000 seconds time elapsed: 0.424000 seconds time elapsed: 0.429000 seconds time elapsed: 0.431000 seconds time elapsed: 0.430000 seconds time elapsed: 0.425000 seconds time elapsed: 0.427000 seconds time elapsed: 0.428000 seconds time elapsed: 0.425000 seconds time elapsed: 0.429000 seconds average time: 0.428700 seconds digest: 1.546028e+27 time elapsed: 0.306000 seconds time elapsed: 0.306000 seconds time elapsed: 0.304000 seconds time elapsed: 0.304000 seconds time elapsed: 0.302000 seconds time elapsed: 0.306000 seconds time elapsed: 0.303000 seconds time elapsed: 0.303000 seconds time elapsed: 0.305000 seconds time elapsed: 0.304000 seconds average time: 0.304300 seconds digest: 1.547263e+27 time elapsed: 0.515000 seconds time elapsed: 0.518000 seconds time elapsed: 0.514000 seconds time elapsed: 0.536000 seconds time elapsed: 0.516000 seconds time elapsed: 0.513000 seconds time elapsed: 0.546000 seconds time elapsed: 0.513000 seconds time elapsed: 0.518000 seconds time elapsed: 0.514000 seconds average time: 0.520300 seconds digest: 1.547960e+27 time elapsed: 0.274000 seconds time elapsed: 0.277000 seconds time elapsed: 0.274000 seconds time elapsed: 0.273000 seconds

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time elapsed: 0.273000 seconds time elapsed: 0.277000 seconds time elapsed: 0.276000 seconds time elapsed: 0.273000 seconds time elapsed: 0.277000 seconds time elapsed: 0.272000 seconds average time: 0.274600 seconds digest: 1.547434e+27 time elapsed: 0.424000 seconds time elapsed: 0.431000 seconds time elapsed: 0.427000 seconds time elapsed: 0.431000 seconds time elapsed: 0.429000 seconds time elapsed: 0.425000 seconds time elapsed: 0.427000 seconds time elapsed: 0.427000 seconds time elapsed: 0.425000 seconds time elapsed: 0.434000 seconds average time: 0.428000 seconds digest: 1.550654e+27 time elapsed: 0.304000 seconds time elapsed: 0.306000 seconds time elapsed: 0.303000 seconds time elapsed: 0.307000 seconds time elapsed: 0.309000 seconds time elapsed: 0.303000 seconds time elapsed: 0.310000 seconds time elapsed: 0.306000 seconds time elapsed: 0.304000 seconds time elapsed: 0.304000 seconds average time: 0.305600 seconds digest: 1.545477e+27 time elapsed: 0.521000 seconds time elapsed: 0.520000 seconds time elapsed: 0.516000 seconds time elapsed: 0.522000 seconds time elapsed: 0.519000 seconds time elapsed: 0.537000 seconds time elapsed: 0.520000 seconds time elapsed: 0.519000 seconds time elapsed: 0.519000 seconds time elapsed: 0.516000 seconds average time: 0.520900 seconds digest: 1.546651e+27 time elapsed: 0.193000 seconds time elapsed: 0.205000 seconds time elapsed: 0.210000 seconds time elapsed: 0.210000 seconds time elapsed: 0.191000 seconds time elapsed: 0.178000 seconds time elapsed: 0.174000 seconds time elapsed: 0.174000 seconds

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```
kji_crc --> 0.0501
         jki_crc --> 0.0517
         jki_ccc --> 0.0529
         kji_ccc --> 0.0642
         kij_rcc --> 0.117000000000000002
         ikj_rcc --> 0.1182
         kij rcr --> 0.1599
         ikj_rcr --> 0.1611
         ikj_rrc --> 0.17930000000000001
         kij_ccc --> 0.1841999999999997
         ikj_rrr --> 0.1884999999999995
         kji rrc --> 0.1901999999999998
         kji_rcc --> 0.1905
         kij_ccr --> 0.19090000000000001
         ikj ccr --> 0.19260000000000005
         kij_rrc --> 0.1927
         kij_rrr --> 0.1955
         ikj_ccc --> 0.1967
         jki_rrc --> 0.211
         jki rcc --> 0.2164
         ijk crr --> 0.27460000000000007
         jik_rrr --> 0.2769
         jik_ccr --> 0.2784000000000001
         ijk_rrr --> 0.28180000000000005
         jik_rcr --> 0.2883
         jik_crr --> 0.2928
         ijk rcr --> 0.3043
         ijk_ccr --> 0.3056
         kij_crr --> 0.3546
         ikj_crc --> 0.3625
         ikj_crr --> 0.36910000000000004
         kij_crc --> 0.3713
         kji_ccr --> 0.4217000000000001
         jki ccr --> 0.4226
         kji crr --> 0.4234999999999999
         jik_crc --> 0.4244
         jik_rrc --> 0.426000000000000005
         ijk_crc --> 0.4279999999999994
         ijk_rrc --> 0.42869999999999997
         jki crr --> 0.44770000000000004
         kji_rrr --> 0.5151
         kji_rcr --> 0.51539999999999999
         jik ccc --> 0.5169
         jik_rcc --> 0.5183000000000001
         ijk_rcc --> 0.5203
         ijk_ccc --> 0.5208999999999999
         jki rrr --> 0.5619
         jki rcr --> 0.5671000000000002
In [13]: | maximum(t for (m,t) in m_sorted) / minimum(t for (m,t) in m_sorted)
Out[13]: 11.319361277445113
```

An over  $10 \times$  difference just from accessing memory in a different order!

 $\begin{bmatrix} a & b \\ c & d \end{bmatrix}$ 

[a,b,c,d]

[a,c,b,d]

#### Scan order

**Scan order** refers to the order in which the training examples are used in a learning algorithm.

As you saw in the programming assignment, using a non-random scan order is an option that can sometimes improve performance by increasing memory locality.

Here are a few scan orders that people use:

- Random sampling with replacement (a.k.a. random scan): every time we need a new sample, we pick one at random from the whole training dataset.
- Random sampling without replacement: every time we need a new sample, we pick one at random and then discard it (it won't be sampled again). Once we've gone through the whole training set, we replace all the samples and continue.
- **Sequential scan** (a.k.a. systematic scan): sample the data in the order in which it appears in memory. When you get to the end of the training set, restart at the beginning.
- **Shuffle-once**: at the beginning of execution, randomly shuffle the training data. Then sample the data in that shuffled order. When you get to the end of the training set, restart at the beginning.
- **Random reshuffling**: at the beginning of execution, randomly shuffle the training data. Then sample the data in that shuffled order. When you get to the end of the training set, reshuffle the training set, then restart at the beginning.

How does the memory locality of these different scan orders compare?

- Worst memory locality: random scan with and without resampling
- Okay memory locality: random reshuffling
- Very good: shuffle once
- Best: sequential scan

Two of these scan orders are actually statistically equivalent! Which ones?

Random sampling w/o replacement and random reshuffling

How does the statistical performance of these different scan orders compare?

random reshuffling = w/o replacement > shuffle once > sequential scan > random with replacement

## A good first choice when compute is light: shuffle once

Generally it performs quite well statistically (although it might have weaker theoretical guarantees), and it has good memory locality.

## Another good choice: without-replacement sampling

This is particularly good when you're doing some sort of data augmentation, since you can construct the without-replacement minibatches on-the-fly.

#### Memory and sparsity

How does the use of sparsity impact the memory subsystem?

Two major effects:

- Sparsity lowers the total amount of memory in use by the program.
- Sparsity lowers the memory locality.
  - Why? Accesses are not dense and so are less predictable.

What else can we do to lower the total memory usage of the machine learning pipeline?