TADIKAMALLA SAI BHARATH

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CAREER OBJECTIVE

To work in an environment that provides learning a experience and opportunity to employmy skills and grow in the area and thereby adding value to the organization.

EDUCATION QUALIFICATIONS

Year	Qualification	Institution	Marks Obtained
2017	B. E (ECE)	Sathyabama university	6.66 (CGPA)
2013	Board of Intermediate, AP.	Sri Gayathri Junior College	71.6 %
2011	Board of Secondary Education, AP.	Sri Chaitanya Techno School	66.5%

SKILLS

- ❖ **Programming languages :** CORE JAVA
- ❖ **Data Base** : Oracle 10g, RDBMS
- ❖ **Web Technologies** : HTML, CSS.

TRAINING PROJECT

- ❖ **Title** : **BANK BILL PAYMENT SYSTEM**.

- ❖ **Technologies :** JAVA**,**J2EE**,**Oracle 10g,HTML,CSS,JAVASCRIPT

- ❖ **Description** : Online bill payment system where the user needs to register bill payment and they can access various payment option like adding and modifying billers, making payment and payment history.

ACADEMIC PROJECT

- ❖ **Title** : **MULTIBAND ANTENNA DESIGN FOR WIRELESS APPLICATIONS**

- ❖ **Technologies :** Embedded systems.

- ❖ **Description :** In Design of an Embedded System that The hepta band inverted T-slot antenna is operated over a range of 1.7 GHz to 8.3 GHz. The resonant frequency is 1.95 GHz with the application of common wireless communication systems. The FR 4 Substrate material is used with a dielectric constant of 4.4 and loss tangent of 0.0001. As like the design procedure, the proposed slot antenna is resonated at 1.95 GHz and gets the maximum desired output. The feed width is 2mm.

PERSONAL SKILLS

- ❖ Ability to learn the things very quickly and to implement things learn.
- ❖ Have very good communication skills with an attitude and interest take any challenging assignment
- ❖ Excellent problem-solving skills with a strong Technical background and good interpersonal Skills.

AREA OF INTEREST

- ❖ CORE JAVA.
- ❖ RDBMS.
- ❖ Intel 8085 Microprocessor.

CO-Curricular Activities

- ❖ Implant training in BSNL Regional telecom training center,CHENNAI .
- ❖ Industrial visit – RTTC ,CHENNAI .
- ❖ Workshop in Embedded system and VLSI design in ICSA CHENNAI

DECLARATION

      I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

PLACE: BANGALORE   **(TADIKAMALLA SAI BHARATH)**