

Computer control of the NaCs experiment

Yichao Yu

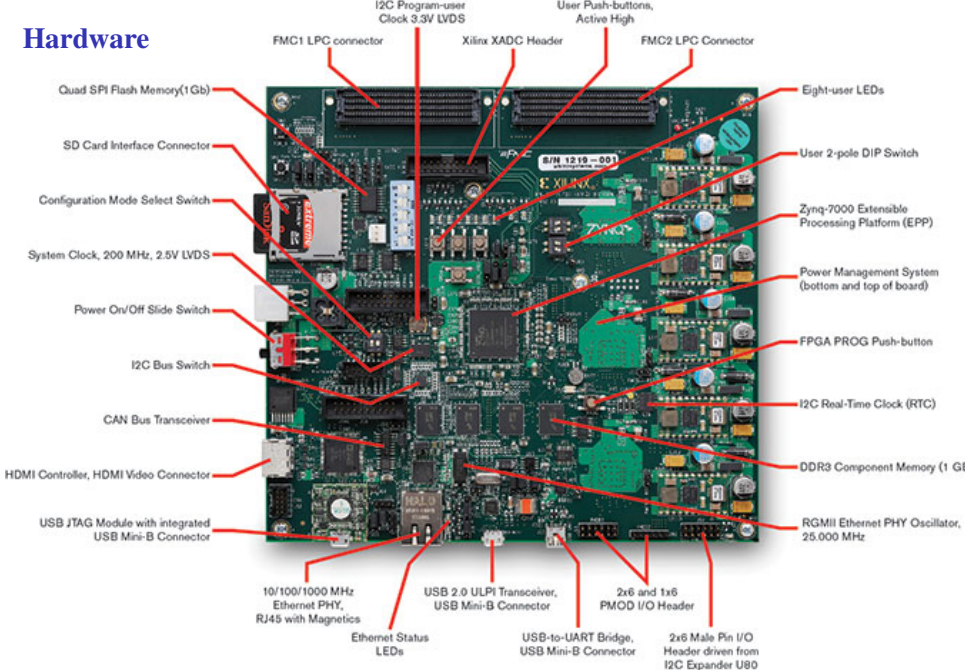
Ni Group/Harvard

October 19, 2014

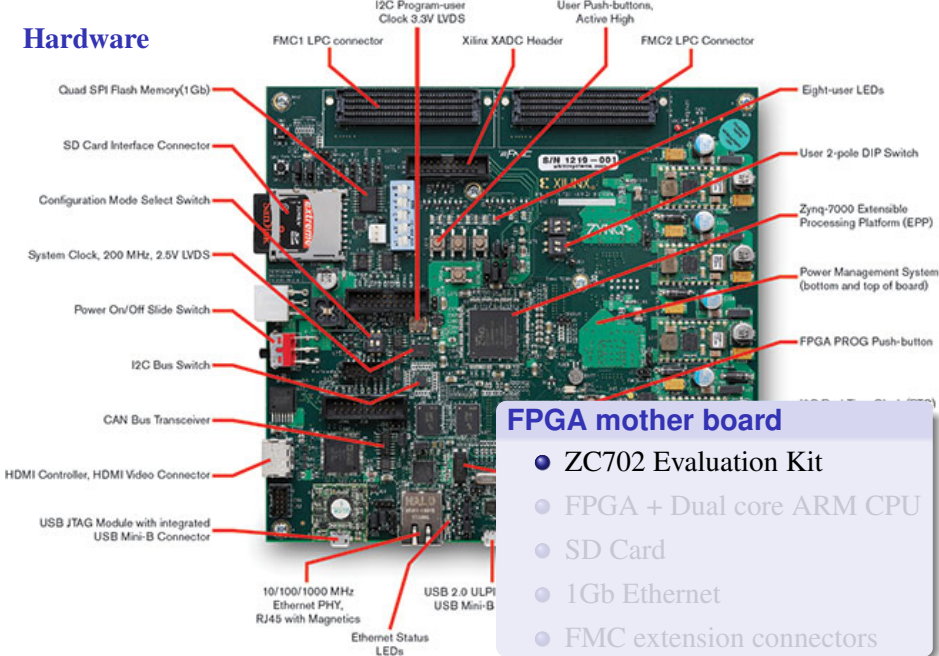
Continuous measurement without precise timing

1 Hardware

Hardware



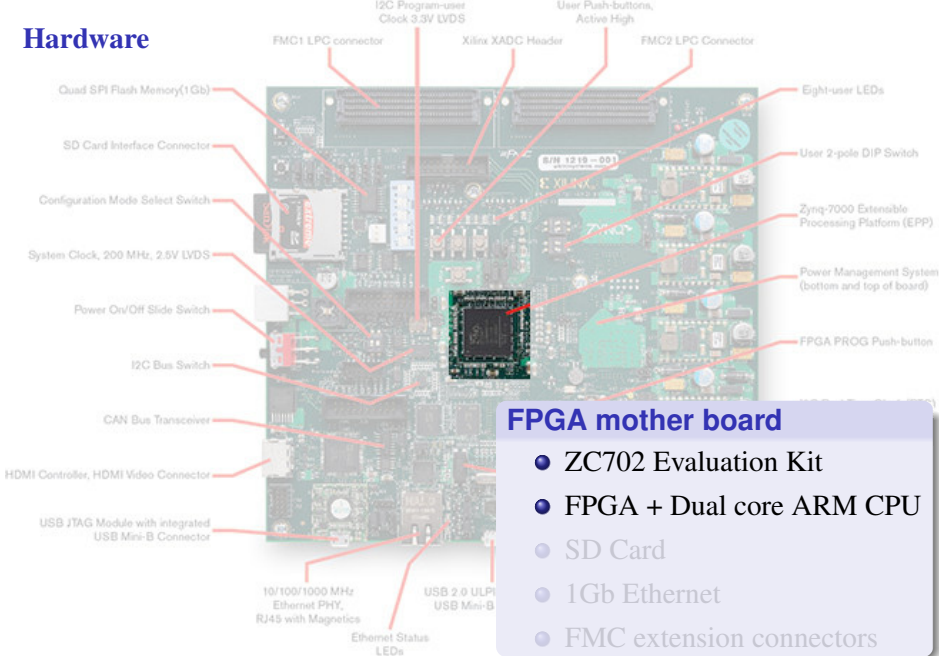
Hardware



FPGA mother board

- ZC702 Evaluation Kit
- FPGA + Dual core ARM CPU
- SD Card
- 1Gb Ethernet
- FMC extension connectors

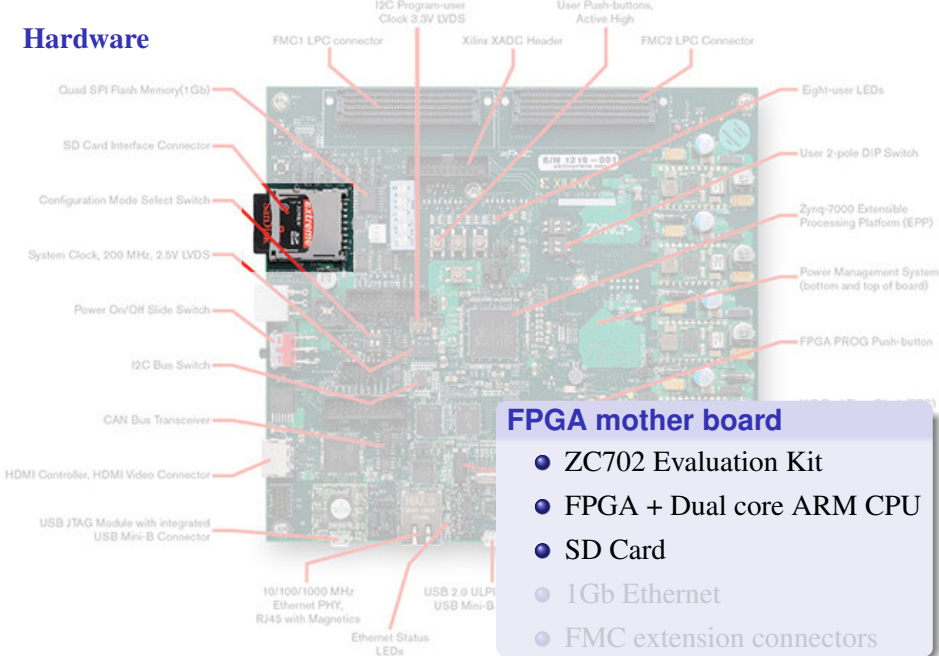
Hardware



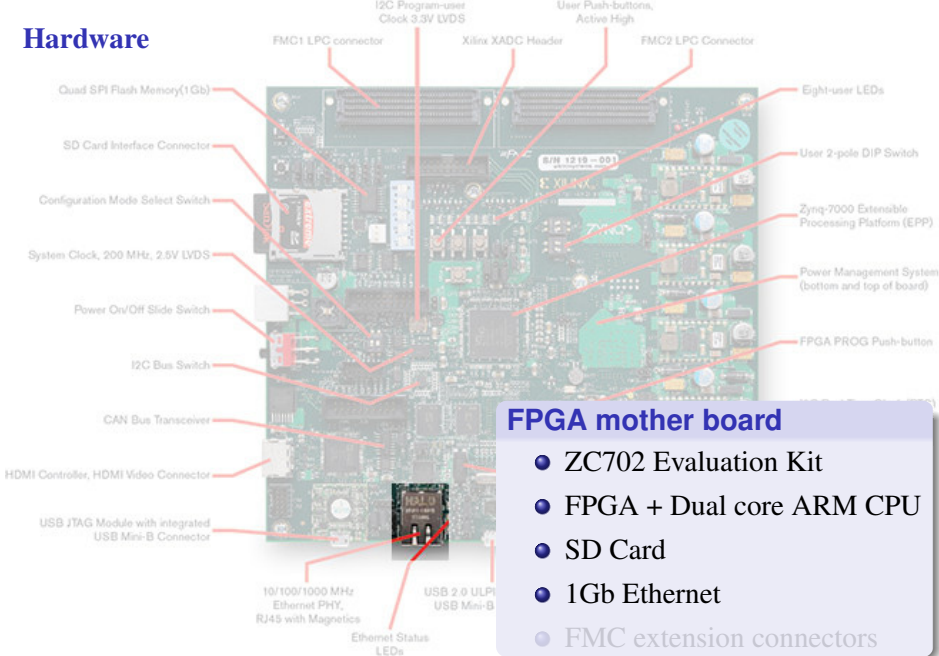
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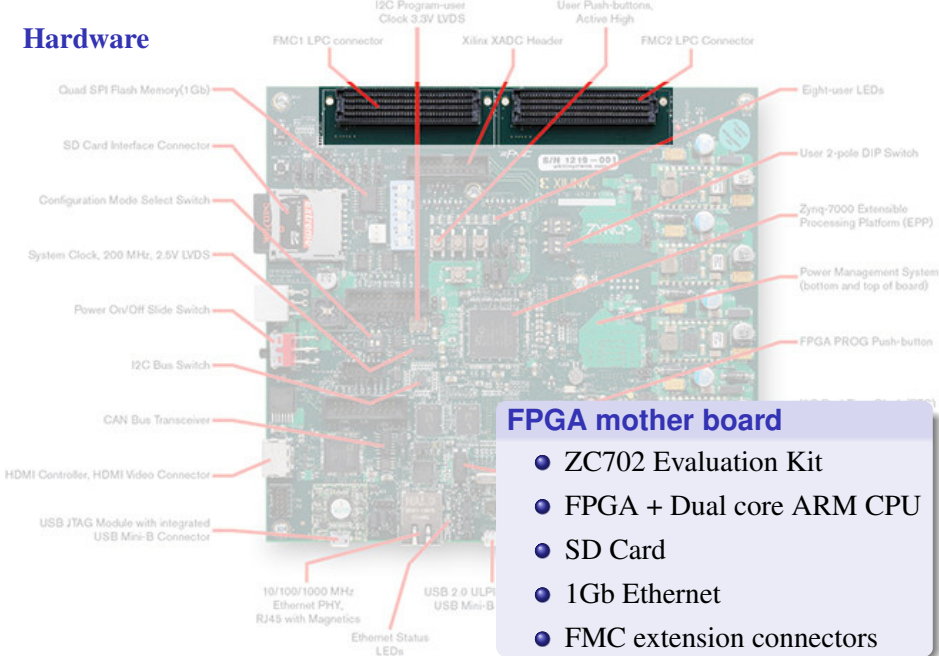
Hardware



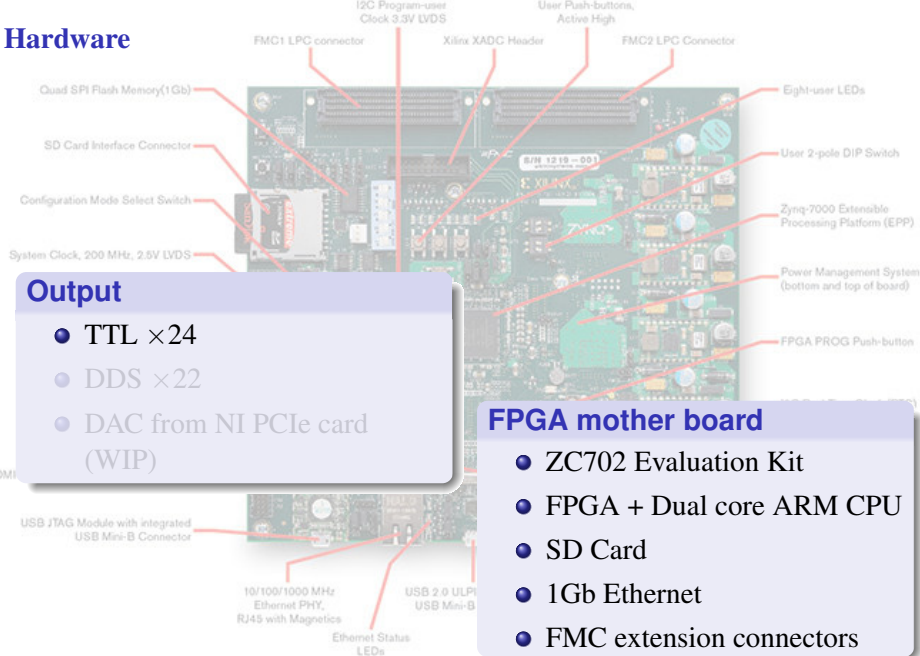
Hardware



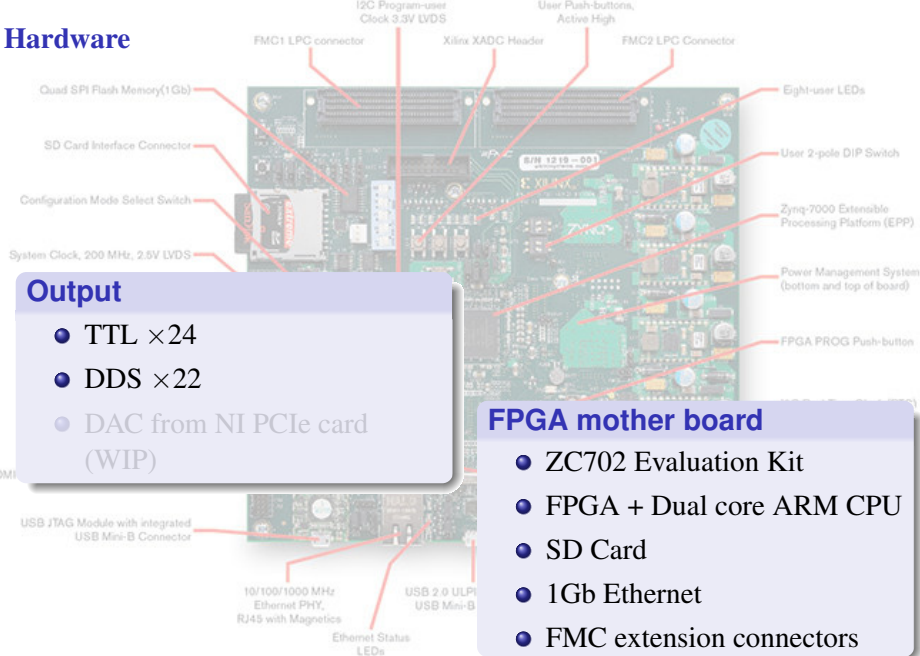
Hardware



Hardware



Hardware



Output

- TTL $\times 24$
- DDS $\times 22$
- DAC from NI PCIe card (WIP)

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Hardware

