

Aprameya Bhat

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Professional Experience

Binghamton University

Binghamton, NY

GRADUATE TEACHING ASSISTANT

August.2017 - Present

- Working on research project 'Performance impact of increasing the virtual CPU count in virtual machines' aiming to improve the performance and keeping it constant with increasing number of virtual cpus in virtual machine by eliminating process and threads scheduling and load balancing
- Graduate teaching assistant for CS550 - Operating Systems & CS451/551 -Systems Programming
- Conducting classes and exams, clearing student doubts, correcting homeworks, tests and assignments

Binghamton University

Binghamton, NY

RESEARCH PROJECT ASSISTANT

May.2017 - August.2017

- Worked on Operating System Virtualization concepts: virtual CPU scheduling and processes pinning, aiming at improving the performance of the virtual machine
- Improved the time to schedule process by pinning processes to vcpu and eliminating load balancing in guest OS
- Conducted experiments on virtual cpu & process scheduling with various PARSEC benchmark suite

Evry India Pvt Ltd

Bangalore, India

SOFTWARE ENGINEER

July.2015 - July.2016

- Developed backend in MVC model using C# and database connectivity using ADO.NET, building REST API's for communication and writing various business logic
- Developed GUI in MVC model using JavaScript & HTML
- Developed unit test cases for complete coverage of both the frontend(Javascript & HTML) and backend(C#) code

Education

Binghamton University (SUNY)

Binghamton, NY

MASTER OF SCIENCE IN COMPUTER SCIENCE

August.2016 - May.2018 (Expected)

- Courses: Computer Algorithms, Adv Topics in Cloud Computing, Computer Networks, Systems Programming, Programming Languages, Computer Origination and Architecture
- GPA: 3.73/4.0

Visvesvaraya Technological University

Mangalore, India

BACHELOR OF ENGINEERING IN COMPUTER SCIENCE

August.2011 - May.2015

- Courses: Computer Networks, Design and Analysis of Algorithms, Object oriented programming, Relational Database Management, Software Engineering, Microprocessors, Compiler Design

Projects

Performance impact of increasing the virtual CPU count in virtual machines

Binghamton, NY

RESEARCH PROJECT WITH PROF. KARTIK GOPALAN

May.2017 - Present

- Developing with a goal to keep the performance of the virtual machine constant with increasing number of virtual cpus
- Eliminating process scheduling and load balancing to improving the performance of virtual machine by 7% -10%
- Pinning processes and interrupts to vcpu and paravirtualizing TLB flush to be handled by hypervisor

APEX Simulator

Binghamton, NY

DEVELOPER

September.2016 - December.2016

- Designed and developed in-order and out-of- order pipeline simulation system for the intel ISA's
- Collaborated with a team of 3 to design and develop the structures and functionality of the system in Java

Good Faith Estimate

Bangalore, India

DEVELOPER

July.2015 - May.2016

- Developed at Evry India, for a USA based mortgage company in C# ASP.NET MVC
- Implemented new logic and functionalities as per USA mortgage requirements in agile methodology

ISpan

Bangalore, India

DEVELOPER

March.2016 - July.2016

- Developed at Evry India Pvt Ltd an Internal system used to track the employee efforts
- Designed and developed in C# ASP.NET as per the company requirements

Data Embedding in Medical Images

Mangalore, India

DEVELOPER

December.2014 - April.2015

- Designed and developed in coordination with Balmatta Diagnostic And Research Center Ltd Mangalore, Data hiding in MRI DICOM image Based on Region of Interest using discrete cosine transformation

Roulette Wheel Cricket

Mangalore, India

DEVELOPER

June.2014 - August.2014

- Designed and developed a casino roulette based interactive game, designed using OpenGL for windows

Technical Skills

Languages

C, C#, Java, HTML, CSS, Javascript, Shell script, JQuery

Other technologies

ASP.NET, MVC, MS Visual Studio, Eclipse, Jira, T-SQL, SVN, Github, Agile Methodology