

ASSIGNMENT - 1

Bhavani

Draw the Logic Circuit of the following Boolean Expression using only NAND Gates : $X.Y + Y.Z$

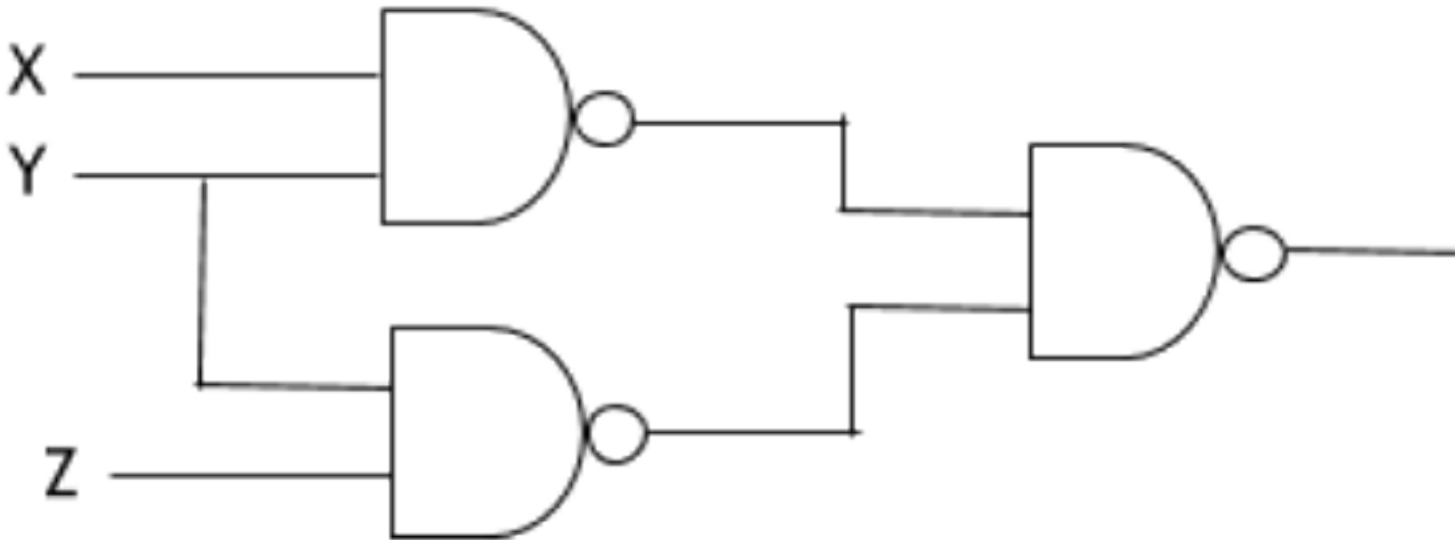


Fig. 0. $f = XY + YZ$ using NAND gates only

https://github.com/bhavani360/FWC_assignments