

# ARM Assignment

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October 2022

## problem

Draw the logic circuit of the following Boolean Expression using only NAND Gates :  $X.Y + Y.Z$

## solution

$$F = XY + YZ$$

Implementing above boolean function using NAND gates only

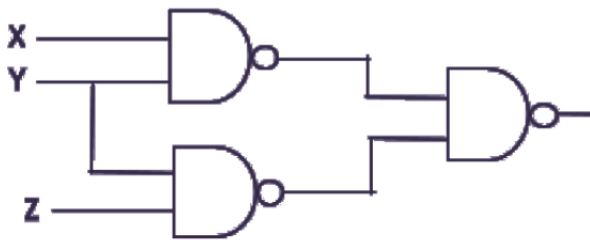


Figure 1: circuit

## 1 Description

Given is a boolean expression with three different variables implying that three inputs are to be given for the circuit, in addition to that we have some mathematical operations, apostrophe and dot operators.

These symbols are nothing but the logic gates representing AND, OR, NOT gates for symbols ".", "+", " ' " respectively. Bubbled AND gate represents "NAND" gate

So, for the given expression 5 distinct logic gates are to be connected using the four inputs in accordance with the boolean expression to execute the logic in practice.

## 2 Procedure

1. After executing the following code using make, a binary file is generated with .bin extension in the output directory.

2. Now from the termux, using scp protocol, send the generated bin file to the laptop.

3. There we are supposed to flash the .bin file into the ARM through the terminal.

4. After flashing, reset the Vaman board.

5. Make connections between the LED and ARM board using jumper wires.

6. Now check the output with reference to the truth table present above.

## 3 Conclusion

Hence implemented the given circuit and boolean expression after verifying its functionality using ARM.

The below code realizes the Boolean logic expression for given circuit.

```
https://github.com/bhavani360/FWC_
assignments
```