

Name: Bhavna Thakur

ID NO. :- 21EL095

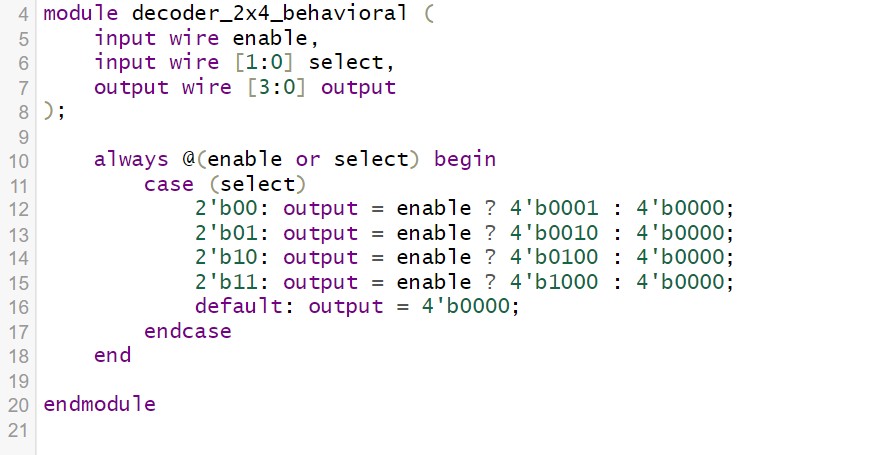
Division :- 11

Subject:- Digital System Design

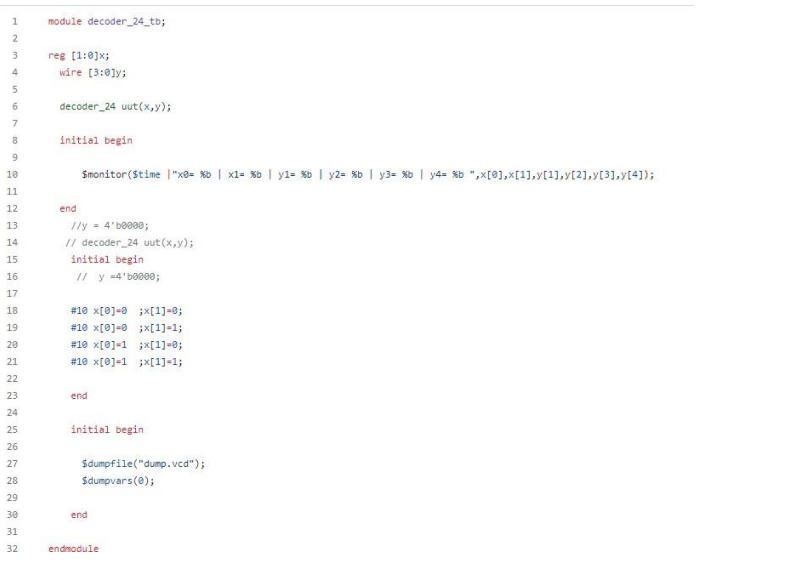
Branch :- Electronics

Assignment :- 1

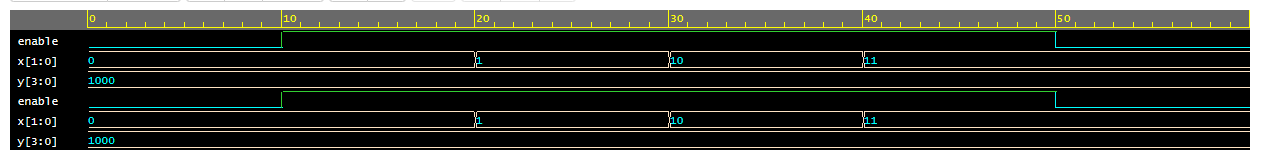
# Q-1 :-Write a Verilog code for 2\*4 decoder.



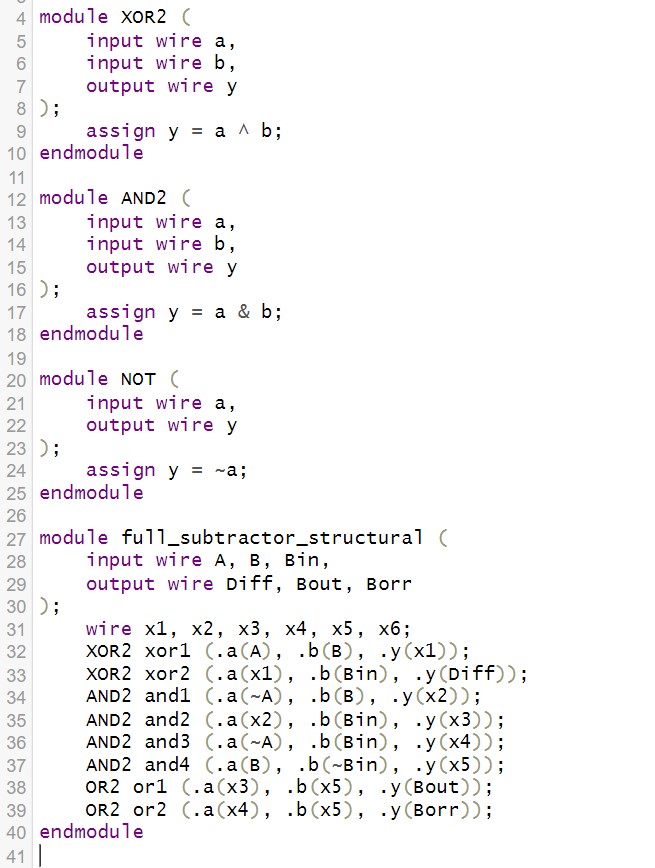
**TESTBENCH**

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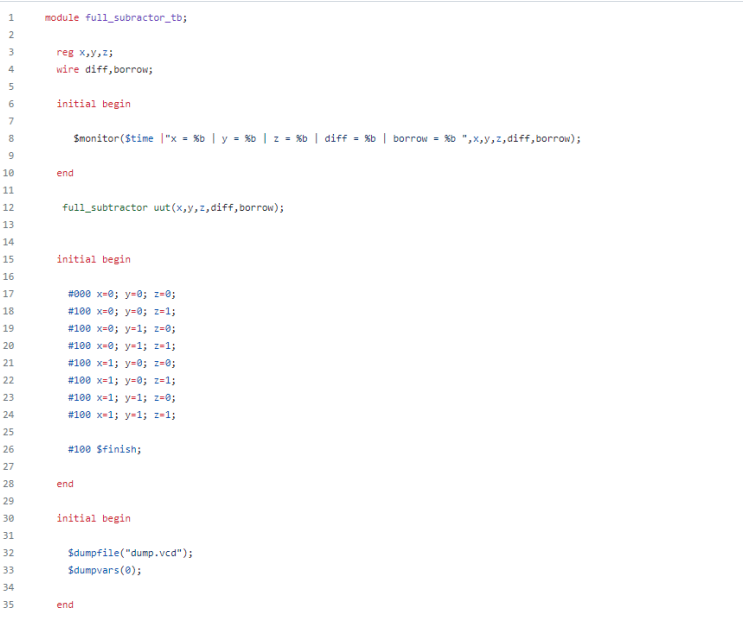
**OUTPUT:-**

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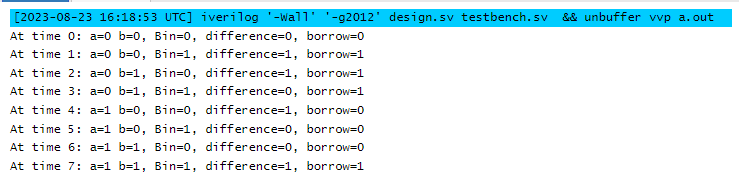
Q-2 :- Write a Verilog code for full subtractor.



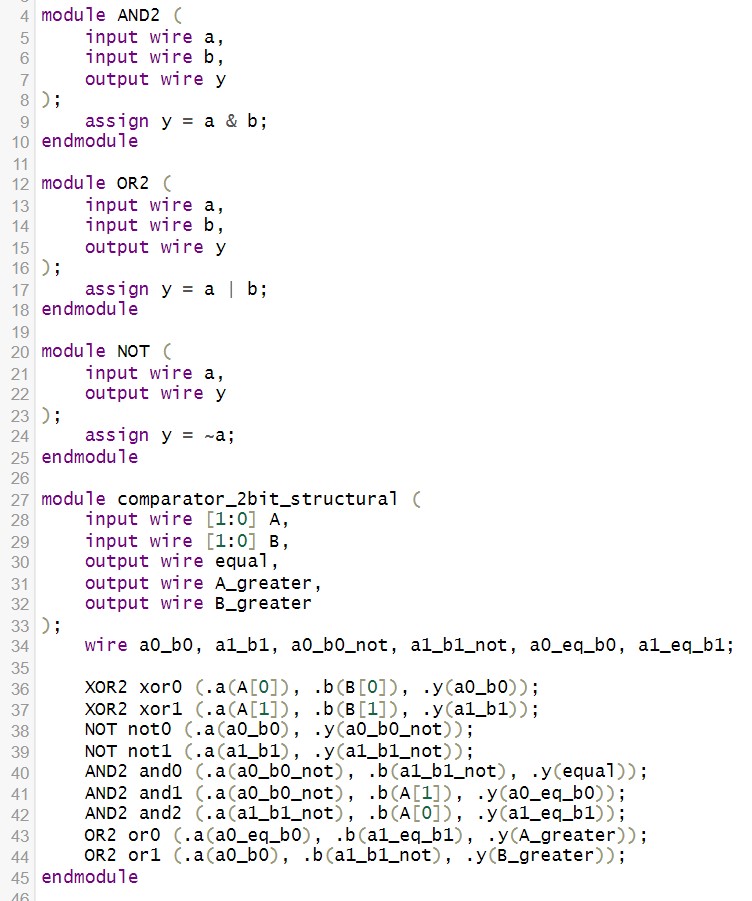
**TESTBENCH**

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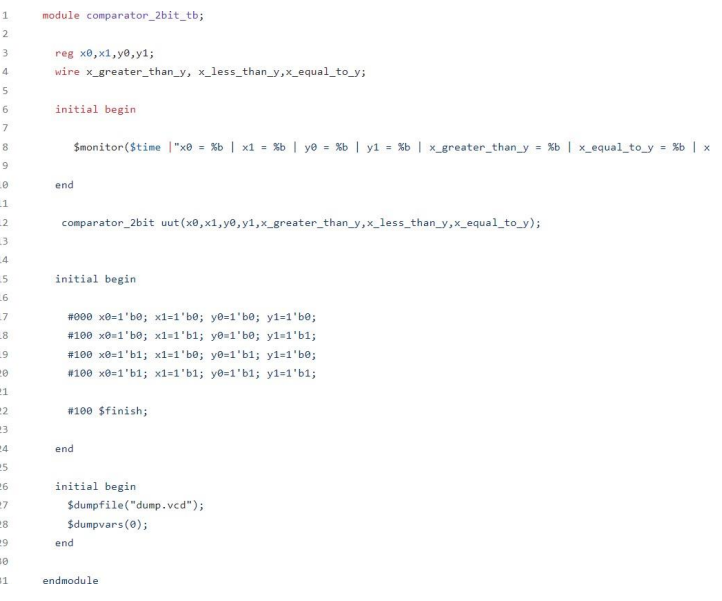
**OUTPUT:-**



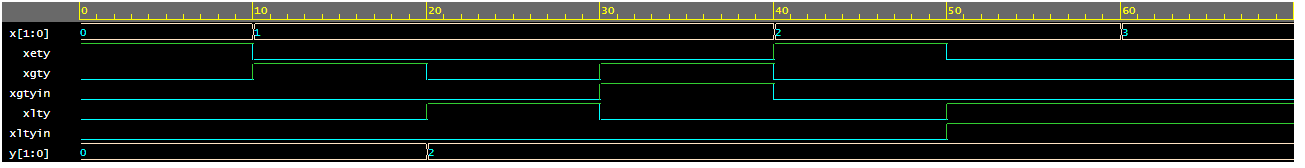
Q-3 :- Write a Verilog code for 2-bit comparator.



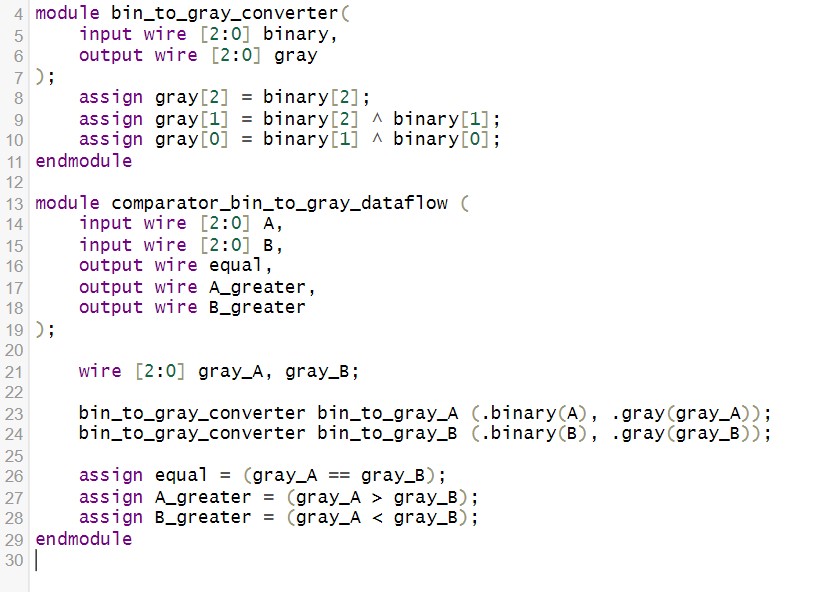
TESTBENCH



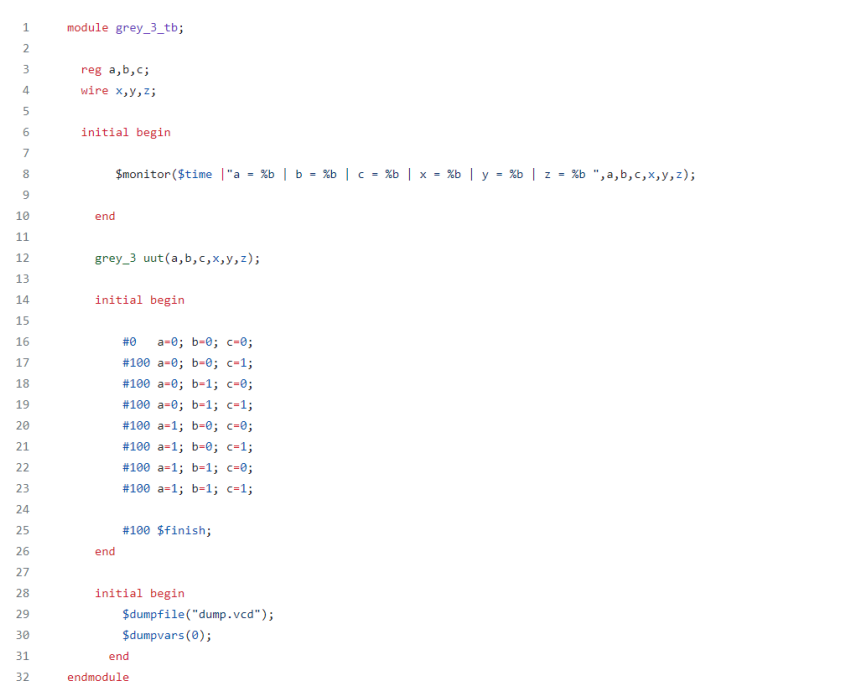
**OUTPUT:-**

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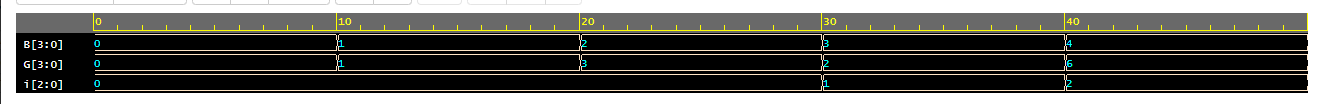
Q-4:- Write a Verilog code for 3 bit binary to gray convertor.



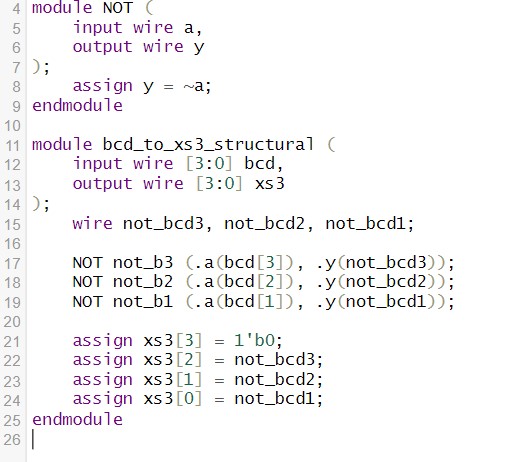
TESTBENCH



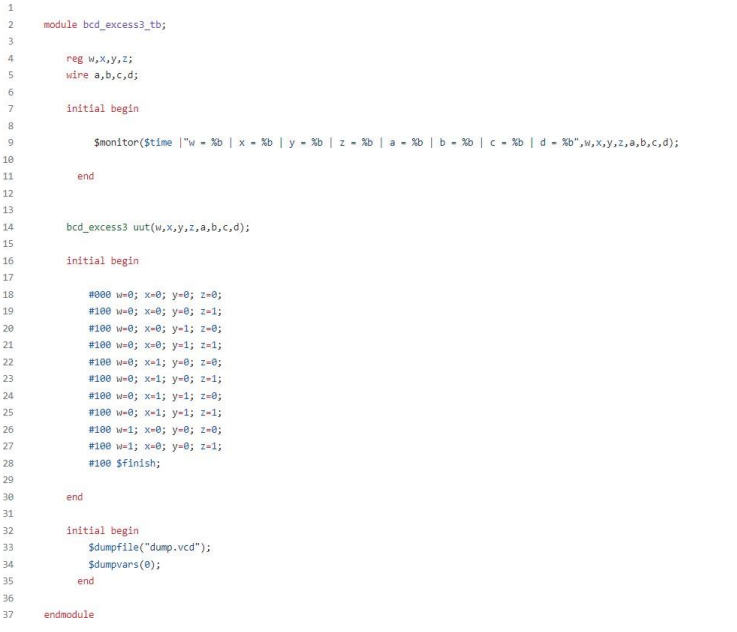
**OUTPUT:-**



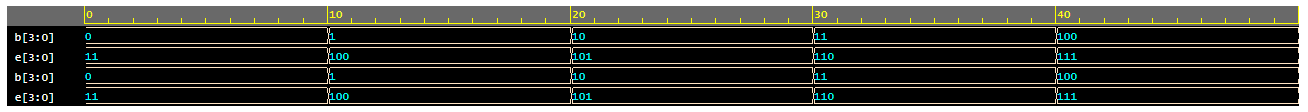
Q-5 :- Write a Verilog code for BCD to excess 3 convertors.



**TESTBENCH**

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**OUTPUT:-**

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