Pipeline Implementation without Data forwarding and Hazard Detection

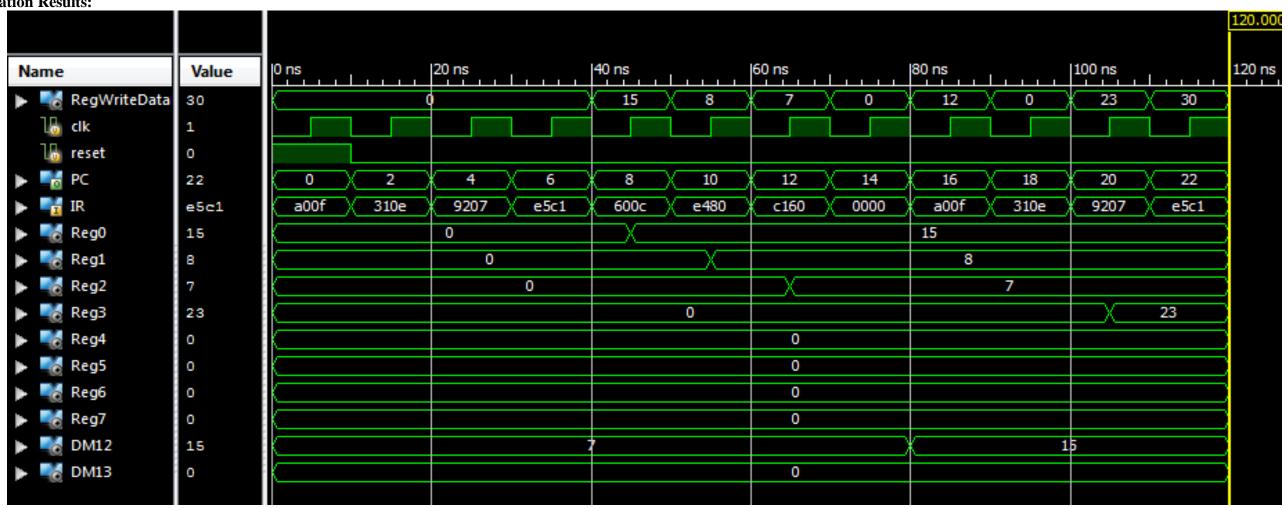
Instruction Format:

Instruction	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
	Opcod	le														
lw	0	0	rs			rt			Imm							
sw	0	1	rs			rt			Imm							
addi	1	0	rs rt			Imm										
add	1	1		rs			rt			rd		X	X	X	X	0
sub	1	1		rs			rt	•		rd		X	X	X	X	1

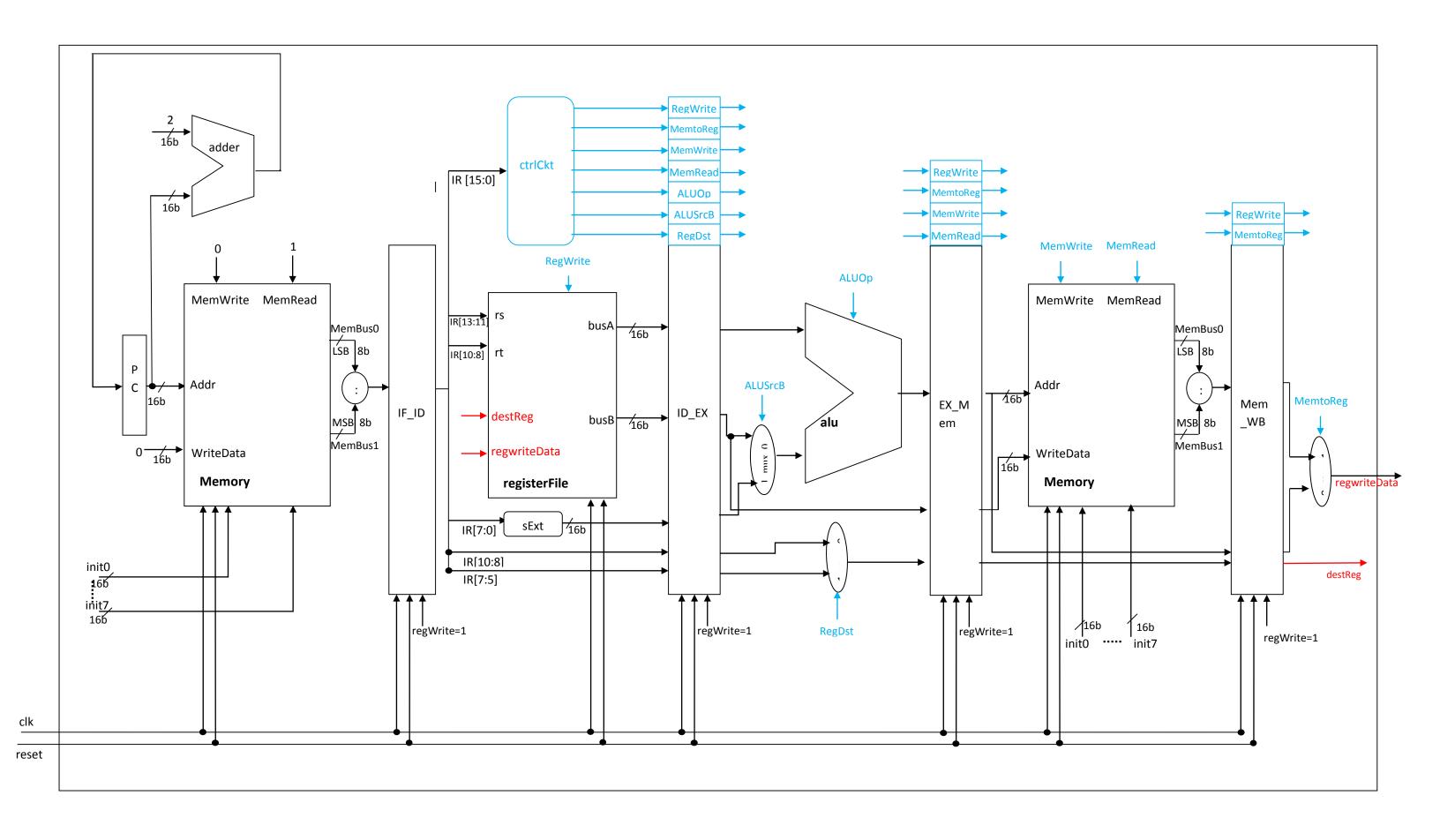
Instruction Memory (used in Lab04):

addi R0, R4, 15	(a00f)
lw R1, R6(14)	(310e)
addi R2, R0, 7	(9207)
sub R6, R0, R5	(e5c1)
sw R0, R4(12)	(600c)
add R4, R4, R4	(e480)
add R3, R0, R1	(c160)

Simulation Results:



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Instruction Memory (to be used in Lab05):

lw R1, R0(2)	(0102)
add R2, R1, R1	(c940)
addi R3, R4, 6	(a306)
sub R4, R3, R2	(da81)
sw R4, R5(4)	(6c0e)
add R4, R3, R1	(d980)
lw R6, R5(0)	(2e00)
addi R5, R6, -2	(b5fe)

Simulation Results (with Lab04Solution.v):

