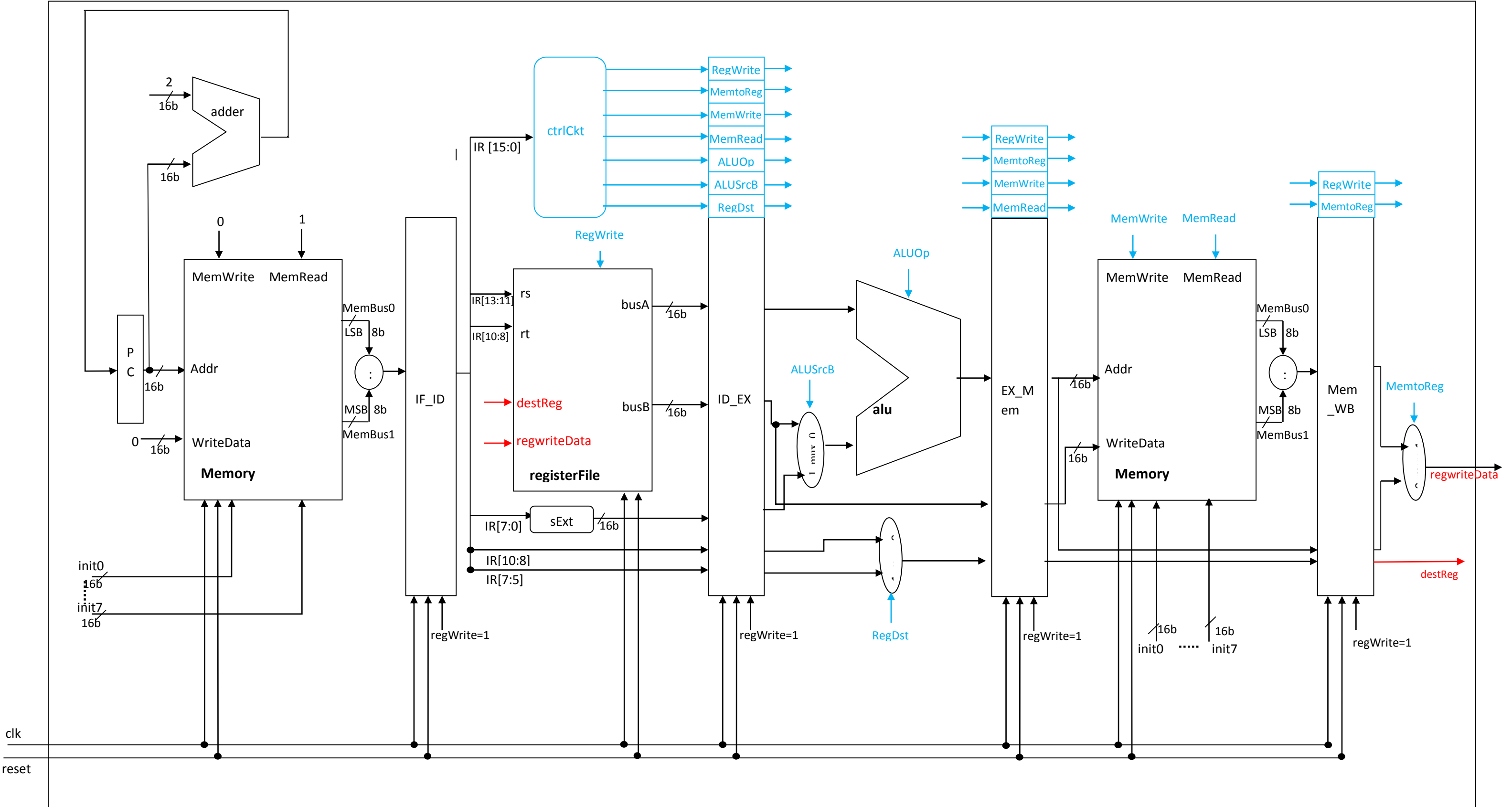




Pipeline Implementation without Data forwarding and Hazard Detection. [Lab04Solution.v file]



Instruction Memory (to be used in Lab05):

```
lw R1, R0(2)      (0102)
add R2, R1, R1     (c940)
addi R3, R4, 6     (a306)
sub R4, R3, R2     (da81)
sw R4, R5(4)       (6c0e)
add R4, R3, R1     (d980)
lw R6, R5(0)       (2e00)
addi R5, R6, -2    (b5fe)
```

Simulation Results (with Lab04Solution.v):

