

**COA**  
**Assignment-3**

- 1.a) Explain floating point representation formats.
- b) Show the step-by-step process of using Booth multiplication algorithm:  $(+15) \times (-13)$ .
- 2.a) Compare and contrast RISC and CISC architectures.
- b) Define Pipelining. Explain four segment instruction pipelining in detail.

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**Assignment-3**

- 1.a) Give a neat sketch that illustrates the components in memory hierarchy.
- b) Explain the different modes of I/O data transfer.
- 2.a) Explain principle of Cache coherence
- b). With neat diagrams, explain different cache mapping techniques.