BLOCK DIAGRAM

How to submit: Place a PDF document with the link/title "Block Diagrams" in your group's resources folder and make a link to this on your group's Wiki page. Your page must be readable on a web browser. What to submit: No more than 5 pages, formatted as below.

You are to create and document a diagram of the hardware design for your project. The design should include all components with a computational, communication, sensing, or actuating function. It should exclude power-related components. The communication between components should be precisely identified in the diagram, including the physical connection (e.g., 2 wires), the lower level protocol (e.g., I2C), and the nature of the data being communicated (e.g., 10-bit sensor data). If the communication is bi-directional, this should be identified and documented. Each component (e.g., sensors, processors, etc.) in the diagram should have a specific model number (e.g., PIC18F2680). The assignment of tasks from your task diagram must be specified in this document. Please refer to the example diagrams and the discussion in class.

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