# **kHz Range Crystal Devices Digital Temperature Compensated Crystal Oscillator** Surface Mount Type DTCXO KT3225T Series



CMOS/ 3.0V Typ./ 3.2×2.5mm



#### **Features**

- Miniature SMD type (3.2×2.5×1.0mm)
- 32.768kHz D-TCXO
- High frequency stability :  $\pm 5.0 \times 10^{-6} / -40$ to +85°C
- Low supply current :  $1.5\mu A$  typ (V<sub>DD</sub> = 3.0V, Output at no load)
- Temperature compensated voltage Range: 2.0V to 5.5V
- Operating Temp. -40 to +105°C (option)
- AEC-Q200 qualified

## **Applications**

- · High accuracy time references
- Microcontroller with built in RTC

### **How to Order**

Frequency Tolerance (vs Temp.) :  $\pm 3.8 \times 10^{-6} / -10^{\circ} C$  to  $60^{\circ} C$ 

<u>KT3225T 32768</u> <u>D</u> <u>G</u> <u>R</u> <u>□</u> <u>T</u> <u>xx</u>  $\overline{3}$   $\overline{4}$   $\overline{5}$   $\overline{6}$   $\overline{7}$   $\overline{8}$ 

Frequency Tolerance (vs Temp.) :  $\pm 5.0 \times 10^{-6} / -40^{\circ} C$  to  $85^{\circ} C$ 

<u>KT3225T 32768</u> <u>E</u> <u>A</u> <u>W</u> <u>□</u> <u>T</u> <u>xx</u>  $\overline{3}$   $\overline{4}$   $\overline{5}$   $\overline{6}$   $\overline{7}$   $\overline{8}$ **(2**) (1)

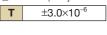
- 2 Output Frequency 1 Series
- ③ Frequency Stability ④ Lower Temperature
- (5) Upper Temperature

	3	4	5
DGR	±3.8×10 <sup>-6</sup>	−10°C	+60°C
EAW	±5.0×10 <sup>-6</sup>	−40°C	+85°C

6 Supply Voltage

7 Initial Frequency Tolerance

30	3.0V	
33	3.3V	
50	5.0V	



**8** Individual Specification

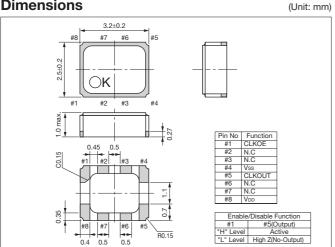
Packaging (Tape & Reel 3000 pcs./ reel)

#### **Specifications**

Item	Symbol	Conditions	Specifications			11.31.
			Min.	Тур.	Max.	Units
Nominal Frequency	f_nom		_	32.768	_	kHz
Oscillation Output Voltage	V <sub>DD</sub>		1.3	3.0	5.5	V
Temperature Compensated Voltage	VTEM		2.0	3.0	5.5	V
Storage Temperature	T_stg		-40	+25	+85	°C
Operating Temperature	T_use		-40	+25	+85	°C
Initial Frequency Tolerance	_	$Ta = 25\pm2^{\circ}C$	-3.0	_	+3.0	×10 <sup>-6</sup>
Frequency Stability vs Temp.	fo-Tc	E: $Ta = -40 \text{ to } +85^{\circ}\text{C}$	-5.0	_	+5.0	×10 <sup>-6</sup>
Frequency Stability vs Supply Voltage	df/ fo	$V_{DD} = 2.0 \text{ to } 5.5\text{V}, \text{ Ta} = 25\pm2^{\circ}\text{C}$	-1.0	_	+1.0	×10 <sup>-6</sup> / V
Frequency Aging	f_age		-3.0	_	+3.0	×10 <sup>-6</sup>
Low Level Output Voltage	Vol	$I_{OL} = +1.0 \text{mA}, V_{DD} = 3V$	0.0	_	0.8	V
High Level Output Voltage	Vон	IOH = -1.0 mA, $VDD = 3V$	2.2	_	3.0	V
Low Level Input Voltage	VIL	CLKOE pin	0.0	_	0.2×V <sub>DD</sub>	V
High Level Input Voltage	Vih	CLKOE pin	0.8×VDD	_	5.5	V
DUTY Ratio	Duty	CL = 15pF	40	_	60	%
Rise Time	tr	$20\%VDD \rightarrow 80\%VDD$ , $CL = 15pF$ , $VDD = 3V$	_	_	100	ns
Fall Time	tf	$80\%VDD \rightarrow 20\%VDD$ , $CL = 15pF$ , $VDD = 3V$	_	_	100	ns
Start-up Time	t_str	Ta = 25°C	_	_	1.0	sec
		$Ta = -40 \text{ to } 85^{\circ}C$	_	_	3.0	sec
Power Supply Current1	Icc1	CLKOE = Vss, VDD = 3V	_	0.6	2.0	μΑ
Power Supply Current2	Icc2	CLKOE = VDD, VDD = 3V, Output at no load	_	1.5	4.0	μΑ
		CLKOE = V <sub>DD</sub> , V <sub>DD</sub> = 3V, CL = 15pF	_	2.7	5.5	μΑ
Output Load Condition	L_CMOS	CMOS Output	_	_	15.0	pF

<sup>\*</sup> Please contact us for other specifications.

# **Dimensions**



#### **Recommended Land Pattern**

(Unit: mm)

