

RAJIV GANDHI UNIVERSITY OF KNOWLEDGE TECHNOLOGIES- BASAR



**BEEE**  
**LABARATORY MANUAL**

DEPARTMENT OF ELECTRICAL ENGINEERING

## PREFACE

Engineering institutions have been modernizing and updating their curriculum to keep pace with the continuously developing technological trends so as to meet the correspondingly changing educational demands of the industry. As the years passed by, multi-disciplinary education system also has become more and more relevant in the present global industrial development. Thus, just as Computer Systems & Applications, *Basic Electrical & Electronics Engineering* also has become an integral part of all the industrial and engineering sectors be it infrastructure, power generation, minor & major Industries, Industrial Safety or process industries where automation has become an inherent part. Accordingly, several universities have been bringing in a significant change in their graduate programs of engineering starting from the first year to meet the needs of these important industrial sectors to enhance the employability of their graduates. Thus, at college entry level itself *Basic Electrical & Electronics Engineering* has become the first Multidisciplinary core engineering subject for almost all the other core engineering branches like Civil, Mechanical, Production engineering, Industrial Engineering, Aeronautical, Instrumentation, Control Systems and Computer Engineering. As a further impetus, since for understanding of this subject practical knowledge is equally important, a laboratory course is also added in the curriculum. The experiments are so chosen that the student comprehends all the important theoretical concepts with good practical insight.

This handbook of Laboratory manual cum Observations for *Basic Electrical and Electronics Engineering*

is brought out in a simple and lucid manner highlighting the important underlying concepts & objectives along with sequential steps to conduct the experiment. Every experiment is further provided with format of test results and most importantly the safety precautions to be taken.

## **INSTRUCTIONS TO STUDENTS**

1. Before entering the lab, the students should carry the following things.
  - Identity card issued by the college.
  - Class notes.
  - Lab observation book/Lab Manual.
  - Lab Record.
2. Student must sign in and sign out in the register provided when attending the lab session without fail. Students need to maintain 100% attendance in lab if not a strict action will be.
3. Come to the laboratory in time. Students, who are late more than 15 min., will not be allowed to attend the lab.
4. All students must follow a **Dress Code** while in the laboratory
5. Foods, drinks are NOT allowed and all bags must be left at the indicated place.
6. The objective of the laboratory is learning. The experiments/ demonstrations are designed to illustrate the need and operation of an equipment/ machine and to expose you, how to use different measuring instruments in industry or our daily life, conduct the experiments with interest and an attitude of learning
7. Students must take care of their valuable things, Work quietly and carefully.
8. Be honest in recording and representing your data.
9. If a particular reading appears wrong repeat the measurement carefully, to get a better fit for a graph
10. All presentations of data, tables and graphs calculations should be neatly and carefully done
11. If you finish early, spend the remaining time to complete the calculations and drawing graphs. Graphs should be neatly drawn with pencil. Always label graphs and the axes and display units.
12. Come equipped with calculator, scales, pencils etc. Before entering to lab, must **prepare for Viva** for which they are going to conduct experiment.
13. Do not fiddle with apparatus. Handle instruments with care. Report any breakage to the Instructor.
14. When the experiment is completed, students should disconnect the setup made by them, and should return all the components/instruments taken for the purpose.
15. Any damage of the equipment or burn-out of components will be viewed seriously either by putting penalty or by dismissing the total group of students from the lab for the semester/year.

## **DO'S AND DON'TS**

### **DO'S:**

- Proper Dress Has to Be Maintained While Entering in The Lab. (Boys Tuck in And Shoes, Girls with Apron).
- Students Should Carry Observation Notes and Record Completed in All Aspects.
- Correct Specifications of The Equipment Have to Be Mentioned in The Circuit Diagram.
- Student Should Be Aware of Operating Equipment.
- Students Should Beat Their Concerned Experiment Table, Unnecessary Moment Is Restricted.
- Student Should Follow the Indent Procedure to Receive and Deposit the Equipment from The Lab Store Room.
- After Completing the Connections Students Should Verify the Circuits by The Lab Instructor.
- The Readings Must Be Shown to The Lecturer In-Charge for Verification.
- Before Leaving the Lab, Students Must Ensure That All Switches Are in The Off Position and All the Connections Are Removed.
- All Patch Cords and Stools Should Be Placed at Their Original Positions.

### **DON'TS:**

- **Don't** Come Late to The Lab.
- **Don't** Enter into The Lab with Golden Rings, Bracelets and Bangles.
- **Don't** Make or Remove the Connections with Power On.
- **Don't** Switch on The Supply Without Verifying by The Staff Member.
- **Don't** Switch Off the Machine with Load.
- **Don't** Leave the Lab Without the Permission of The Lecturer In-Charge.

## **COURSE OBJECTIVES:**

**The course should enable the student's ability to**

1. To verify the basic electrical circuit laws and theorems.
2. To plot the V-I characteristics of PN junction Diode.
3. To plot the input and output characteristics of Transistor in CE configurations.
4. To determine the characteristics of Transformers.

## **COURSE OUTCOMES:**

**At the end of the course, the students are expected to**

1. Understand the circuit analysis techniques.
2. Plot the V-I characteristics of PN junction Diode
3. Plot the input and output characteristics of Transistor in CB and CE configurations.
4. Understand the basic characteristics of transformer.

## List of Experiments

S. NO	NAME OF THE EXPERIMENT	PAGE NO
1.	A). Basicsafety precautions. Introductionanduseofmeasuringinstruments— voltmeter, ammeter, multi-meter, oscilloscope. Real-liferesistors, capacitors and inductors.  B). Demonstration of cut-out sections of machines: dc machine (commutator brush arrangement), induction machine (squirrel cage rotor), synchronous machine (field winging - slip ring arrangement) and single-phase induction machine.	01-30
2.	Transient time-response of R-L, R-C and RLC	31-43
3.	Resonance in series R-L-C circuit	44-45
4.	Transformers: Observation of the no-load current waveform on an oscilloscope. Loading of a transformer: measurement of primary and secondary voltages and currents, and power.	46-52
5.	Open circuit & Short circuit test on single phase transformer.	53-57
6.	Characteristic of the lamps (Tungsten, Fluorescent and Compact Fluorescent Lamps)	58-61
7.	Verification of KCL&KVL	62-64
8.	Verification of Network Theorems: Superposition, Thevenin's Theorems	65-68
9.	V-I characteristics of Diode and BJT	69-76
10.	Half-wave and Full-wave rectifiers	77-85
11.	Study of Logic Gates	86-87

## ExperimentNo. 01(a)

### Aim:

Basicsafetyprecaution, Introductionanduseofmeasuringinstruments—voltmeter,ammeter,multi-meter.Real-liferesistors, capacitorsandinductors.

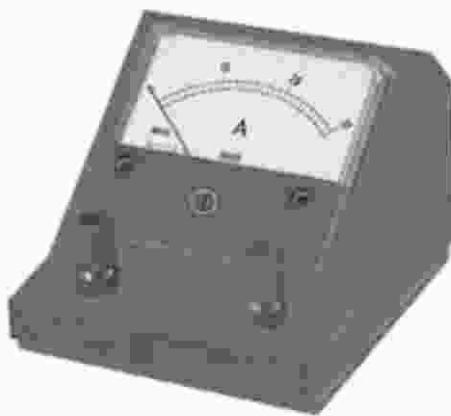
### APPARATUSREQUIRED:

S. No.	Name of apparatus	Type	Range	Quality
1.	Ammeter	MI, MC	(0-2)A AC, (0-1) A DC	As required
2.	Voltmeter	MI, MC	(0-300)V AC, (0-20) V DC	As required
3.	Multimeter	DIGITAL	-	1
4.	Resistors	Fixed, Variable	-	As required
5.	Capacitors	Fixed, Variable	-	As required
6	Inductors	Fixed, Variable		As required

### THEORY

#### Ammeter:

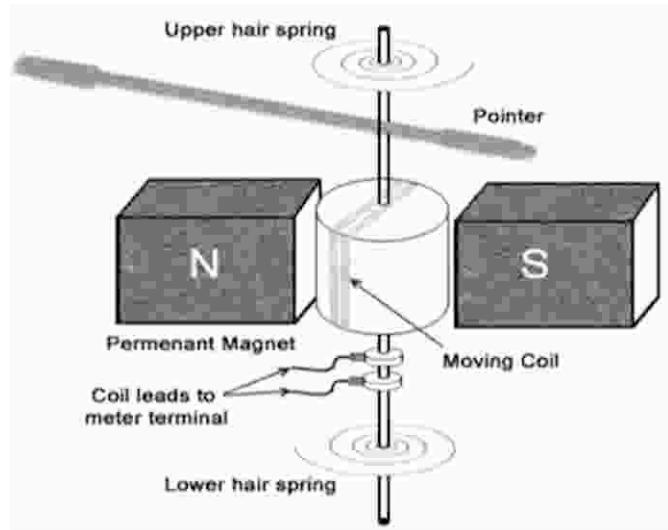
Ammeterisanelectronicinstrumentsdeviceusedtodeterminetheelectriccurrentflowingthroughacircuit.Ammeter smeasuringcurrentinmilli-ampererangeisknownasmilli-ammeters.Commontypesofammetersaremoving-coilammeterandmoving-ironammeter.Ammetersareconnectedinseries tothecircuitwhosecurrent is to be measured. Hence this electronic instruments are designed to have as minimum resistance/loadingaspossible.



**Fig. 1(a).1Ammeter- Electronic Instruments**

#### MovingCoilAmmeter:

Moving coil ammeters are used to measure DC Currents. This electronic instruments consists of a coil suspended by two hair springs. This coil is placed in a magnetic field created by a fixed permanent magnet. A torque is experienced when current passes through this coil which is proportional to the current. When the coil turns, the springs will exert a restoring force proportional to the angle turned. By these two forces, the coil will stop at some point and the angular deflection will be proportional to the current.

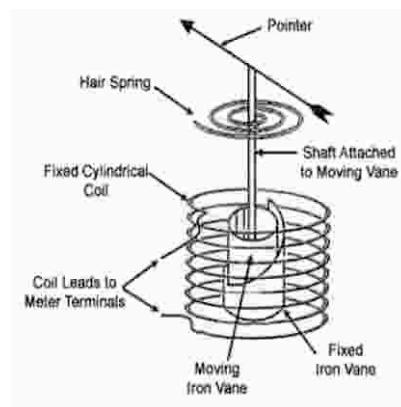


ome point and the angular deflection will be proportional to the current.

**Fig. 1(a).2 Moving coil ammeter**

#### **Moving Iron Ammeter:**

Moving iron ammeters as electronic instruments can be used for measuring both direct and alternating currents in electronics lab. In this type of ammeter, a piece of soft iron is used. This iron piece constitutes of a moving vane and a fixed vane. Current to be checked flows through a fixed coil placed around the iron piece. This coil produces a magnetic field proportional to the current. So the iron pieces will get magnetized with the same polarity. The moving vane turns away from the fixed vane due to magnetic repulsion. As the iron turns, the spring of the electronic instruments will exert a restoring force and stop the vane, when both the forces become equal. The pointer of the ammeter is attached to the movable vane, which will point to the proper current reading using a calibrated scale.



**Fig. 1(a).3 Moving Iron Ammeter**

### **Voltmeter:**

Voltmeter is an electronic instruments used in an electric circuit to determine the potential difference or voltage between two different points. Digital and analog voltmeters are available in electronics lab. They are usually connected in parallel (shunt) to the circuit. Hence they are designed to have maximum resistance as possible to reduce the loading effect. This device is also common in electronics lab.



**Fig.1(a).4(i) Analog Voltmeter**

**(ii) Digital Voltmeter**

### **Analog Voltmeter:**

Analog voltmeter is a type of voltmeter and electronic instruments with an extra connection of a series resistor (high resistance). It consists of a movable coil placed in a magnetic field. The coil ends are connected to the measuring terminals. As current flows across the coil, it will start turning due to magnetic force exerted on the coil and thus the hair spring will stop the coil by an equal and opposite restoring force. Angular rotation will be proportional to the voltage in these electronic instruments.

### **Digital Voltmeter**

Digital voltmeters can measure both AC and DC measurements with high accuracy as an electronic instrument. It can measure a high voltage up to 1kV. Main component of a digital voltmeter is an Analog-to-Digital Converter (ADC). Voltage to be measured is amplified or attenuated properly by the circuit and the output is sent to an Analog-to-Digital Converter (ADC) IC. This IC will convert the analog signal input to digital signal output. A digital display driven by this IC will display the proper voltage value.

### **Digital Multi Meter (DMM)**

A multimeter or a multimeter, also known as a VOM (Volt-Ohmmeter), is an electronic measuring instrument that combines several measurement functions in one unit. A typical multimeter would include basic features such as the ability to measure voltage, current, and resistance. Analog multimeters use a micro ammeter whose pointer moves over a scale calibrated for all the different measurements that can be made. Digital multimeters (DMM, DVOM) display the measured value in numerals, and may also display a bar of a length proportional to the quantity being measured. Digital multimeters are now more common than analog ones, but analog multimeters are still preferable in some cases, for example when monitoring a rapidly-varying value.

A multimeter can be a hand-held device useful for basic fault finding and field service work, or a bench instrument which can measure to a very high degree of accuracy. They can be used to troubleshoot electrical problems in a wide array of industrial and household devices such as electronic equipment, motor controls, domestic appliances, power supplies, and wiring systems.

### **Operation:**

A multimeter is a combination of a multi range DC voltmeter, multi range AC voltmeter, multi range ammeter, and multi range ohmmeter. An un-

amplified analog multimeter combines a meter movement, range resistors and switches.

For an analog meter movement, DC voltage is measured with a series resistor connected between the meter movement and the circuit under test. A set of switches allows greater resistance to be inserted for high voltage ranges. The product of the basic full-scale deflection current of the movement, and the sum of the series resistance and the movement's own resistance, gives the full-scale voltage of the range. As an example, a meter movement that required 1 millampere for full scale deflection, with an internal resistance of 500 ohms, would, on a 10-volt range of the multimeter, have 9,500 ohms of series resistance. For analog current ranges, low-resistance shunts are connected in parallel with the meter movement to divert most of the current around the coil. Again for the case of a hypothetical 1 mA, 500 ohm movement on a 1 Ampererange, the shunt resistance would be just over 0.5 ohms.

Moving coil instruments respond only to the average value of the current through them. To measure alternating current, a rectifier diode is inserted in the circuit so that the average value of current is non-zero. Since the rectified average value and the root-mean-square value of a waveform need not be the same, simpler rectifier-type circuits may only be accurate for sinusoidal waveforms. Other wave shapes require a different calibration factor to relate RMS and average value. Since practical rectifiers have non-zero voltage drop, accuracy and sensitivity is poor at low values.

To measure resistance, a small battery within the instrument passes a current through the device under test and the meter coil. Since the current available depends on the state of charge of the battery, a multimeter usually has an adjustment for the ohm scale to zero it. In the usual circuit found in analog multimeters, the meter deflection is inversely proportional to the resistance; so full-scale is 0 ohms, and high resistance corresponds to smaller deflections. The ohm scale is compressed, so resolution is better at lower resistance values.

Amplified instruments simplify the design of the series and shunt resistor networks. The internal resistance of the coil is decoupled from the selection of the series and shunt range resistors; the series network becomes a voltage divider. Where AC measurements are required, the rectifier can be placed after the amplifier stage, improving precision at low range.



**Fig 1(a).5Digital Multi meter**

Digital instruments, which necessarily incorporate amplifiers, use the same principles as analog instruments for range resistors. For resistance measurements, usually a small constant current is passed through the device under test and the digital multimeter reads the resultant voltage drop; this eliminates the scale compression found in analog meters, but requires a source of significant current. An auto ranging digital multimeter can automatically adjust the scaling network so that the measurement uses the full precision of the A/D converter.

In all types of multimeters, the quality of the switching elements is critical to stable and accurate measurements. Stability of the resistors is a limiting factor in the long-term accuracy and precision of

the instrument.

### Quantities measured

Contemporary multimeters can measure many quantities. The common ones are:

- Voltage, alternating and direct, in volts.
- Current alternating and direct in amperes.

The frequency range for which AC measurements are accurate must be specified.

- Resistance in ohms.

Additionally, some multimeters measure:

- Capacitance in farads.
- Conductance in Siemens.
- Decibels.
- Duty cycle as a percentage.
- Frequency in hertz.
- Inductance in henrys.
- Temperature in degrees Celsius or Fahrenheit, with an appropriate temperature test probe, often a thermocouple.

Digital multimeters may also include circuits for:

- Continuity tester; sounds when a circuit conducts
- Diodes (measuring forward drop of diode junctions), and transistors (measuring current gain and other parameters)
- Battery checking for simple 1.5 volt and 9 volt batteries. This is a current loaded voltage scale which simulates in-use voltage measurement.

## RESISTANCE & RESISTOR

The electrical resistance of an electrical element measures its opposition to the passage of an electric current; the inverse quantity is electrical conductance, measuring how easily electricity flows along a certain path. Electrical resistance shares some conceptual parallels with the mechanical notion of friction. The SI unit of electrical resistance is the ohm ( $\Omega$ ), while electrical conductance is measured in Siemens (S).

An object of uniform cross section has a resistance proportional to its resistivity and length and inversely proportional to its cross-sectional area. All materials show some resistance, except for superconductors, which have a resistance of zero.

The resistance of an object is defined as the ratio of voltage across it to current through it:

$$R = \frac{V}{I}$$

Such materials are called Ohmic materials. For objects made of ohmic materials the definition of the resistance, with  $R$  being a constant for that resistor, is known as Ohm's law. In the case of a nonlinear conductor (not obeying Ohm's law), this ratio can change as current or voltage changes; the inverse slope of a chord to an  $I-V$  curve is sometimes referred to as a "chordal resistance" or "static resistance".



**Fig.1(a).6 Resistor**

### **DCresistance**

The resistance of a given resistor or conductor grows with the length of conductor and specific resistivity of the material, and decreases for larger cross-sectional area. The resistance  $R$  and conductance  $G$  of a conductor of uniform cross section, therefore, can be computed as

$$R = \rho \frac{L}{A}$$

$$G = \sigma \frac{A}{L}$$

where  $L$  is the length of the conductor, measured in meters [m],  $A$  is the cross-section area of the conductor measured in square meters [ $m^2$ ],  $\sigma$  (sigma) is the electrical conductivity measured in Siemens per meter ( $Sm^{-1}$ ), and  $\rho$  (rho) is the electrical resistivity (also called specific electrical resistance) of the material, measured in ohm-meters ( $\Omega m$ ). Resistivity is a measure of the material's ability to oppose electric current. For purely resistive circuits conductance is related to resistance  $R$  by:

$$G = \frac{1}{R}$$

For practical reasons, any connections to a real conductor will almost certainly mean the current density is not totally uniform. However, this formula still provides a good approximation for long thin conductors such as wires.

### **ACresistance**

A wire carrying alternating current has a reduced effective cross sectional area because of the skin effect.

Adjacent conductors carrying alternating current have a higher resistance than they would in isolation or when carrying direct current, due to the proximity effect. At commercial power frequency, these effects are significant for large conductor sets carrying large currents, such as busbars in an electrical substation, or large power cables carrying more than a few hundred amperes.

red amperes.

When an alternating current flows through the circuit, its flow is not opposed only by the circuit resistance, but also by the opposition of electric and magnetic fields to the current change. That effect is measured by electrical reactance. The combined effects of reactance and resistance are expressed by electrical impedance.

### Measuring resistance

An instrument for measuring resistance is called an ohmmeter. Simple ohmmeters cannot measure low resistances accurately because the resistance of their measuring leads causes a voltage drop that interferes with the measurement, so more accurate devices use four-terminal sensing.

### Temperature dependence

Near room temperature, the electric resistance of a typical metal increases linearly with rising temperature, while the electrical resistance of a typical semiconductor decreases with rising temperature. The amount of that change in resistance can be calculated using the temperature coefficient of resistivity of the material using the following formula:

$$R(T) = R_0[1 + \alpha(T - T_0)]$$

Where  $T$  is its temperature,  $T_0$  is a reference temperature (usually room temperature),  $R_0$  is the resistance at  $T_0$ , and  $\alpha$  is the percentage change in resistivity per unit temperature. The constant  $\alpha$  depends only on the material being considered. The relationship stated is actually only an approximate one, the true physics being somewhat non-

linear, or looking at it another way, it itself varies with temperature. For this reason it is usual to specify the temperature at which was measured at with a suffix, such as  $\alpha_{15}$  and the relationship only holds in a range of temperatures around the reference.

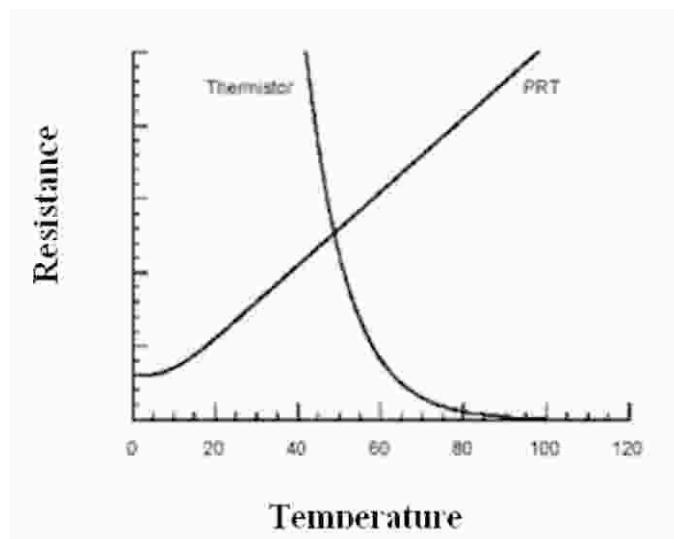


Fig.1(a).7: Temperature characteristics of resistance

## INDUCTANCE AND INDUCTOR

### I. Elementary Characteristics

The coil in the figure simulates an inductor. The main issue is how the magnetic field lines go across the inductor (lines with arrows). There is some magnetic field at the top and bottom of the coil too.

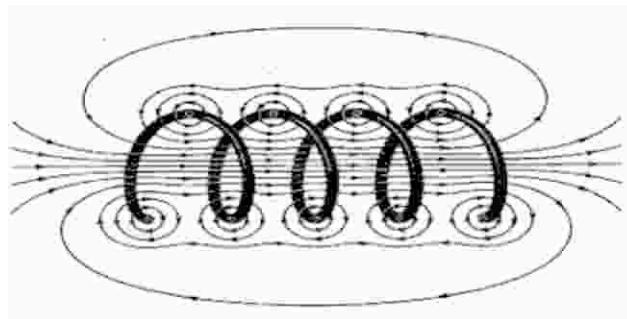
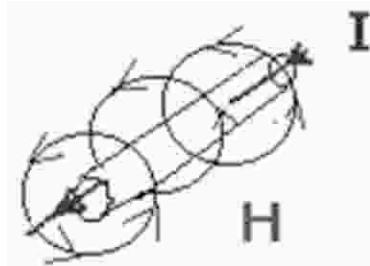


Fig. 1(a).8 Elementary characteristics

The current  $I$  going through the inductor generates a magnetic field which is perpendicular to  $I$ . The Magnetic Field  $H$  is given by the loops that surround the current  $I$ . The direction of the Magnetic Field is given by the arrows around the loops. If the current was to flow in the opposite direction the Magnetic Field arrows would be reversed. For a practical display of this



phenomenon see: Magnetic field on wire.

Fig. 1(a).8 Magnetic field on wire

It is the Magnetic Field which contains the current through the coil which by the principle called Self-Induction will induce a voltage  $V$ . More specifically speaking, the voltage  $V$  across the inductor  $L$  is given by:  $V = \Delta\Phi/\Delta T$  which reads – the voltage  $V$  is caused by the change in flux over the correspondent change in time, but since the change in flux is given by the inductance  $L$  and the change in current across the coil  $\Delta I$ , the voltage  $V$  becomes:

$$V = L \frac{\Delta I}{\Delta T} \quad (\text{Electrical definition for inductance})$$

On the other hand the physical definition of inductance  $L$  is given by:

$$L = \mu N^2 \frac{A}{l} \quad (\text{Physical definition for inductance})$$

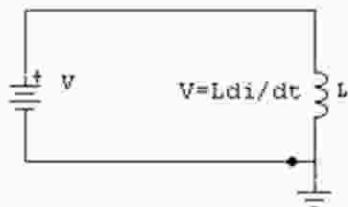
where  $\mu$  stands for the relative ease with which current flows through the inductor or Permeability of the medium.  $N$  stands for the number of turns in the coil,  $A$  stands for its cross-sectional area, and the length of the coil is given by  $l$ . Hence this formula tells us that the more number of turns the larger the inductance (i.e.: current can be contained better), also the larger the cross-sectional area the larger the inductance (since there is more flux of current that can be contained) and the longer the coil the smaller the inductance (since more current can be lost through the turns).  $L$  is also proportional to  $\mu$ , since the better the permeability current will flow with more ease.

### Inductance and Energy

By containing the current via the magnetic field the inductor is capable of storing Energy. A Transformer such as the one on the Figure will certainly remind us of the ability of storing Energy associated with Inductors. Whereas for a capacitor the Energy stored depends on the Voltage across it, for the inductor the Energy stored depends on the current being held, such that:

$$W = \frac{1}{2}LI^2 \text{ Where } W \text{ stands for the energy on the inductor}$$

### Basic Inductor Circuit



**Fig. 1(a).9 Basic inductor circuit**

The electrical parameters  $V$  and  $L$  (the inductance - measured in Henrys-H - review DC Basics or go to are given. The current  $I$  is implicitly given by the relationship:

$$V = L \frac{di}{dt}$$

In a similar case as with the basic capacitor circuit we are implying that at time 0 a switch closes connecting the battery to the coil and the inductor starts to get charged. Also, in all real cases there will be a small resistance in series with the inductor, but we will get to this case in the discussion of R-L circuits.

At a specific point of time the voltage across the inductor is expressed by  $V = Ldi/dt$  which is basically the electrical definition of inductance, except that since we are just focusing at a point in time and not at an interval of time  $\Delta t$  we will need to use the term  $dt$  and similarly for the current  $di$  instead of  $\Delta I$ . The electrical definition still holds, since all we are saying is that the flux or change in current over time times the inductance is the Induced Voltage across the Inductor.

### CAPACITOR

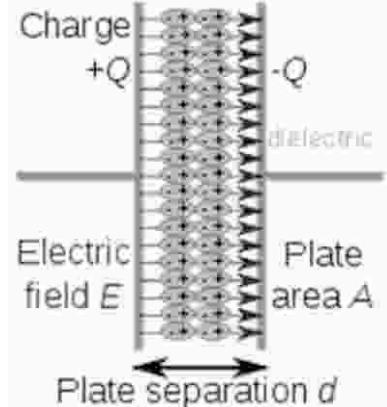
A capacitor is a passive electrical component that can store energy in the electric field between a pair of conductors (called "plates"). The process of storing energy in the capacitor is known as "charging", and involves electric charges of equal magnitude, but opposite polarity, building up on each plate. A capacitor's ability to store charge is measured by its capacitance, in units of farads.

Capacitors are often used in electric and electronic circuits as energy-stored devices. They can also be used to differentiate between high-frequency and low-frequency signals.



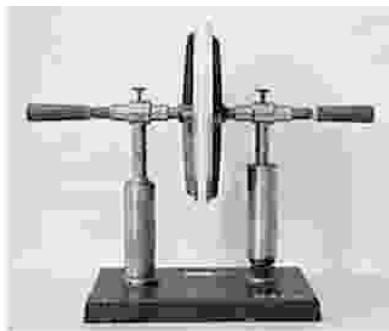
**Fig 1(a).10 Capacitor**

### Theory of Operation



**Fig1(a).11 Parallel-Plate capacitor**

Charge separation in a parallel-plate capacitor causes an internal electric field. A dielectric (orange) reduces the field and increases the capacitance. A capacitor consists of two conductors separated by a non-conductive region. The non-conductive region is called the dielectric or sometimes the dielectric medium. In simpler terms, the dielectric is just an electrical insulator. Examples of dielectric mediums are glass, air, paper, vacuum, and even a semiconductor depletion region chemically identical to the conductors. A capacitor is assumed to be self-contained and isolated, with no net electric charge and no influence from any external electric field. The conductors thus hold equal and opposite charges on their facing surfaces, and the dielectric develops an electric field. In SI units, a capacitance of one farad means that one coulomb of charge on each conductor causes a voltage of one volt across the device.



**Fig 1(a).12 Demonstration of a parallel-plate capacitor**

The capacitor is a reasonably general model for electric fields within electric circuits. An ideal capacitor is wholly characterized by a constant capacitance  $C$ , defined as the ratio of charge  $\pm Q$  on each conductor to the voltage  $V$  between them:

$$C = \frac{Q}{V}$$

Sometimes charge build-up affects the capacitor mechanically, causing its capacitance to vary. In this case, capacitance is defined in terms of current

$$C = \frac{dq}{dt}$$

mental changes:

### Energystorage

Work must be done by an external influence to "move" charge between the conductors in a capacitor. When the external influence is removed the charge separation persists in the electric field and energy is stored to be released when the charge is allowed to return to its equilibrium position. The work done in establishing the electric field, and hence the amount of energy stored, is given by:

$$W \int_{q=0}^Q V dq = \int_{q=0}^Q \frac{q}{C} dq = \frac{1}{2} \frac{Q^2}{C} = \frac{1}{2} CV^2 = \frac{1}{2} VQ$$

## OBSERVATION TABLE

S.No.	Components for identification/Testing	Type	Measured Value		Quality
			By multimeter	By Color Coding	
1.	Resistors	i.			
		ii.			
2.	Capacitors	i.			
		ii.			
3.	Inductors	i.			
		ii.			

## PROCEDURE

### For Resistors

- Identify the type of element and write in observation table.
- Find different value of resistor using color coding and multimeter, noted down in observation table.
- Using multimeter test given resistor for open and short conditions.

### For Inductors

- Identify the type of element and write in observation table.
- Find different value of resistor using color coding and multimeter, noted down in observation table.
- Using multimeter test given resistor for open and short conditions.

### For Capacitor

- Identify the type of element and write in observation table.
- Find different value of resistor using color coding and multimeter, noted down in observation table.
- Using multimeter test given resistor for open and short conditions.

## **RESULT**

Study of various passive components viz. resistor, capacitor, inductor and their testing and identification has done.

## **PRECAUTIONS**

1. All connection must be tight.
2. Get the circuit connections checked by the teacher before performing the experiment.
3. Power to the circuit must be switched on in the presence of the teacher.
4. Get the experimental readings checked by the teacher.
5. Don't touch directly the live parts of equipment and circuit.
6. Wear leather shoes in the lab.

## **VIVA VOICE:**

- 1) What are the various uses of multimeter?
- 2) What is a capacitor?
- 3) Which device is used to measure current?
- 4) What is a voltmeter?

## OSCILLOSCOPE

### **OBJECTIVE:**

Introduction and use of oscilloscope.

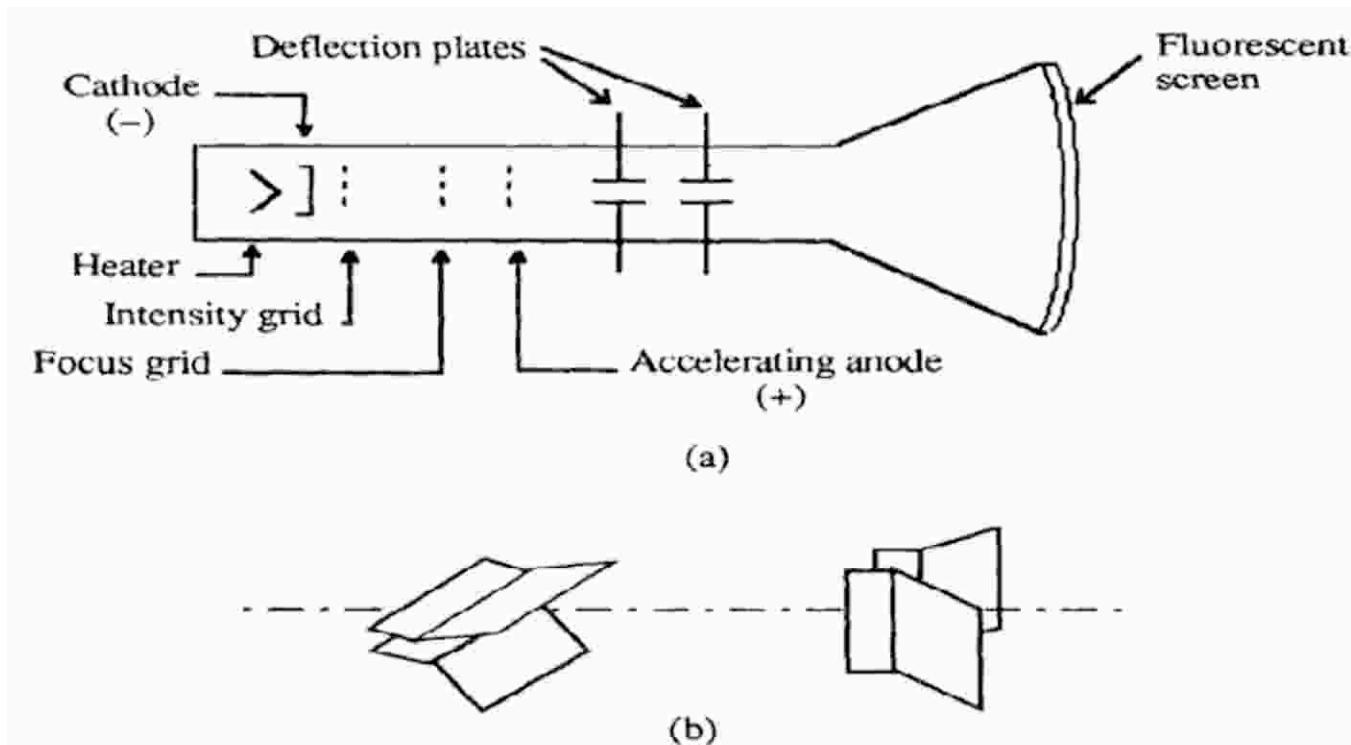
### **APPARATUS REQUIRED:**

S.No	Name of the equipment	Quantities	Type
1.	<b>Function Generator</b>	1	
2.	<b>CRO</b>	1	
3.	<b>Connecting Probe</b>	<b>As Required</b>	

### **THEORY**

#### **CATHODERAY OSCILLOSCOPE**

The cathode-ray oscilloscope (CRO) is a common laboratory instrument that provides accurate time and amplitude measurements of voltage signals over a wider range of frequencies. Its reliability, stability, and ease of operation make it suitable as a general-purpose laboratory instrument. The heart of the CRO is a cathode-ray tube shown schematically in Fig. 2.1



**Fig: 1(b).1 Cathode-ray oscilloscopes (CRO) (a) Schematic (b) Details of deflection Plate**

The cathode ray is a beam of electrons which are emitted by the heated cathode (negative electrode) and accelerated toward the fluorescent screen. The assembly of the cathode, intensity grid, focus grid, and accelerating anode (positive electrode) is called an electron gun. Its purpose is to generate the electron beam and control its intensity and focus. Between the electron gun and the fluorescent screen are two pairs of metal plates - one oriented to provide horizontal deflection of the beam and one pair oriented to give

vertical deflection to the beam.

These plates are thus referred to as the horizontal and vertical deflection plates. The combination of these two deflections allows the beam to reach any portion of the fluorescent screen. Wherever the electron beam hits the screen, the phosphor is excited and light is emitted from that point. This conversion of electron energy into light allows us to write with points or lines of light on a otherwise darkened screen.

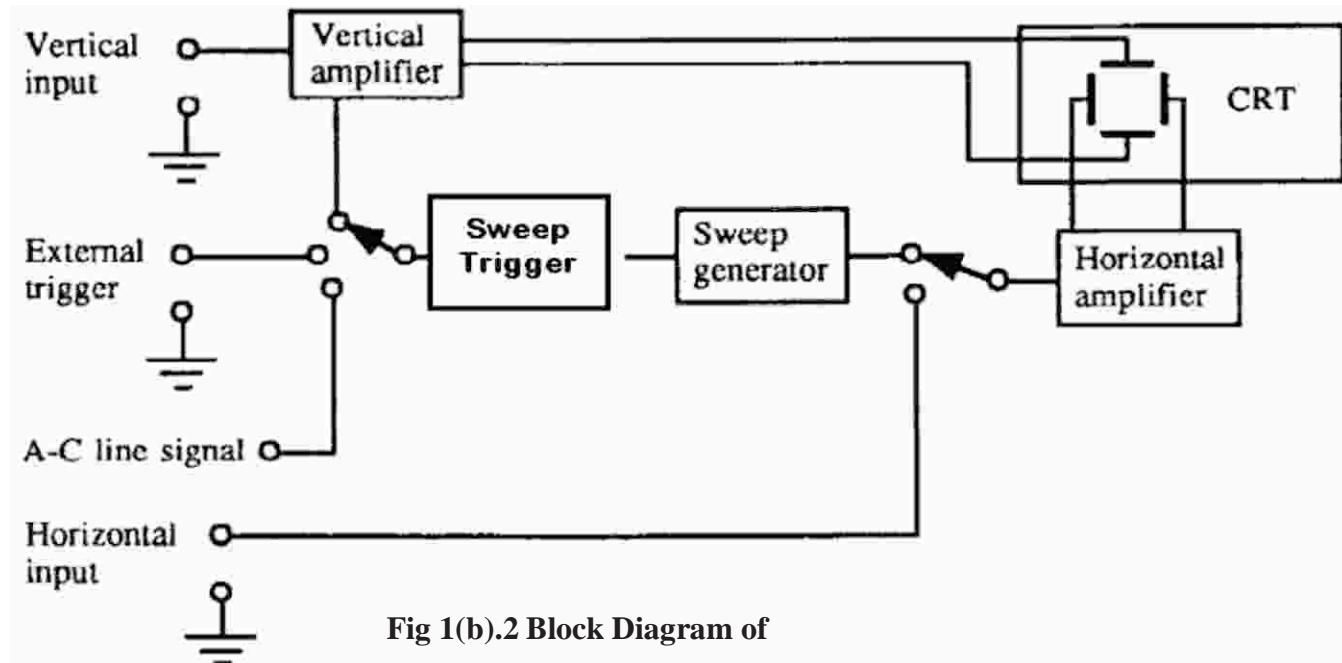


Fig 1(b).2 Block Diagram of

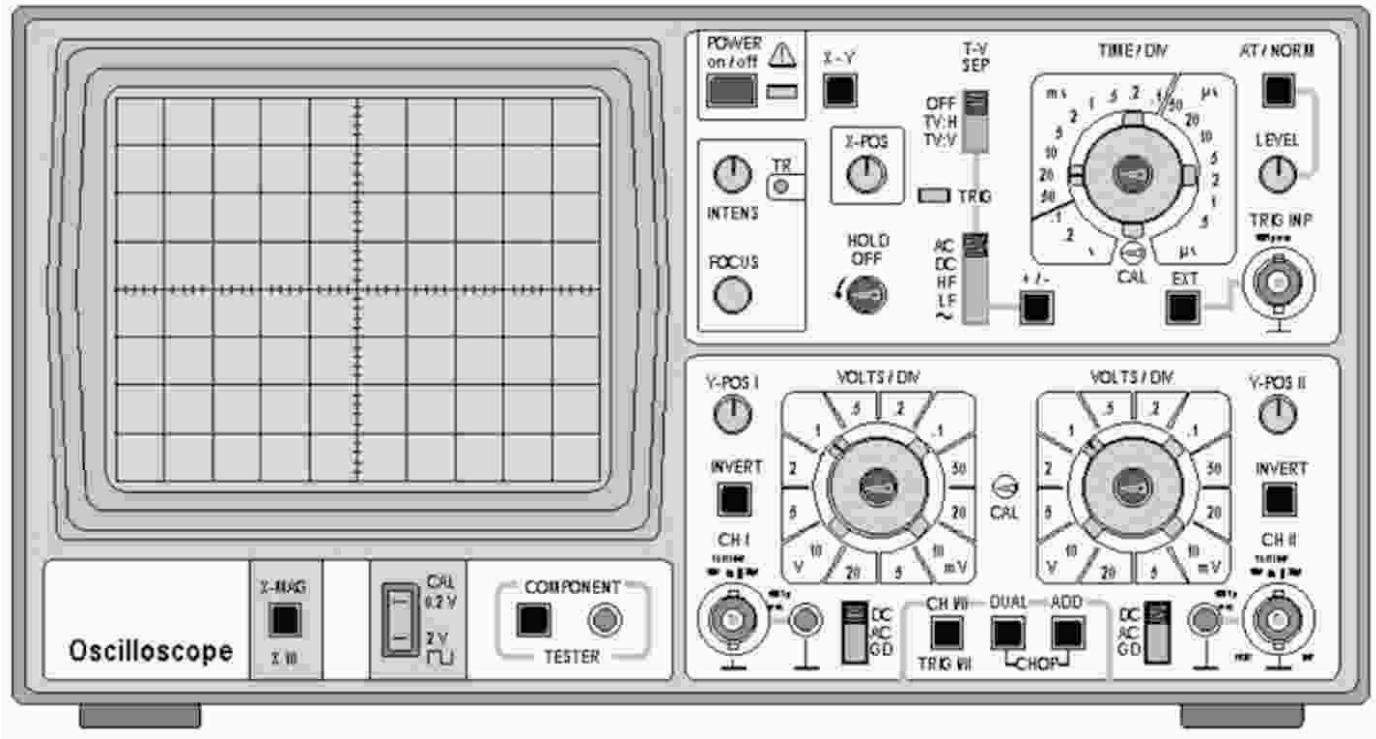
### Oscilloscope CONNECTIONS FOR THE OSCILLOSCOPE

**Vertical Input** A pair of jacks for connecting the signal under study to the Y (or vertical) amplifier. The lower jack is grounded to the case.

**Horizontal Input** A pair of jacks for connecting an external signal to the horizontal amplifier. The lower terminal is grounded to the case of the oscilloscope.

**External Trigger Input** Connector for external trigger signal.

**Cal. Out** Provides amplitude calibrated square waves of 25 and 500 milivolts for use in calibrating the gain of the amplifiers.



**Fig 1(b).3 Cathode Ray Oscilloscope**

Accuracy of the vertical deflection is +3%. Sensitivity is variable.

Horizontal sweep should be accurate to within 3%. Range of sweep is variable

Operating Instructions: Before plugging the oscilloscope into a wall receptacle, set the controls as follows:

- Powerswitch off
- Intensity fully counter clockwise
- Vertical centering in the center of fringe
- Horizontal centering in the center of fringe
- Vertical at 0.2
- Sweep times 1

**WARNING** Never advance the Intensity Control so far that an excessively bright spot appears. Bright spots simply burn ing of the screen. A sharp focused spot of high intensity (great brightness) should never be allowed to remain fixed in one position on the screen for any length of time as damage to the screen may occur.

## PROCEDURE

- I. Set the signal generator to a frequency of 1000 cycles per second. Connect the output from the generator to the vertical input of the oscilloscope. Establish a steady trace of this input signal on the scope. Adjust (play with) all of the scope and signal generator controls until you become familiar with the function of each.
- II. Measurements of Voltage: By adjusting the Horizontal Sweep time/cm and trigger, a steady trace of the sine wave may be displayed on the screen. The trace represents a plot of voltage vs. time, where the vertical

deflection of the trace about the line of symmetry CD is proportional to the magnitude of the voltage at any instant of time.

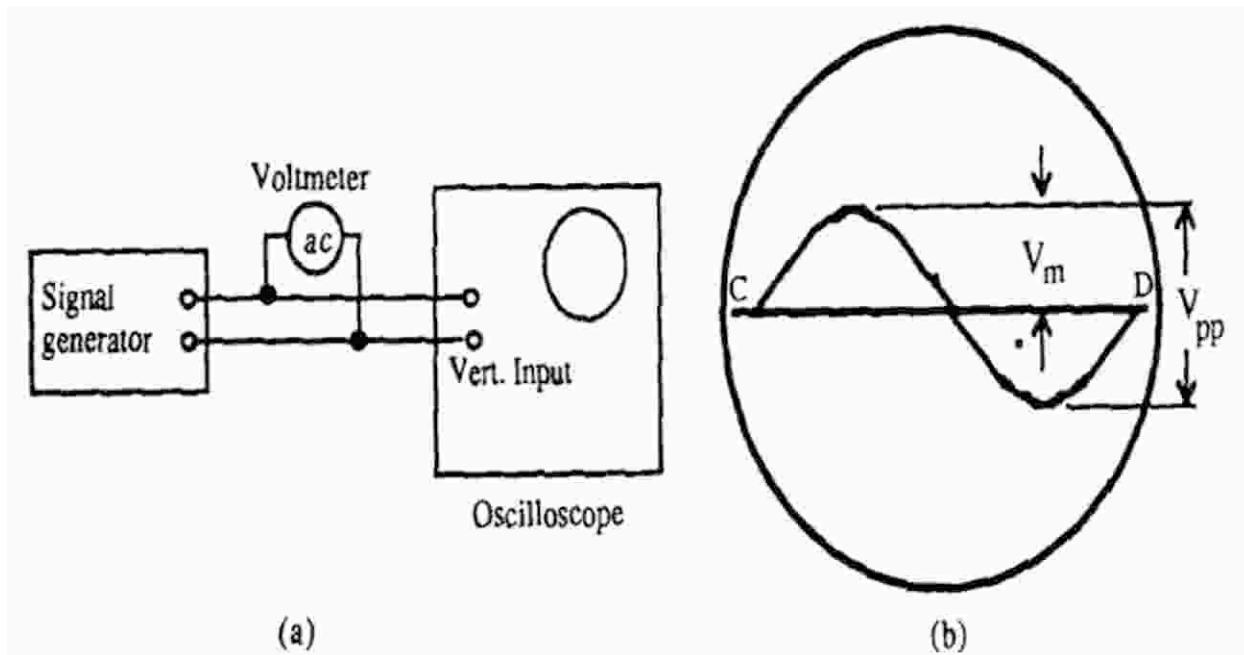


Fig 1(b).4(a) Circuit for Procedure (b) Trace Seen on Scope

The relationship between the magnitude of the peak voltage displayed on the scope and the effective or RMS voltage (VRMS) read on the AC voltmeter is

$$VRMS = 0.707 V_m \text{ (for sine or cosine wave). Thus}$$

$$V_m = \frac{V_{rms}}{0.707}$$

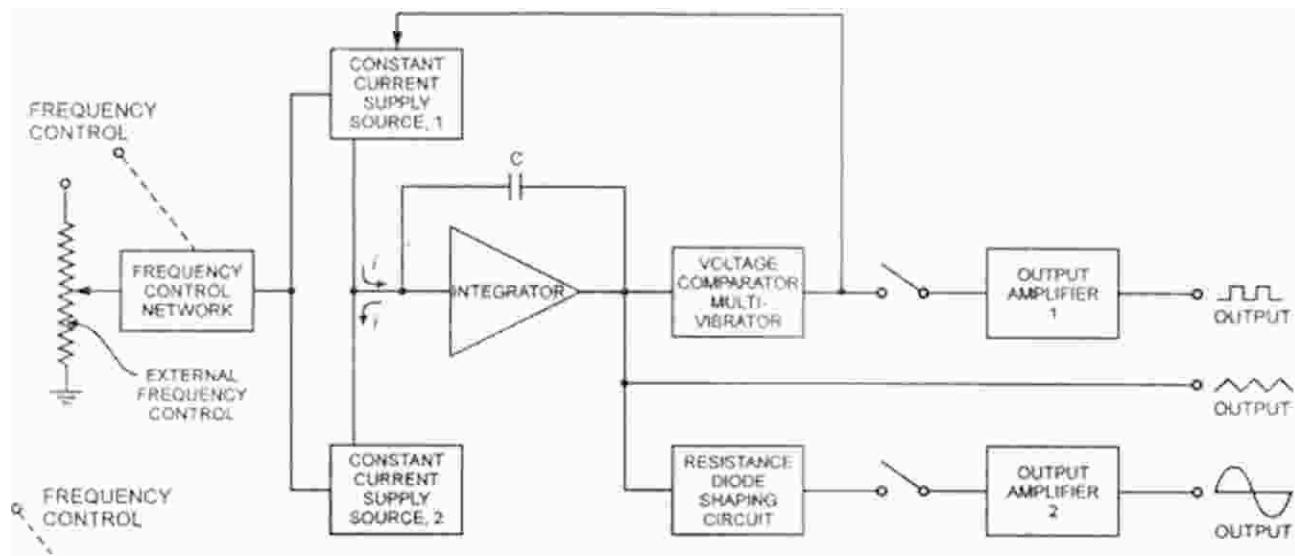
**III. Frequency Measurements:** When the horizontal sweep voltage is applied, voltage measurements can still be taken from the vertical deflection. Moreover, the signal is displayed as a function of time. If the time base (i.e. sweep) is calibrated, such measurements as pulse duration or signal period can be made. Frequencies can then be determined as reciprocal of the periods.

**IV. Lissajous Figures:** When sine-waves signals of different frequencies are input to the horizontal and vertical amplifiers a stationary pattern is formed on the CRT when the ratio of the two frequencies is an integral fraction such as 1/2, 2/3, 3/4, 4/3, 1/5, etc. These stationary patterns are known as Lissajous figures and can be used for comparison measurements of frequencies.

Use two oscillators to generate some simple Lissajous figures like those shown in Fig. You will find it difficult to maintain the Lissajous figures in a fixed configuration because the two oscillators are not phase and frequency locked. Their frequencies and phase drift slowly causing the two different signals to change slightly with respect to each other.

## FUNCTION GENERATOR

A function generator is a device that can produce various patterns of voltage at a variety of frequencies and amplitudes. It is used to test the response of circuits to common input signals.



**Fig 1(b).5 Function Generator**

A function generator is usually a piece of electronic test equipment or software used to generate different types of electrical waveforms over a wide range of frequencies. Some of the most common waveforms produced by the function generator are the sine, square, triangular and saw tooth shapes. These waveforms can be either repetitive or single-shot (which requires an internal or external trigger source). Integrated circuits used to generate waveforms may also be described as function generator ICs. Although function generators cover both audio and RF frequencies, they are usually not suitable for applications that need low distortion or stable frequency signals. When those traits are required, other signal generators would be more appropriate. Some function generators can be phase-locked to an external signal source (which may be a frequency reference) or another function generator. Function generators are used in the development, test and repair of electronic equipment. For example, they may be used as a signal source to test amplifiers or to introduce an error signal into a control loop.

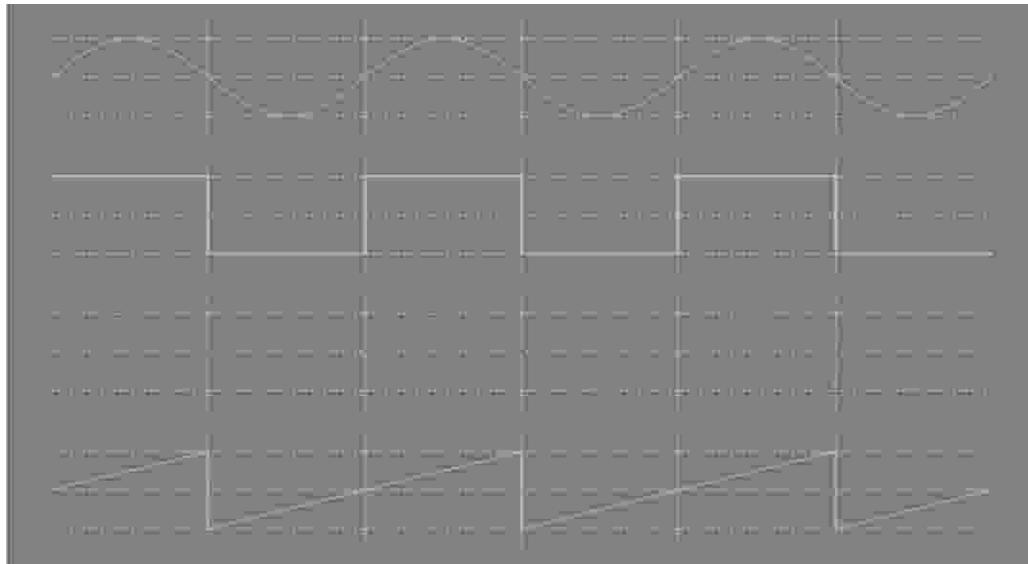
### Working

Simple function generators usually generate triangular waveform whose frequency can be controlled smoothly as well as in steps. This triangular wave is used as the basis for all of its other outputs. The triangular wave is generated by repeatedly charging and discharging a capacitor from a constant current source. This produces a linearly ascending or descending voltage ramp. As the output voltage reaches upper and lower limits, the charging and discharging is reversed using a comparator, producing the linear triangle wave. By varying the current and the size of the capacitor, different frequencies may be obtained. Sawtooth waves can be produced by charging the capacitor slowly, using a current, but using a diode over the current source to discharge quickly - the polarity of the diode changes the polarity of the resulting sawtooth, i.e. slow rise and fast fall, or fast rise and slow fall.

A 50% duty cycle square wave is easily obtained by noting whether the capacitor is being charged or discharged, which is reflected in the current switching comparator output. Other duty cycles (theoretically from 0% to 100%) can be obtained by using a comparator and the saw tooth or triangle signal. Most function generators also contain a non-linear diode shaping circuit that can convert the triangle wave into a reasonably accurate sine wave by rounding off the corners of the triangle wave in a process similar to clipping in audio systems.

A typical function generator can provide frequencies up to 20 MHz. RF generators for higher frequencies are not function generators in the strict sense since they typically produce pure or modulated sine signals only. Function generators, like most signal generators, may also contain an attenuator, various means of modulating the output waveform, and often the ability to automatically and repetitively "sweep" the frequency of the output waveform (by means of a voltage-controlled oscillator) between two operator-determined limits. This capability makes it very easy to evaluate the frequency response of a given electronic circuit. Some function generators can also generate white or pink noise.

More advanced function generators are called arbitrary waveform generators (AWG). They use direct digital synthesis (DDS) techniques to generate any waveform that can be described by a table of amplitudes.



**Fig. 1(b).6 Function generators output waveforms**

## RESULT

We have studied about the construction, working of CRO, function generator and learn how to measure frequency, voltage with the help of CRO.

## PRECAUTIONS

1. All connections must be tight.
2. Get the circuit connections checked by the teacher before performing the experiment.
3. Power to the circuit must be switched on in the presence of the teacher.

## Experiment No. 01(b)

### **Aim:**

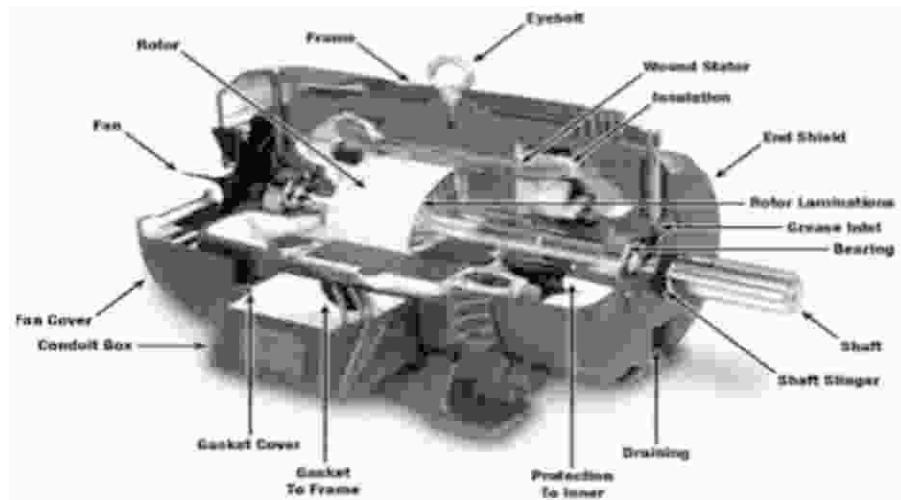
Demonstration of cut-out sections of machines: dc machine (Commutator brush arrangement), induction machine (squirrel cage rotor), synchronous machine (field winding-slip ring arrangement) and single-phase induction machine.

### **DC Machine: Construction and their Applications**

The DC machine can be classified into two types namely DC motors as well as DC generators. Most of the DC machines are equivalent to AC machines because they include AC currents as well as AC voltages in them. The output of the DC machine is DC output because they convert AC voltage to DC voltage. The conversion of this mechanism is known as the commutator, thus these machines are also named as commutating machines. DC machine is most frequently used for a motor. The main benefit of this machine includes torque regulation as well as speed regulation. The applications of the DC machine is limited to trains, mills, and mines. As examples, underground subway cars, as well as trolleybuses, may utilize DC motors. In the past, automobiles were designed with DC dynamos for charging their batteries.

### **What is a DC Machine?**

A DC machine is an electromechanical energy alteration device. The working principle of a DC machine is when electric current flows through a coil within a magnetic field, and then the magnetic force generates a torque which rotates the dc motor. The DC machines are classified into two types such as DC generator as well as DC motor. The main function of the DC generator is to convert mechanical power to DC electrical power, whereas a DC motor converts DC power to mechanical power. The AC motor is frequently used in the industrial applications for altering electrical energy to mechanical energy. However, a DC motor is applicable where

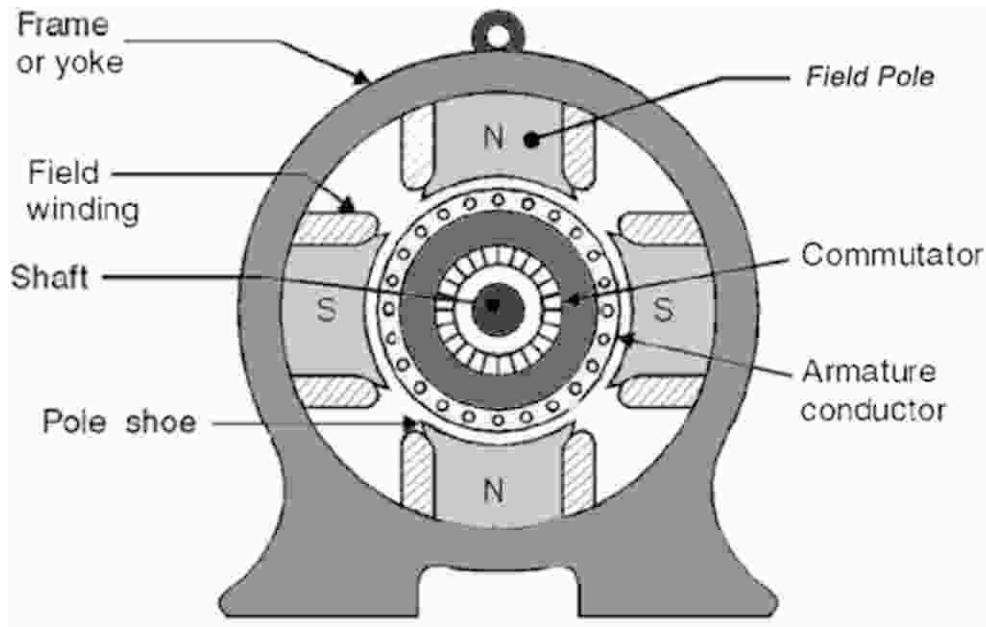


the good speed regulation & a wider range of speeds are necessary like in electric transactions systems.

**Fig 4.1: DC machine**

### **Construction of DC Machine**

The construction of DC machine can be done using some of the essential parts like Yoke, Pole core & pole shoes, Pole coil & field coil, Armature core, Armature winding otherwise conductor, commutator, brushes & bearings. Some of the parts of the DC machine is discussed below.



**Fig 4.2 : parts of dc machine**

### **Yoke**

Another name of a yoke is the frame. The main function of the yoke in the machine is to offer mechanical support intended for poles and protects the entire machine from the moisture, dust, etc. The materials used in the yoke are designed with cast iron, cast steel otherwise rolled steel.

### **Pole and Pole Core**

The pole of the DC machine is an electromagnet and the field winding is wound among pole. Whenever field winding is energized then the pole gives magnetic flux. The materials used for this are cast steel, cast iron otherwise pole core. It can be built with the annealed steel laminations for reducing the power drop because of the eddy currents.

### **Pole Shoe**

Pole shoe in DC machine is an extensive part as well as enlarge the region of the pole. Because of this region, flux can be spread out within the air-gap as well as extra flux can be passed through the air space toward armature. The materials used to build pole shoe is cast iron otherwise cast steel, and also used annealed steel lamination to reduce the loss of power because of eddy currents.

### **Field Windings**

In this, the windings are wounded in the region of pole core & named as field coil. Whenever current is supplied through field winding then it electromagnetic poles which generate required flux. The material used for field windings is copper.

### **Armature Core**

Armature core includes the huge number of slots within its edge. Armature conductor is located in these slots

It provides the low-reluctance path toward the flux generated with field winding. The materials used in this core are permeability low-reluctance materials like iron otherwise cast. The lamination is used to decrease the loss because of the eddy current.

### **Armature Winding**

The armature winding can be formed by interconnecting the armature conductor. Whenever an

armature winding is turned with the help of prime mover then the voltage, as well as magnetic flux, gets induced within it. This winding is allied to an exterior circuit. The materials used for this winding are conducting material like copper.

### **Commutator**

The main function of the commutator in the DC machine is to collect the current from the armature conductor as well as supplies the current to the load using brushes. And also provides uni-directional torque for DC-motor. The commutator can be built with a huge number of segments in the edge form of hard drawn copper. The Segments in the commutator are protected from thin mica layer.

### **Brushes**

Brushes in the DC machine gather the current from commutator and supplies it to exterior load. Brushes wear with time to inspect frequently. The materials used in brushes are graphite otherwise carbon which is in rectangular form.

### **AC Machine: Construction and working of squirrel cage induction motor.**

A 3 phase squirrel cage induction motor is a type of three phase induction motor which functions based on the principle of electromagnetism. It is called a 'squirrel cage' motor because the rotor inside of it – known as a 'squirrel cage rotor' – looks like a squirrel cage.

This rotor is a cylinder of steel laminations, with highly conductive metal (typically aluminum or copper) embedded into its surface. When an alternating current is run through the stator windings, a rotating magnetic field is produced.

This induces a current in the rotor winding, which produces its own magnetic field. The interaction of the magnetic fields produced by the stator and rotor windings produces a torque on the squirrel cage rotor.

One big advantage of a squirrel cage motor is how easily you can change its speed-torque characteristics.

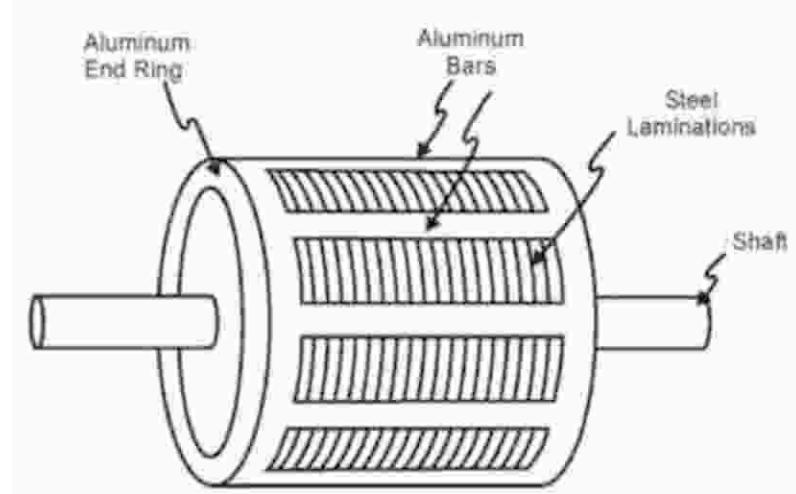
This can be done by simply adjusting the shape of the bars in the rotor. Squirrel cage induction motors are used a lot in industry – as they are reliable, self-starting, and easy to adjust.

### **Squirrel Cage Induction Motor Working Principle**

When a 3 phase supply is given to the stator winding it sets up a rotating magnetic field in space. This rotating magnetic field has a speed which is known as the synchronous speed.

This rotating magnetic field induces the voltage in rotor bars and hence short-circuit currents start flowing in the rotor bars. These rotor currents generate their self-magnetic field which will interact with the field of the stator. Now the rotor field will try to oppose its cause, and hence the rotor starts following the rotating magnetic field.

The moment the rotor catches the rotating magnetic field the rotor current drops to zero as there is no more relative motion between the rotating magnetic field and rotor. Hence, at that moment the rotor experiences zero tangential force hence the rotor decelerates for the moment.



**Fig 4.3 rotor**

After deceleration of the rotor, the relative motion between the rotor and the rotating magnetic field reestablishes hence rotor current again being induced. So again, the tangential force for rotation of the rotor is restored, and therefore again the rotor starts following rotating magnetic field, and in this way, the rotor maintains a constant speed which is just less than the speed of rotating magnetic field.

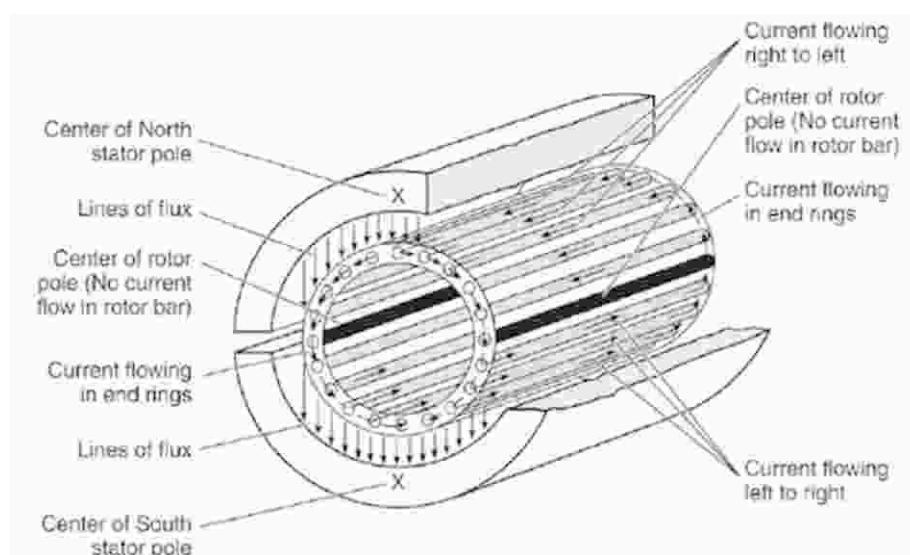
or synchronous speed. Slip is a measure of the difference between the speed of the rotating magnetic field and rotor speed. The frequency of the rotor current = slip × supply frequency

#### Squirrel Cage Induction Motor Construction

As squirrel cage induction motor consists of the following parts:

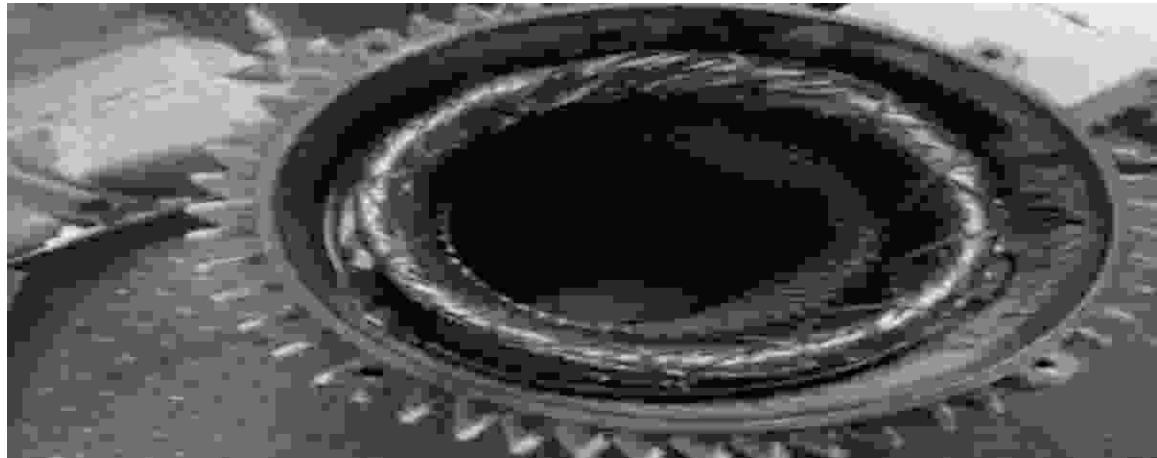
- Stator
- Rotor
- Fan

#### Bearings

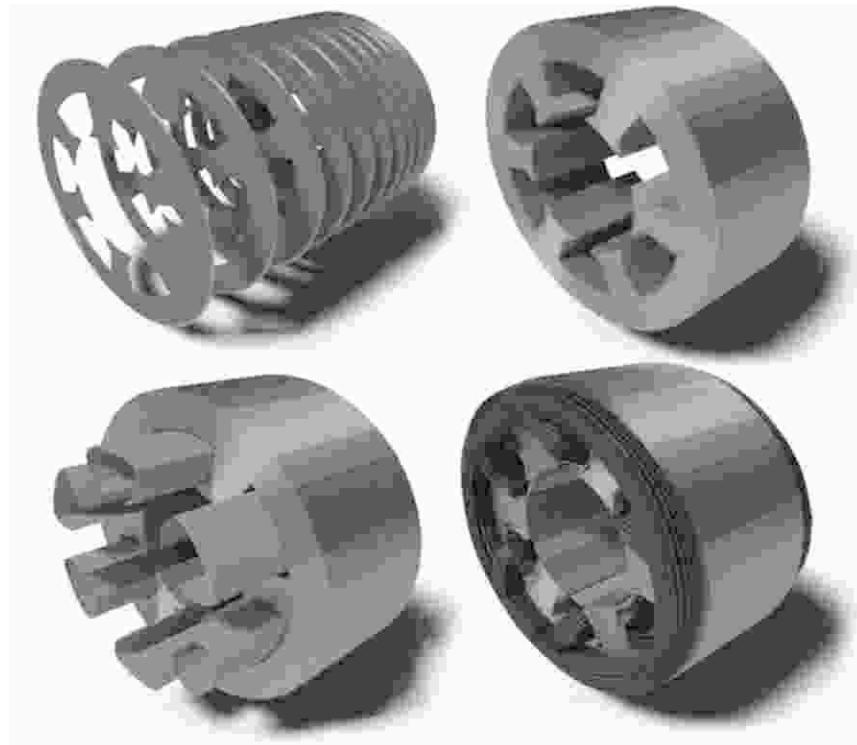


#### Stator

It consists of a 3 phase winding with a core and metal housing. Windings are such placed that they are electrically and mechanically 120° apart from each other in space. The winding is mounted on the laminated iron core to provide low reluctance path for generated flux by AC currents.



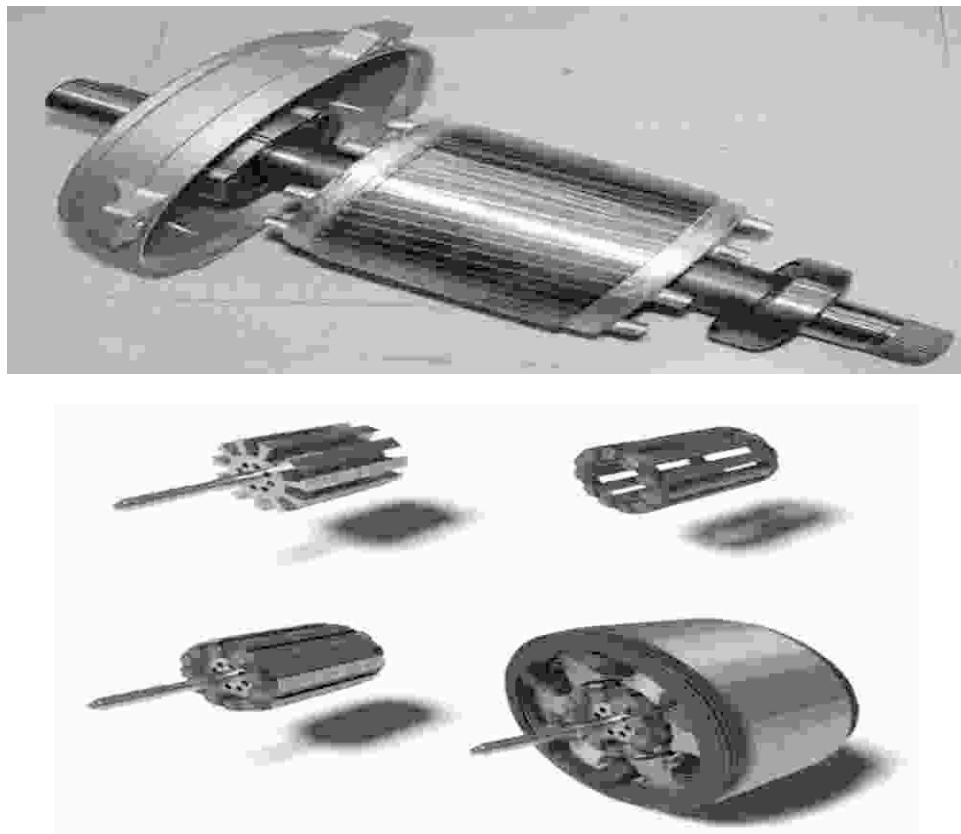
**Fig 4.5:stator**



**Fig 4.6:stator**

### **Rotor**

It is the part of the motor which will be in a rotation to give mechanical output for a given amount of electrical energy. The rated output of the motor is mentioned on the nameplate in horsepower. It consists of a shaft, short-circuited copper/aluminum bars, and a core.



**Fig 4.6:Rotor**

The rotor core is laminated to avoid power loss from eddy currents and hysteresis. Conductors are skewed to prevent cogging during starting operation and gives better transformation ratio between stator and rotor.

### Fan

A fan is attached to the back side of the rotor to provide heat exchange, and hence it maintains the temperature of the motor under limit.

### Bearings

Bearings are provided as the base for rotor motion, and the bearings keep the smooth rotation of the motor.

### Application of Squirrel Cage Induction Motor

Squirrel cage induction motors are commonly used in many industrial applications. They are particularly suited for applications where the motor must maintain a constant speed, be self-starting, or there is a desire for low maintenance.

### These motors are commonly used in:

- Centrifugal pumps
- Industrial drives (e.g. for conveyor belts)
- Large blowers and fans
- Machine tools
- Lathes and other turning equipment

### Advantages of Squirrel Cage Induction Motor

Some advantages of squirrel cage induction motors are:

- They are low cost
- Require less maintenance (as there are no slip rings or brushes)
- Good speed regulation (they are able to maintain a constant speed)
- High efficiency in converting electrical energy to mechanical energy (while running, not during startup)
- Have better heat regulation (i.e. don't get hot)
- Small and lightweight
- Explosion proof (as there are no brushes which eliminate the risk of sparking)

### **Disadvantages of Squirrel Cage Induction Motor**

Although squirrel cage motors are very popular and have many advantages – they also have some downsides. Some disadvantages of squirrel cage induction motors are:

- Very poor speed control
- Although they are energy efficient while running at full load current, they consume a lot of energy on startup
- They are more sensitive to fluctuations in the supply voltage. When the supply voltage is reduced, induction motor draws more current. During voltage surges, increase in voltage saturates the magnetic component of the squirrel cage induction motor
- They have high starting current and poor starting torque (the starting current can be 5-9 times the full load current; the starting torque can be 1.5-2 times the full load torque)

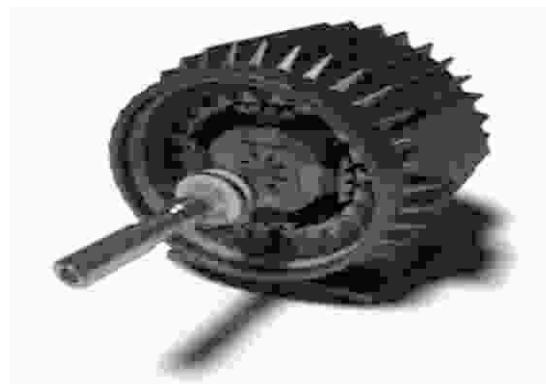
### **Construction of Synchronous Machines**

Synchronous machines run at synchronous speed. The synchronous speed is given by

$$N_s = \frac{120f}{p}$$

Where,  $N_s$  = synchronous speed,  $f$  = supply frequency and  $p$  = number of poles. As we can see from the equation, the synchronous speed depends on the frequency of the supply and the number of poles.

The construction of a synchronous machine is very similar to the construction of an alternator. Both are synchronous machines where one uses a motor and the other is a generator. Just like any other motor, the synchronous motor also has a stator and a rotor. We will look into the construction details of the various parts in one detail.



**Fig 4.7: Stator of Synchronous Motor**

The main stationary part of the machine is stator. The stator consists of the following parts.

**Stator Frame**

The stator frame is the outer part of the machine and is made up of cast iron. It protects the inner parts of the machine.



**Fig 4.8: Stator frame of Synchronous Motor**

**Stator Core**

The stator core is made up of thin silicon laminations. It is insulated by a surface coating to minimize hysteresis and eddy current losses. Its main purpose is to provide a path of low reluctance for the magnetic lines of force and accommodate the stator windings.



**Fig 4.9: Stator core of Synchronous Motor**

**Stator Winding**

The stator core has cutouts on the inner periphery to accommodate the stator windings. The stator windings could be either three-phase windings or single phase windings.



**Fig 4.10: Stator winding of Synchronous Motor**

Enamelled copper is used as the winding material. In the case of 3 phase windings, the windings are distributed over several slots. This is done to produce a sinusoidal distribution of EMF.

#### **Rotor of Synchronous Motor**

The rotor is the moving part of the machine. Rotors are available in two types:

- Salient Pole Type
- Cylindrical Rotor Type

The salient pole type rotor consists of poles projecting out from the rotor surface. It is made up of steel laminations to reduce eddy current losses.



**Fig 4.11: Rotor of Synchronous Motor**

A salient pole machine has a non-uniform air gap. The gap is maximum between the poles and is minimum at the pole centres. They are generally used for medium and low-speed operations as they have a large number of poles. They contain damper windings which are used for starting the motor.

A cylindrical rotor is made from solid forgings of high-grade nickel chrome molybdenum steel forgings of high-grade nickel chrome molybdenum steel. The poles are created by the current flowing through the windings. They are used for high-speed applications as they have less number of poles. They also produce less noise and windage losses as

they have a uniform air gap. DC supply is given to the rotor windings via slip-rings. Once the rotor windings are excited, they act like poles.

### **SinglePhaseInductionMotor**

We use the single-phase power system more widely than three phase system for domestic purposes, commercial purposes and some extent in industrial uses. Because, the single-phase system is more economical than a three-

phase system and the power requirement in most of the houses, shops, offices are small, which can be easily met by a single-phase system.

The single phase motors are simple in construction, cheap in cost, reliable and easy to repair and maintain.

Due to all these advantages, the single phase motor finds its application in vacuum cleaners, fans, washing machines, centrifugal pumps, blowers, washing machines, etc.

The single phase AC motors are further classified as:

1. Single phase induction motors or asynchronous motors.
2. Single phase synchronous motors.
3. Commutator motors.

This article will provide fundamentals, description and working principle of single phase induction motor. Construction of Single Phase Induction Motor

Like any other electrical motor, a synchronous motor also has two main parts namely rotor and stator.

## **Stator:**

As its name indicates stator is a stationary part of induction motor. A single phase AC supply is given to the stator of single phase induction motor.

## **Rotor:**

The rotor is a rotating part of an induction motor. The rotor connects the mechanical load through the shaft. The rotor in the single-phase induction motor is of squirrel cage type.

The construction of single phase induction motor is almost similar to the squirrel cage three-phase induction motor. But in case of a single phase induction motor, the stator has two windings instead of one three-phase winding in three phase induction motor.

### **Stator of Single Phase Induction Motor**

The stator of the single-phase induction motor has laminated stamping to reduce eddy current losses on its periphery. The slots are provided on it for stamping to carry stator or main winding. Stampings are made up of silicon steel to reduce hysteresis losses. When we apply a single phase AC supply to the stator winding, the magnetic field gets produced, and the motor rotates at speeds slightly less than the synchronous speed  $N_s$ .

$$N_s = \frac{120f}{P}$$

Synchronous speed  $N_s$  is given by

Where,  $f$  = supply voltage frequency,  $P$  = No. of poles of the motor.

The construction of the stator of the single-phase induction motor is similar to that of three phase induction motor except there are two dissimilarities in the winding part of the single phase induction motor.

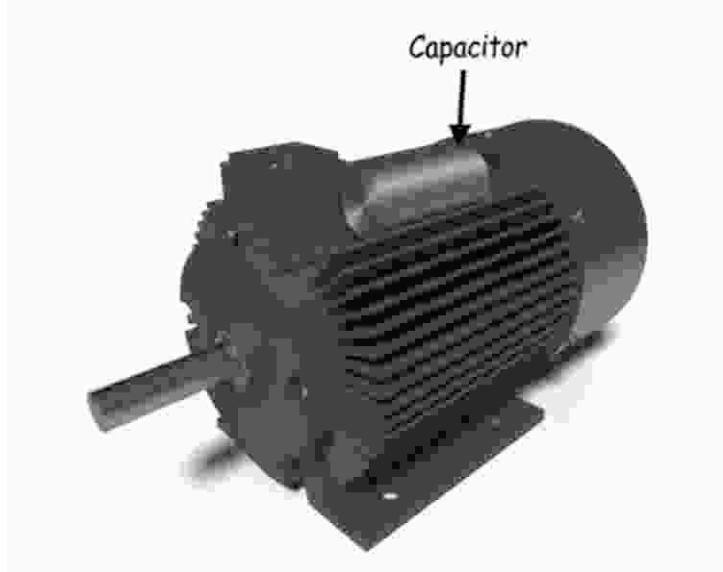
1. Firstly, the single-phase induction motors are mostly provided with concentric coils. We can easily adjust the number of turns per coil can with the help of concentric coils. The mmf distribution is almost sinusoidal.
2. Except for shaded pole motor, the synchronous motor has two stator windings namely the main winding and the auxiliary winding. These two windings are placed in space quadrature to each other.

### **Rotor of Single Phase Induction Motor**

The construction of the rotor of the single-phase induction motor is similar to the squirrel cage three-phase induction motor. The rotor is cylindrical and has slots all over its periphery. The slots are not made parallel to each other but are a little bit skewed as the skewing prevents magnetic locking of stator and rotor teeth and makes the working of induction motor more smooth and quiet (i.e. less noisy).

The squirrel cage rotor consists of aluminum, brass or copper bars. These aluminum or copper bars are called rotor conductors and placed in the slots on the periphery of the rotor. The copper or aluminum rings permanently short the rotor conductors called the end rings.

To provide mechanical strength, these rotor conductors are braced to the end ring and hence form a complete closed circuit resembling a cage and hence got its name as squirrel cage induction motor. As end rings permanently short the bars, the rotor electrical resistance is very small and it is not possible to add external resistance as the bars get permanently shorted. The absence of slip ring and brushes make the construction of single phase induction motor very simple and robust.



### **RESULT:**

We have studied about DC machines, Squirrel cage induction motor, Synchronous machines, 1-ph induction motor.

### **VIVA VOICE:**

1. Why 1-ph induction motor is not self-starting ?
2. What is the difference between dc machine and synchronous machine ?
3. What is slip ?
4. Write the types of dc machine ?

## Experiment No. 02

### 2(a): TRANSIENT RESPONSE OF R-C NETWORK

**AIM:**

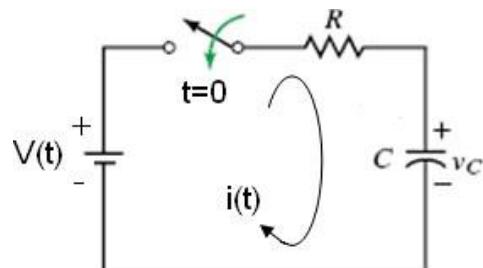
Study and obtain the transient response of a series R-C Network

**APPARATUS:**

S. No.	Name of the Equipment	Range	Type	Quantity
1	Function Generator			
2	Required resistors			
3	Required Inductors			
4	Required capacitors			
5	Voltmeter			
6	Connecting wires			

**CIRCUITDIAGRAM:**

**THEORY:** Let us consider the R-C circuit as shown below



Applying KVL, we obtain

$$v(t) = Ri(t) + \frac{1}{C} \int i(t) dt$$

Taking Laplace transform on both sides of the above equation,

$$V(s) = RI(s) + \frac{1}{C} \left[ \frac{I(s)}{s} + \frac{q(0_+)}{s} \right]$$

$$\text{or, } V(s) = RI(s) + \frac{I(s)}{sC} + \frac{v_C(0_+)}{s}$$

Now as all initial conditions set equal to zero, i.e.  $v_C(0_+) = 0$ , so the equation becomes

$$V(s) = RI(s) + \frac{I(s)}{sC}$$

$$\text{or, } V(s) = I(s) \left[ R + \frac{1}{sC} \right]$$

Example:

To study the transient response of a series R-C circuit where  $R=200\Omega$ ,  $C=10\mu F$  for the following conditions

1) source voltage is 40V DC with all initial conditions set equal to zero.

2) source voltage is a pulse signal with a period of 0s, width of 5ms, rise and fall times of 1μs, amplitude of 20V

and an initial value of 0V and all initial conditions set equal to zero.

**For Case - 1:-**

$$v(t) = 40u(t) \quad \therefore V(s) = \frac{40}{s}$$

Therefore,  $I(s) = \frac{sC}{RC(s + \frac{1}{RC})} \cdot \frac{40}{s} = \frac{40}{R} \cdot \frac{1}{(s + \frac{1}{RC})}$

Taking Inverse Laplace transform on both sides of the above equation,

$$i(t) = \frac{40}{R} e^{-\frac{1}{RC}t}$$

Putting  $R = 200\Omega$  and  $C = 10\mu F$ , we get

$$i(t) = \frac{40}{200} e^{-500t}$$

At  $t = 0$ ,  $i(t) = \frac{40}{200} A = 200mA$

At  $t = \infty$ ,  $i(t) = 0$

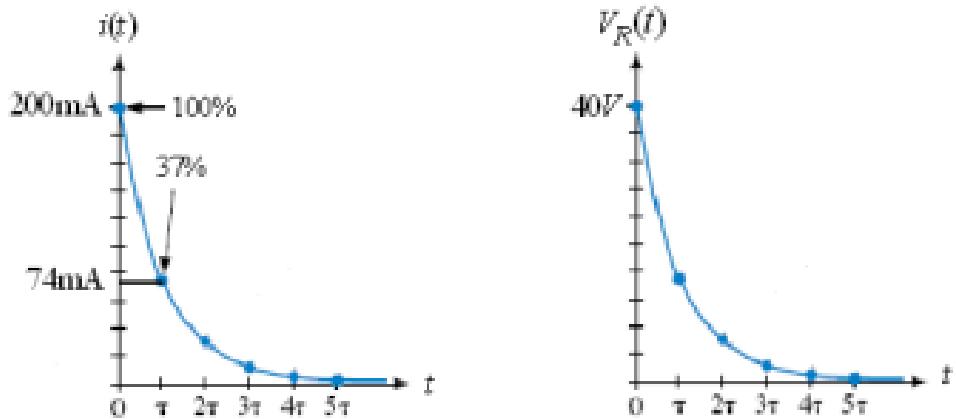
At  $t = \tau = RC = 2ms$ ,  $i(t) = 200 \times (37\%) = 74mA$

Voltage drop across the resistor  $R$  is,

$$V_R(t) = Ri(t) = 200 \times \frac{40}{200} \cdot e^{-500t} = 40e^{-500t}$$

At  $t = 0$ ,  $V_R(t) = 40V$

At  $t = \infty$ ,  $V_R(t) = 0$



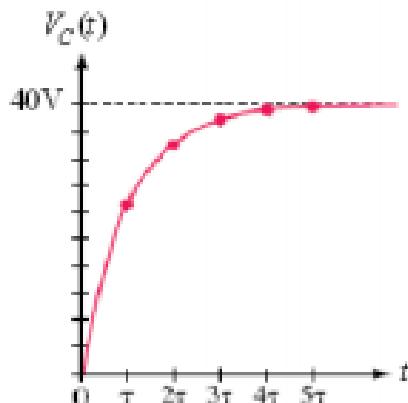
Voltage drop across the capacitor  $C$  is,

$$V_C(t) = v(t) - V_R(t) = 40(1 - e^{-500t})$$

At  $t = 0$ ,  $V_C(t) = 0$

At  $t = \infty$ ,  $V_C(t) = 40V$

The plot of  $V_C(t)$  vs.  $t$  is as follows:



For Case - 2:-

$$v(t) = 20[u(t) - u(t-1)]$$

Therefore,  $V(s) = \frac{20}{s}(1 - e^{-s})$

and,

$$I(s) = \frac{20sC(1-e^{-s})}{sRC(s + \frac{1}{RC})} \cdot \frac{1}{s} = \frac{20}{R} \cdot \left[ \frac{1}{(s + \frac{1}{RC})} - \frac{e^{-s}}{(s + \frac{1}{RC})} \right]$$

Taking Inverse Laplace transform on both sides of the above equation,

$$i(t) = \frac{20}{R} \left[ e^{-\frac{1}{RC}t} u(t) - e^{-\frac{1}{RC}(t-1)} u(t-1) \right]$$

Putting  $R = 200\Omega$  and  $C = 10\mu F$ , we get

$$i(t) = \frac{20}{200} \left[ e^{-500t} u(t) - e^{-500(t-1)} u(t-1) \right]$$

At  $t = 0$ ,  $i(t) = \frac{20}{200} A = 100mA$

At  $t = \infty$ ,  $i(t) = 0$

Voltage drop across the resistor  $R$  is,

$$V_R(t) = Ri(t) = 200 \times \frac{20}{200} \left[ e^{-500t} u(t) - e^{-500(t-1)} u(t-1) \right]$$

or,  $V_R(t) = 20 \left[ e^{-500t} u(t) - e^{-500(t-1)} u(t-1) \right]$

At  $t = 0$ ,  $V_R(t) = 20V$

At  $t = \infty$ ,  $V_R(t) = 0$

Voltage drop across the capacitor  $C$  is,

$$V_C(t) = v(t) - V_R(t) = 20(1 - e^{-500t})u(t) - 20(1 - e^{-500(t-1)})u(t-1)$$

At  $t = 0$ ,  $V_C(t) = 0$

At  $t = \infty$ ,  $V_C(t) = 0$

At  $t = T = 5ms$ ,  $V_C(t) = 18.36V$

## PROCEDURE:

1. Connect the circuit as shown in fig.
2. Set the voltage Accordingly Case 1 & Case 2.
3. Vary the frequency of the signal in steps and note down the magnitude of response on CRO respectively.( response wave form is observed across Elements ,RC)
4. Form the observation table between the time and magnitude of response in CRO.
5. Draw a graph between time and magnitude of response on the semi-log sheet.

### **Observation Table**

S. No	t(sec)	Ic (t)	Vc (t)	Vr(t)
1				
2				
3				
4				

**RESULT:** Transient response of a series R-C and series R-L circuit

## EXPERIMENT – 2 (b)

### TRANSIENT RESPONSE OF R-L NETWORK

**AIM:**

Study and obtain the transient response of a series R-L circuit

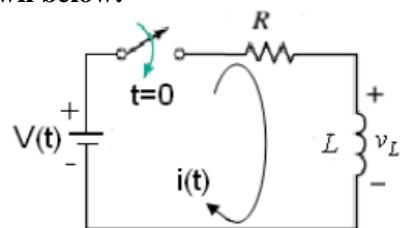
**APPARATUS:**

S. No.	Name of the Equipment	Range	Type	Quantity
1	Function Generator			
2	Required resistors			
3	Required Inductors			
4	Required capacitors			
5	Voltmeter			
6	Connecting wires			

**CIRCUITDIAGRAM:**

**THEORY:** Let us consider the R-L circuit as shown below

Let us consider the R-L circuit as shown below:



Applying KVL, we obtain

$$v(t) = Ri(t) + L \frac{di(t)}{dt}$$

Taking Laplace transform on both sides of the above equation,

$$V(s) = RI(s) + L[sI(s) - i(0)]$$

Now as all initial conditions set equal to zero, i.e.  $i(0) = 0$ , so the equation becomes

$$V(s) = I(s)[R + sL]$$

$$\text{or, } I(s) = \frac{V(s)}{R + sL} = \frac{1}{L} \cdot \frac{V(s)}{s + \frac{R}{L}}$$

To study the transient response of a series R-L circuit where  $R=100\Omega$ ,  $L=10mH$  for the following conditions:

- 1) Source voltage is 10V DC with all initial conditions set equal to zero.
- 2) Source voltage is 10V DC with initial condition  $i_L(0_-)=20mA$

For Case - 1:-

$$v(t) = 10u(t) \quad \therefore V(s) = \frac{10}{s}$$

Therefore,

$$I(s) = \frac{1}{L} \cdot \frac{\frac{10}{s}}{s + \frac{R}{L}} = \frac{1}{L} \left[ \frac{10}{s(s + \frac{R}{L})} \right] = \frac{10}{R} \left[ \frac{1}{s} - \frac{1}{s + \frac{R}{L}} \right]$$

Taking inverse Laplace transform on both sides of the above equation,

$$i(t) = \frac{10}{R} \left[ 1 - e^{-\frac{R}{L}t} \right]$$

Putting  $R=100\Omega$  and  $L=10mH$ , we get

$$i(t) = \frac{10}{100} \left[ 1 - e^{-10^4 t} \right]$$

At  $t=0$ ,  $i(t)=0$

At  $t=\infty$ ,  $i(t)=\frac{10}{100} A=100mA$

At  $t=\tau=\frac{L}{R}=100\mu s$ ,  $i(t)=100\times(63\%)=63mA$

Voltage drop across the resistor  $R$  is,

$$V_R(t) = Ri(t) = 100 \times \frac{10}{100} (1 - e^{-10^4 t}) = 10(1 - e^{-10^4 t})$$

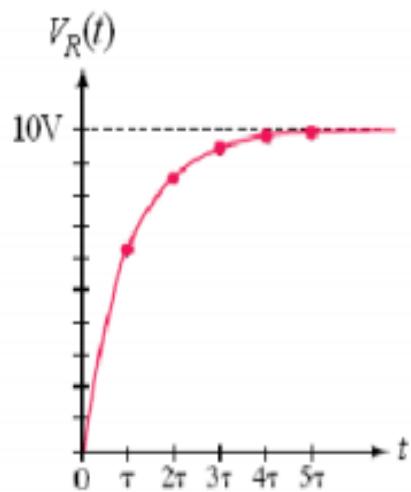
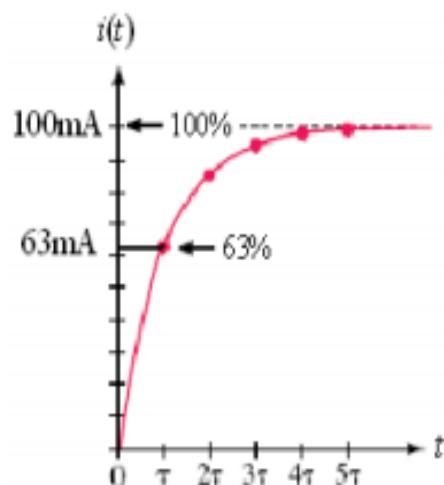
Voltage drop across the inductor  $L$  is,

$$V_L(t) = v(t) - V_R(t) = 10e^{-10^4 t}$$

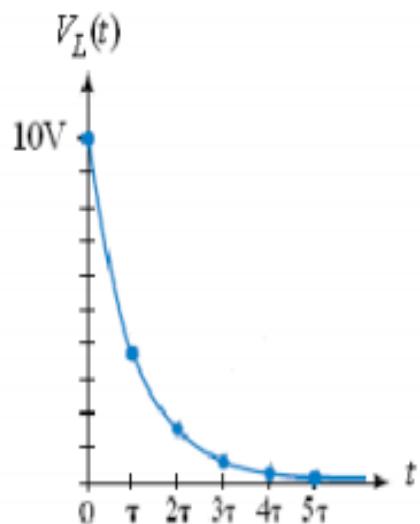
At  $t = 0$ ,  $V_L(t) = 10\text{V}$

At  $t = \infty$ ,  $V_L(t) = 0$

The plot of  $i(t)$  vs.  $t$  and  $V_R(t)$  vs.  $t$  are as follows:



The plot of  $V_L(t)$  vs.  $t$  is as follows:



**For Case - 2:-**

$$v(t) = 10u(t) \quad \text{and} \quad i_L(0_+) = 20\text{mA}$$

$$\therefore V(s) = \frac{10}{s}$$

$$V(s) = RI(s) + L[sI(s) - 20 \times 10^{-3}]$$

$$\text{or, } \frac{10}{s} + 20 \times 10^{-4} = LI(s) \left[ s + \frac{R}{L} \right]$$

$$\text{or, } I(s) = \frac{0.02}{s+10^4} + \frac{10^3}{s(s+10^4)}$$

Taking inverse Laplace transform on both sides of the above equation,

$$i(t) = 0.1 - 0.08e^{-10^4 t}$$

At  $t = 0$ ,  $i(t) = 20\text{mA}$

At  $t = \infty$ ,  $i(t) = 100\text{mA}$

Voltage drop across the resistor  $R$  is,

$$V_R(t) = Ri(t) = 100 \times i(t) = 100(0.1 - 0.08e^{-10^4 t}) = 10 - 8e^{-10^4 t}$$

At  $t = 0$ ,  $V_R(t) = 2\text{V}$

At  $t = \infty$ ,  $V_R(t) = 10\text{V}$

Voltage drop across the inductor  $L$  is,

$$V_L(t) = v(t) - V_R(t) = 8e^{-10^4 t}$$

At  $t = 0$ ,  $V_L(t) = 8\text{V}$

At  $t = \infty$ ,  $V_L(t) = 0$

## PROCEDURE:

1. Connect the circuit as shown in fig.
2. Set the voltage Accordingly Case 1 & Case 2.
3. Vary the frequency of the signal in steps and note down the magnitude of response on CRO respectively.( response wave form is observed across Elements ,RC)
4. Form the observation table between the time and magnitude of response in CRO.
5. Draw a graph between time and magnitude of response on the semi-log sheet.

**RESULT:** Transient response of a series R-C and series R-L circuit

## Experiment No. 2(c)

### TRANSIENT RESPONSE OF R-L-C

#### AIM:

Study and obtain the transient response of a R-L-C circuit

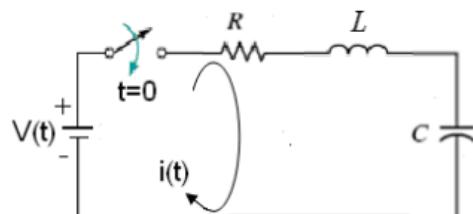
1. Transient response of a R-L-C circuit, excited by a unit step input

#### APPARATUS:

S. No.	Name of the Equipment	Range	Type	Quantity
1	Signal generator			
2	Required resistors			
3	Required Inductors			
4	Required capacitors			
5	CRO probes			
6	Connecting wires			

#### CIRCUITDIAGRAM:

THEORY: Let us consider the R-L-C circuit as shown below:



Applying KVL, we obtain

$$v(t) = Ri(t) + L \frac{di(t)}{dt} + \frac{1}{C} \int i(t) dt$$

Taking Laplace transform on both sides of the above equation,

$$V(s) = RI(s) + L[sI(s) - i(0)] + \frac{I(s)}{sC} + \frac{v_C(0)}{s}$$

Now as all initial conditions set equal to zero, i.e.  $i(0) = 0$  and  $v_C(0) = 0$ , so the equation becomes,

$$V(s) = I(s) \left[ R + sL + \frac{1}{sC} \right]$$

Here,  $v(t) = u(t)$

$$\therefore V(s) = \frac{1}{s}$$

Therefore,  $\frac{1}{s} = I(s) \left[ R + sL + \frac{1}{sC} \right]$

or,  $I(s) = \frac{\frac{1}{L}}{s^2 L + \frac{R}{L}s + \frac{1}{LC}}$

The roots of the denominator polynomial of the above equation are,

$$s^2 L + \frac{R}{L}s + \frac{1}{LC} = 0$$

or,  $s_1 = -\frac{R}{2L} + \sqrt{\frac{R^2}{4L^2} - \frac{1}{LC}}$  and

$$s_2 = -\frac{R}{2L} - \sqrt{\frac{R^2}{4L^2} - \frac{1}{LC}}$$

Let  $\omega_0 = \frac{1}{\sqrt{LC}}$  and  $\xi\omega_0 = \frac{R}{2L}$

$$\therefore \xi = \frac{R}{2} \sqrt{\frac{C}{L}}$$

Now,

$$I(s) = \frac{\frac{1}{L}}{(s-s_1)(s-s_2)} = \frac{1}{L(s_1-s_2)} \frac{1}{(s-s_1)} + \frac{1}{L(s_2-s_1)} \frac{1}{(s-s_2)}$$

or,  $I(s) = \frac{1}{2\omega_0 L \sqrt{\xi^2 - 1}} \left[ \frac{1}{(s-s_1)} - \frac{1}{(s-s_2)} \right]$

Taking inverse Laplace Transform on both sides,

$$i(t) = \frac{1}{2\omega_0 L \sqrt{\xi^2 - 1}} e^{-\xi\omega_0 t} \left[ e^{\omega_0 t \sqrt{\xi^2 - 1}} - e^{-\omega_0 t \sqrt{\xi^2 - 1}} \right]$$

**Case - 1:-**

$$R < 2\sqrt{\frac{L}{C}}$$

i.e.  $\xi < 1$

$$i(t) = \frac{1}{\omega_0 L \sqrt{1 - \xi^2}} e^{-\xi\omega_0 t} \sin(\omega_0 t \sqrt{1 - \xi^2})$$

The network is then said to be **Under Damped or Oscillatory**.

**Case - 2:-**

$$R = 2\sqrt{\frac{L}{C}}$$

i.e.  $\xi = 1$

$$i(t) = \frac{1}{L} t e^{-\omega_0 t}$$

The network is then said to be **Critically Damped**.

**Case - 3:-**

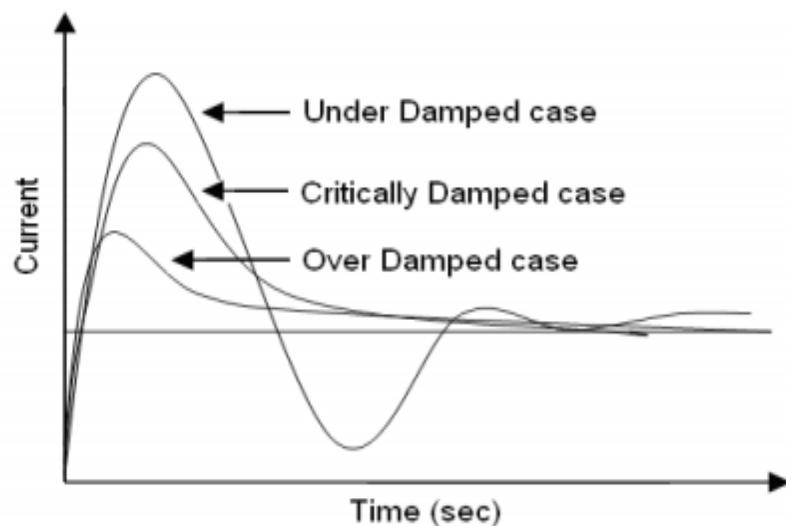
$$R > 2\sqrt{\frac{L}{C}}$$

i.e.  $\xi > 1$

$$i(t) = \frac{1}{\omega_0 L \sqrt{\xi^2 - 1}} e^{-\xi\omega_0 t} \sinh(\omega_0 t \sqrt{\xi^2 - 1})$$

The network is then said to be **Over Damped**.

The current response for the above three cases is shown in the figure below:



#### PROCEDURE:

1. Connect the circuit as shown in fig.
2. Set the voltage Accordingly.
3. Vary the frequency of the signal in steps and note down the magnitude of response on CRO respectively.
4. Form the observation table between the time and magnitude of response in CRO.
5. Draw a graph between time and magnitude of response on the semi-log sheet

**RESULT:** Transient response of a R-L-C circuit is verified

## **Experiment No-03** **VERIFICATION OF SERIESRESONANCE**

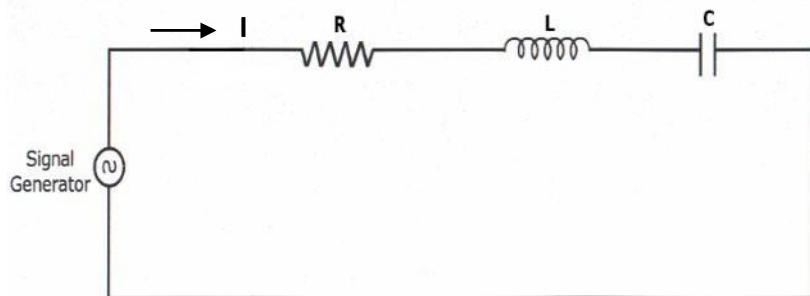
### **AIM:**

To design the resonant frequency, quality factor and band width of a series resonant circuit.

### **APPARATUS:**

S. No.	Name of the Equipment	Quantity
1.	Series resonance Trainer Kit	1
2.	Digital CRO	1
3.	CRO probes	As Required
4.	Connecting wires	As Required

### **CIRCUITDIAGRAM:**



**Fig – 12.1 Series Resonance**

### **PROCEDURE:**

1. Connect the circuit as shown in fig.12.1 for series resonant circuit
2. Set the voltage of the signal from function generator to 10V.
3. Vary the frequency of the signal in steps and note down the magnitude of response on CRO respectively.( response wave form is observed across element R)
4. Form the observation table between the frequency and magnitude of response in CRO for series resonance circuit.
5. Draw a graph between frequency and magnitude of response on the semi-log sheet and determine the resonant frequency, quality factor and bandwidth of series RLCC circuit\|.

## THEORETICAL CALCULATIONS: SeriesResonance

$$\text{Resonant Frequency } (f_r) = 1/(2\pi\sqrt{LC}) \quad \text{Lower cutoff frequency } (f_1) = f_r R / 4\pi L$$

$$\text{Upper cut off frequency } (f_2) = f_r + R / 4\pi L$$

$$\text{Quality factor } Q_r = \omega_r L / R = 1 / \omega_r R C$$

$$\text{BandWidth} \quad f_2 - f_1 = R / 2\pi L$$

## TABULARCOLUMN:

S.No	Frequency (kHz)	Magnitude of response
1	2.701	1.031
2	2.787	1.028
3	3.315	1.019
4	4.167	0.993
5	5.014	0.935
6	6.068	0.885
7	6.682(f <sub>r</sub> )	0.900
8	7.475	0.940
9	8.250	0.968
10	9.028	0.991

## MODELGRAPH:

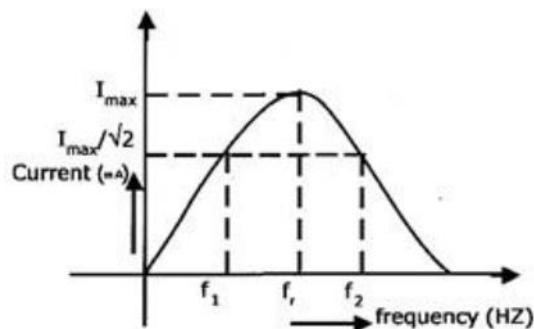


Fig – 12.2 Series Resonance

**RESULT:** Series Resonance is Verified

## Experiment No. 04

- a) Observation of the no-load current waveform for single phase transformer on CRO.  
 b) To measure the voltages and currents, and power of a for single phase transformer for resistive load and calculate turn ratio.

### **Apparatus Required**

S. No	Name of apparatus	Type	Specification	Quantity
1.	Single Phase Transformer	Core Type	2 KVA	1
2.	CRO	Dual channel	30 MHz	1
3.	Ammeter	MI	0-5 A	2
4.	Voltmeter	MI	0-300V	2
5.	Wattmeter	Electro Dynamic	0-1500W	2
6.	Load	Resistive	3KW	
7.	Connecting Leads		1.5 sq mm, 9A	

### **Introduction**

Transformers are one of the most important components of any power system. It basically changes the level of voltages from one value to the other at constant frequency. Being a static machine the efficiency of a transformer could be as high as 99%. The transformer is a static device (means that has no moving parts) that consists of one, two or more windings which are magnetically coupled and electrically separated with or without a magnetic core. It transfers the electrical energy from one circuit to the other by electromagnetic induction principle. The winding connected to the AC mains supply is called primary winding and the winding connected to the load or from which energy is drawn out is called secondary winding. These two windings with proper insulation are wound on a laminated core which provides a magnetic path between windings.

When the primary winding is energized with alternating voltage source, an alternating magnetic flux or field will be produced in the transformer core. This magnetic flux amplitude depends on the applied voltage magnitude, frequency of the supply and the number of turns on the primary side. This flux circulates through the core and hence links with the secondary winding. Based on the principle of electromagnetic induction, this magnetic linking induces a voltage in the secondary winding. This is called as mutual induction between two circuits.

### **Construction of single phase transformer**

Generally, the name associated with the construction of a transformer is dependent upon how the primary and secondary windings are wound around the central laminated steel core. The two most common and basic designs of transformer construction are the Closed-core Transformer and the Shell-core Transformer.

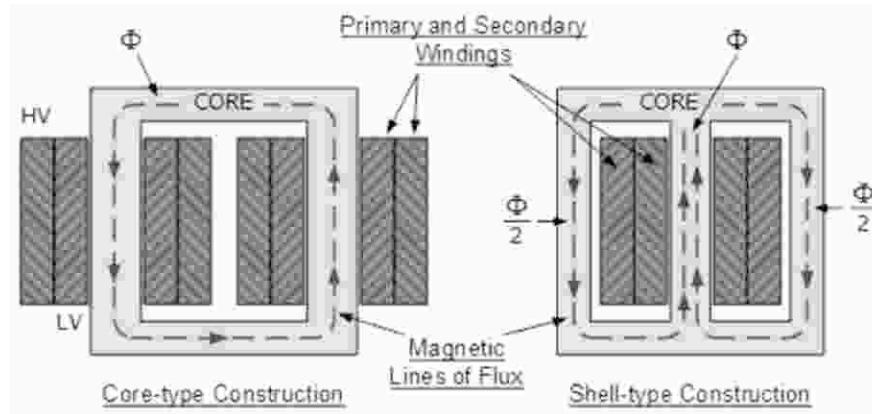
In the "closed-core" type (core form) transformer, the primary and secondary windings are wound outside and surround the core ring. In the "shell type" (shell form) transformer, the primary and secondary windings pass inside the steel magnetic circuit (core) which forms a shell around the windings as shown below.

In both types of transformer core design, the magnetic flux linking the primary and secondary windings travels entirely within the core with no loss of magnetic flux through air. In the core type transformer construction, one half of each winding is wrapped around each leg (or limb) of the transformer's magnetic circuit as shown above. The coils are not arranged with the primary winding on one leg and the secondary on the other but instead half of the primary winding and half of the secondary winding are placed one over the other concentrically on each leg in

order to increase magnetic coupling allowing practically all of the magnetic lines of force go through both the primary and secondary windings at the same time. However, with this type of transformer construction, a small percentage of the magnetic lines of force flow outside of the core, and this is called "leakage flux".

Shell type transformer cores overcome this leakage flux as both the primary and secondary windings are wound on the same centre leg or limb which has twice the cross-sectional area of the two outer limbs. The advantage here is that the magnetic flux has two closed magnetic paths to flow around external to the coils on both left and right hand sides before returning back to the central coils.

This means that the magnetic flux circulating around the outer limbs of this type of transformer construction is equal to  $\Phi/2$ . As the magnetic flux has a closed path around the coils, this has the advantage of decreasing core losses and increasing overall efficiency.



**Fig2(a).1 Core type and shell type transformer**

### Laminating the Iron Core

Eddy current losses within a transformer core cannot be eliminated completely, but they can be greatly reduced and controlled by reducing the thickness of the steel core. Instead of having one big solid iron core as the magnetic core material of the transformer or coil, the magnetic path is split up into many thin pressed steel shapes called "laminations".

The laminations used in a transformer construction are very thin strips of insulated metal joined together to produce a solid but laminated core as was seen above. These laminations are insulated from each other by a coat of varnish or paper to increase the effective resistivity of the core thereby increasing the overall resistance to limit the flow of the eddy currents.

The result of all this insulation is that the unwanted induced eddy current power-loss in the core is greatly reduced, and it is for this reason why the magnetic iron circuit of every transformer and other electromagnetic machines are all laminated. Using laminations in a transformer construction reduces eddy current losses.

The losses of energy, which appears as heat due both to hysteresis and to eddy currents in the magnetic path, is known commonly as "transformer core losses". Since these losses occur in all magnetic materials as a result

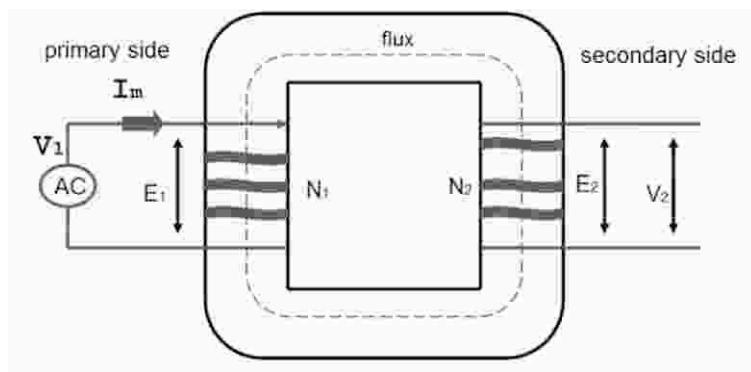
of alternating magnetic fields. Transformer core losses are always present in a transformer whenever the primary is energized, even if no load is connected to the secondary winding. Also these hysteresis and the eddy current losses are sometimes referred to as "transformer iron losses", as the magnetic flux causing these losses is constant at all loads.



**Fig. 2(a).2 Lamination of transformer**

#### **core Principle of operation Ideal Transformer**

A transformer is a device used to change voltages and currents of AC electric power by mutual induction principle. In the simplest version it consists of two windings wrapped around a magnetic core; windings are not electrically connected, but they are coupled by the magnetic field, as it is shown in Figure 3.1. When one winding is connected to the AC electric power, the electric current is generated. This winding is called the primary winding. The primary current produces the magnetic field and the magnetic flux links the second winding, called the secondary winding. The AC flux through the secondary winding produces an AC voltage, so that if some impedance is connected to the terminals, an AC electric current is supplied.

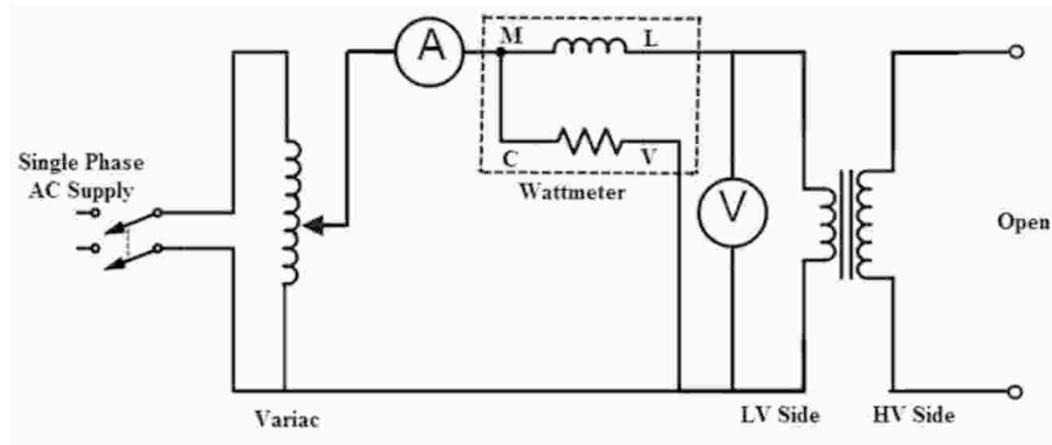


**Fig. 2(a).3 Ideal Transformer**

#### **Transformer on No Load Condition**

When the transformer is operating at no load, the secondary winding is open circuited, which means there is no load on the secondary side of the transformer and, therefore, current in the secondary will be zero, while primary winding carries a small current  $I_0$  called no load current which is 2 to 10% of the rated current. This

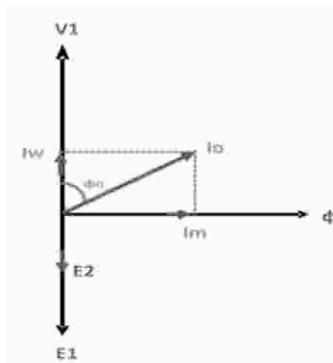
current is responsible for supplying the iron losses (hysteresis and eddy current losses) in the core and a very small amount of copper losses in the primary winding. The angle of lag depends upon the losses in the transformer. The power factor is very low and varies from 0.1 to 0.15.



**Fig 2(a).4 Single phase transformer on no load condition**

The no load current consists of two components

- Reactive or magnetizing component  $I_m$  (It is in quadrature with the applied voltage  $V_1$ . It produces flux in the core and does not consume any power)
  - Active or power component  $I_w$ , also known as working component (It is in phase with the applied voltage  $V_1$ . It supplies the iron losses and a small amount of primary copper loss) The following steps are given below to draw the phasor diagram
1. The function of the magnetizing component is to produce the magnetizing flux, and thus, it will be in phase with the flux.
  2. Induced emf in the primary and the secondary winding lags the flux  $\phi$  by 90 degrees.
  3. The primary copper loss is neglected, and secondary current losses are zero as  $I_2 = 0$ . Therefore, the current  $I_0$  lags behind the voltage vector  $V_1$  by an angle  $\phi_0$  called no-load power factor angle shown in the phasor diagram above.
  4. The applied voltage  $V_1$  is drawn equal and opposite to the induced emf  $E_1$  because the difference between the two, at no load, is negligible.
  5. Active component  $I_w$  is drawn in phase with the applied voltage  $V_1$ .
  6. The phasor sum of magnetizing current  $I_m$  and the working current  $I_w$  gives the no load current  $I_0$ .



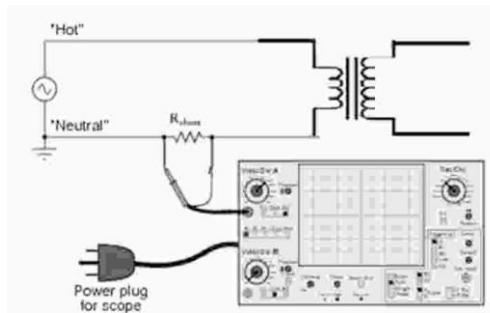
**Fig 2(a).5 Phasor diagram of transformer no load condition**

#### Observation of no load current in transformer

**Method:-** Oscilloscope typically doesn't measure current but instead it measures voltage. Thus to observe current waveform you could simply use current-to-voltage component like resistor, and probe the voltage across that resistor. The current information can be obtained by using Ohm Law formula:  $V = I \cdot R$  or  $I = V/R$  which means current waveform will match exactly the observed voltage, despite the magnitude may be different.

## PROCEDURE:

1. Connect circuit as shown in the circuit diagram. Open circuit the secondary and apply full load voltage to the primary through a variac. The copper loss is negligible since there is no load current flowing. Hence power consumed is the core losses of the core.
2. Oscilloscope typically doesn't measure current but instead it measures voltage. Thus to observe current waveform you could simply use current-to-voltage component like resistor, and probe the voltage across that resistor.
3. The current information can be obtained by using Ohm Law formula:  $V = I \cdot R$  or  $I = V/R$  which means current waveform will match exactly the observed voltage, despite the magnitude may be different.
4. Observe the no-load current waveform on the CRO screen.
5. Note ammeter reading and Current observed by CRO(A) reading in observation table.



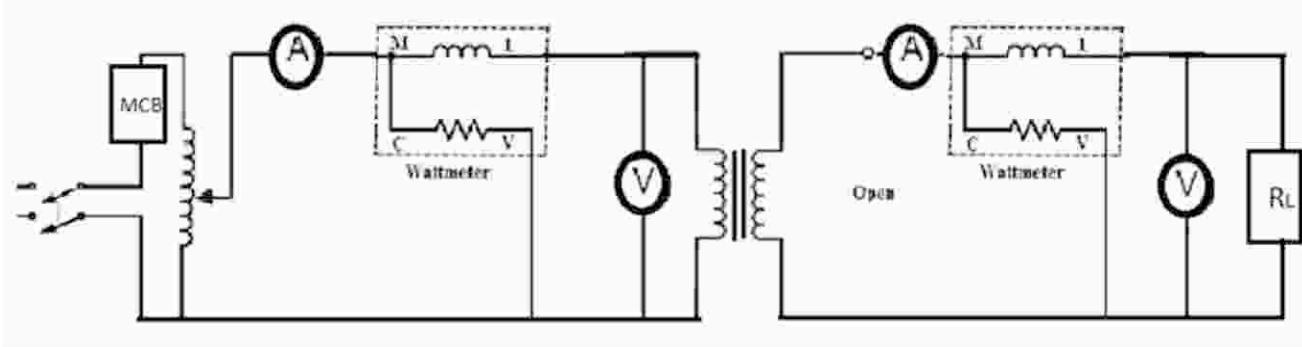
**Fig 2(a).6 Observation of no load current in transformer**

S.No	Current observed by Ammeter(A)	Current observed by CRO(A)
1.		
2.		
3.		
4.		

### Transformer "On-load"

When an electrical load is connected to the secondary winding of a transformer and the transformer loading is either greater than zero, a current flows in the secondary winding and out to the load. This secondary current is due to the induced secondary voltage, set up by the magnetic flux created in the core from the primary current.

The secondary current,  $I_S$  which is determined by the characteristics of the load, creates a self-induced secondary magnetic field,  $\Phi_S$  in the transformer core which flows in the exact opposite direction to the main primary field,  $\Phi_P$ . These two magnetic fields oppose each other resulting in a combined magnetic field of less magnetic strength than the single field produced by the primary winding alone when the secondary circuit was open circuited.



**Fig2(b).1 Transformer On-load**

We know that the turns ratio of a transformer states that the total induced voltage in each winding is proportional to the number of turns in that winding and also that the power output and power input of a transformer is equal to the voltage times amperes, ( $V \times I$ ). Therefore:

$$(\text{Power})_{\text{primary}} = (\text{Power})_{\text{secondary}}$$

$$V_p * I_p = V_s * I_s$$

$$\frac{V_p}{V_s} = \frac{I_s}{I_p}$$

But we also know previously that the voltage ratio of a transformer is equal to the turns ratio of a transformer as: "voltage ratio = turns ratio". Then the relationship between the voltage, current and number of turns in a transformer can be linked together and is therefore given as

Transformer Ratio (n)

$$\frac{N_s}{N_p} = \frac{V_p}{V_s} = \frac{I_s}{I_p}$$

Where:

- $N_p/N_s = V_p/V_s$  represents the voltage ratio
- $N_p/N_s = I_s/I_p$  represents the current ratio

Note that the current is inversely proportional to both the voltage and the number of turns. This means that with a transformer loading on the secondary winding, in order to maintain a balanced power level across the transformer's windings, if the voltage is stepped up, the current must be stepped down and vice versa. In other words, "high voltage—low current" or "low voltage—high current".

As a transformer's ratio is the relationship between the number of turns in the primary and secondary, the voltage across each winding, and the current through the windings, we can rearrange the above transformer ratio equation to find the value of any unknown voltage, ( $V$ ) current, ( $I$ ) or number of turns, ( $N$ ) as shown.

The total current drawn from the supply by the primary winding is the vector sum of the no-load current,  $I_0$  and

the additional supply current,  $I_1$  is a result of the secondary transformer loading and which lags behind the supply voltage by an angle of  $\Phi$ .

#### **Procedure:**

1. Connect circuit as shown in the circuit diagram.
2. Apply Load on the transformer step by step.
3. Note down the readings of Primary, secondary side voltmeter, ammeter, wattmeter.
4. Repeat step no 3 for different loads on the transformer.
5. Switch off the supply.

#### **Observation Table:-**

S. No.	Load	Primary Side			Secondary Side			<b>Efficiency(%) (<math>W_s/W_p \times 100</math>)</b>
		$V_p$	$I_p$	$W_p$	$V_s$	$I_s$	$W_s$	
1								
2								
3								

#### **Result:-**

Observation of the no-load current waveform has been done on an oscilloscope.

Measurement of primary and secondary voltages and currents, and power is done after loading of a transformer with different resistive load condition.

Efficiency of transformer is calculate for direct loading condition.

#### **VIVA VOICE:**

1. What happens when Transformer is given DC supply?
2. For a transformer with primary turns 100, secondary turns 400, if 200V is applied at primary what voltage we will get in secondary?
3. What is difference between transformer and amplifier?
4. How can eddy current loss be minimized?
5. What is autotransformer?
6. Does the transformer draw any current when its secondary is open?
7. What is current transformer?
8. What type of transformer distribution transformer is? step-up or step-down.
9. What is transformer regulation?

## **Experiment No. 05**

### **Open circuit and short circuit test on a single-phase transformer**

#### **AIM OF THE EXPERIMENT:**

To perform the open circuit and short circuit test on a single-phase transformer and to draw the equivalent circuit after determining its constants.

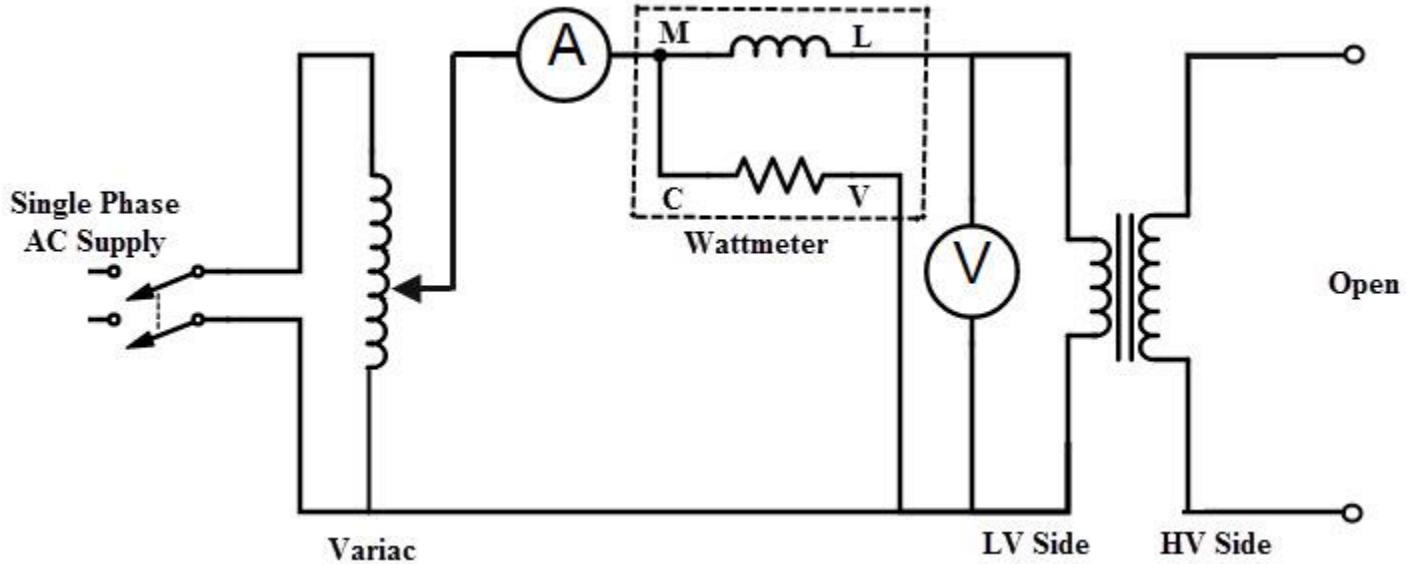
**NAMEPLATEDETAILS:**

VoltageRatio	220/110V
Full loadCurrent	13.6A
KVARATING	3KVA

**EQUIPMENTREQUIRED:**

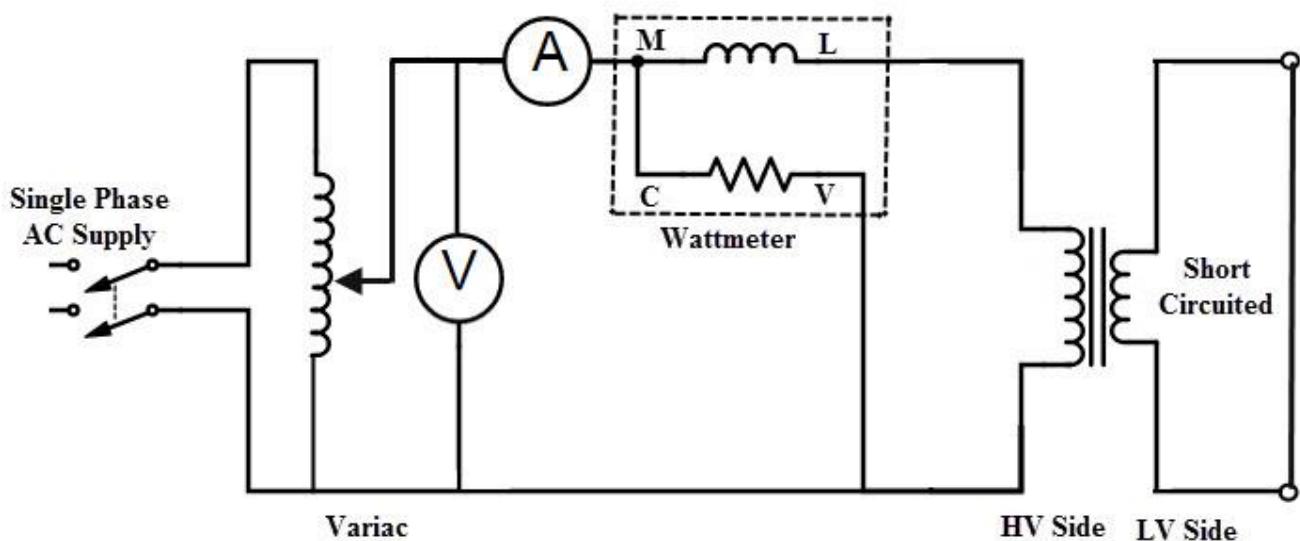
S.NO	NAMEOFTHEEQUIPMENT	RANGE	TYPE	QUANTITY
1	Singlephase transformer	3KVA,230/230V,50HZ		1
2	Wattmeter	2.5/5ALPF	LPF	1
3	Wattmeter	10/20A UPF	UPF	1
4	Ammeter	0-1A		1
5	Ammeter	0-20A		1
6	Voltmeter	0-300V		1
7	Voltmeter	0-30/75V		1
8	1ph Variac	0-300V,15A		1

**OPENCIRCUITTEST:**



Circuit Diagram: OC Test

#### SHORTCIRCUITTEST:



Circuit Diagram: SC Test

#### THEORY:

The performance of a transformer can be calculated on the basis of its equivalent circuit which contains four main parameters, the equivalent resistance  $R_{01}$  as referred to primary (or secondary  $R_{02}$ ), the equivalent leakage reactance  $X_{01}$  as referred to primary, the core-loss conductance  $G_0$  and the magnetizing

susceptance  $B_0$ . These constants or parameters can be easily determined by two tests i.e. Open circuit test and short circuit test. These are very economical and convenient, because they furnish the required information without actually loading the transformer. In fact, the testing of very large a.c machinery consists of running two tests similar to the open and short circuit test of a transformer.

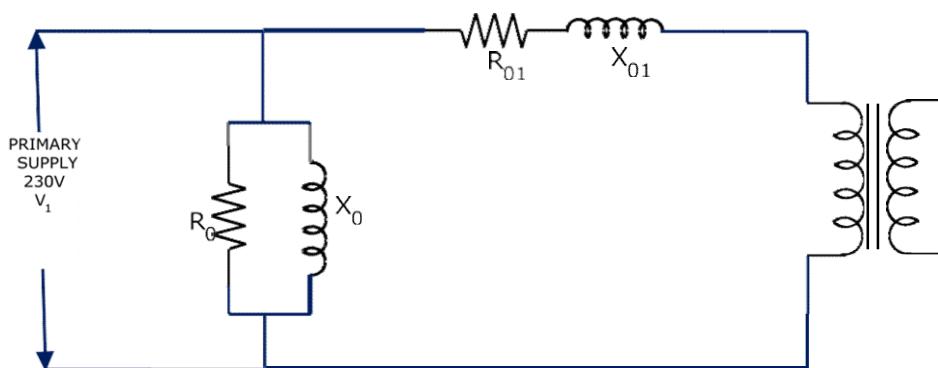
The purpose of this test is to determine no load loss or core loss and no load  $I_0$  which is helpful in finding  $X_0$  and  $R_0$ . One winding of the transformer whichever is convenient but usually high voltage winding is left open and the other is connected to its supply of normal voltage and frequency. A wattmeter (W), Voltmeter (V) and ammeter (A) are connected in the low voltage winding i.e. primary winding in the present case. With normal voltage applied to the primary, normal flux will be set up in the core, hence normal iron losses will occur which are recorded by the wattmeter. As the primary no load current  $I_0$  is small, Cu loss is negligibly small in primary and nil in secondary. Hence, the wattmeter reading represents practically the core loss under no load condition.

For short circuit test, one winding usually the low voltage winding, is solidly short-circuited by a thick conductor (or through an ammeter which may serve the additional purpose of indicating rated load current).

A low voltage (usually 5 to 10% of normal primary voltage) at correct frequency (though for Cu losses it is not essential) is applied to the primary and is cautiously increased till full load current is flowing both in primary and secondary (as indicated by the respective ammeters).

Since, in this test, the applied voltage is a small percentage of the normal voltage, the mutual flux  $\phi$  produced is also a small percentage of its normal value. Hence, core losses are very small with the result that the wattmeter reading represents the full load Cu loss or  $i^2 R$  loss for the whole transformer i.e. both primary Cu loss and secondary Cu loss. The equivalent circuit of the transformer under short-circuit condition. If  $V_{sc}$  is the voltage required to circulate rated load currents, then  $Z_{01} = V_{sc}/I_1$

A two winding transformer can be represented by means of an equivalent circuit as shown below



#### PRECAUTIONS:

1. Don't switch on power supply without consulting respected teachers.
2. 1Ø Auto transformer must be kept at minimum potential point.  
Before switching on the experiment.

## OPEN CIRCUIT TESTPROCEDURE:

1. Connect circuit as shown in the circuit diagram. open circuit the secondaryand apply full load voltage to the primary through a variac. The copper lossis negligible since there is only no load current is flowing. Hence powerconsumedis the corelosses of thecore.

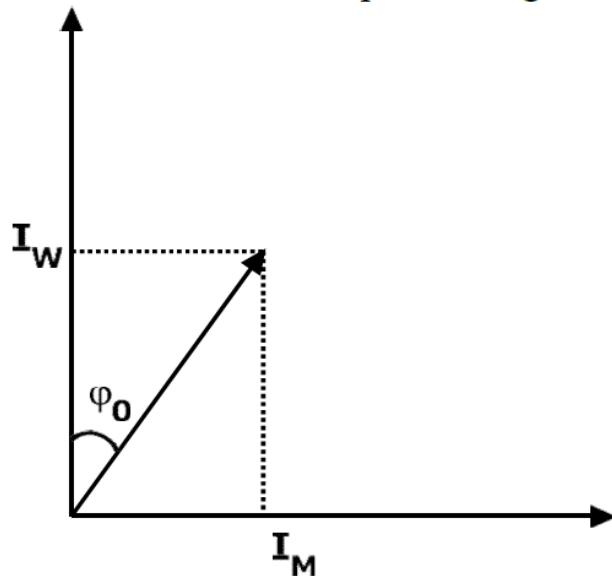
2. Note voltmeterand ammeter and wattmeterreading.

## OBSERVATION TABLE:

S.No.	V	$I_0$	W	$I_w = W/V$	$I_M = \sqrt{I_0^2 - I_w^2}$	$\cos\phi = W/(VI_0)$
1	125	0.37	24			

## CALCULATION:

See the no load phasor diagram below



$$W = VI_0 \cos \phi_0$$

$$I_w = \frac{W}{V} \quad , \quad I_M = \sqrt{I_0^2 - I_w^2} \quad R_0 = \frac{V}{I_w} \quad , \quad X_0 = \frac{V}{I_M}$$

## SHORTCIRCUIT TEST PROCEDURE:

1. Connect as shown in the circuit diagram. Short circuit the secondary and apply a lowvoltage to the primary through aauto transformer. The iron losses are negligiblesince thefluxwillbeverylow on account of the primaryandsecondary.
2. Increase the voltage gradually till full load current flows in the

primary. Note voltmeter and ammeter and wattmeter reading.

### OBSERVATION:

SL.NO.	V	I	W <sub>c</sub>
1	15	40.9	52

### CALCULATIONS:

Let the total equivalent resistance of primary and secondary referred to primary side be  $R_1$  ohms and the total equivalent leakage reactance referred to primary side be  $X_1$  ohms.

$$W_c = I^2 R_1$$

$$\text{Hence } R_1 = W_c / I^2 \text{ Also } V / I = Z_1 \text{ and } X_1 = \sqrt{Z_1^2 - R_1^2} \text{ ohms.}$$

### CONCLUSION:

- Now draw the equivalent circuit.
- Plot a graph of copper loss versus load current (short circuit current). What is the shape of the curve?
- Determine the regulation of the transformer at various loads for an assumed load power factor of 0.8 lagging.

$$\text{Regulation percentage} = \frac{(V_o - V_t)}{V_t} \times 100$$

Where  $V_o$  = secondary no load voltage.

$V_t$  = secondary full load voltage.

- Plot a curve of regulation versus load current.

### RESULT:

### POST LAB QUESTIONS:

- Why iron is chosen as the material for the core of the transformer? Why not we use aluminium?
- What is normally the efficiency of a transformer to be?
- Why Transformers Are Rated In KVA?

# Experiment No. 06

## Characteristics of Lamps

### Aim:

- I) Obtaining the V-I characteristics of the following nonlinear elements  
 Lamp(L1): 40W, 220VAC Tungsten Lamp  
 Lamp(L2): 18W, 220VAC Compact Fluorescent Lamps (CFL).

### APPARATUS:

Sl.NO.	Name of the equipment	Range	Type	Qty
1	Autotransformer	0-230V		1
2	Ammeter	0-0.2A	MI	2
3	Voltmeter	0-300V	MI	1
4	Incandescent lamp, CFL lamp	40w, 18w		1

### Circuit Diagram:

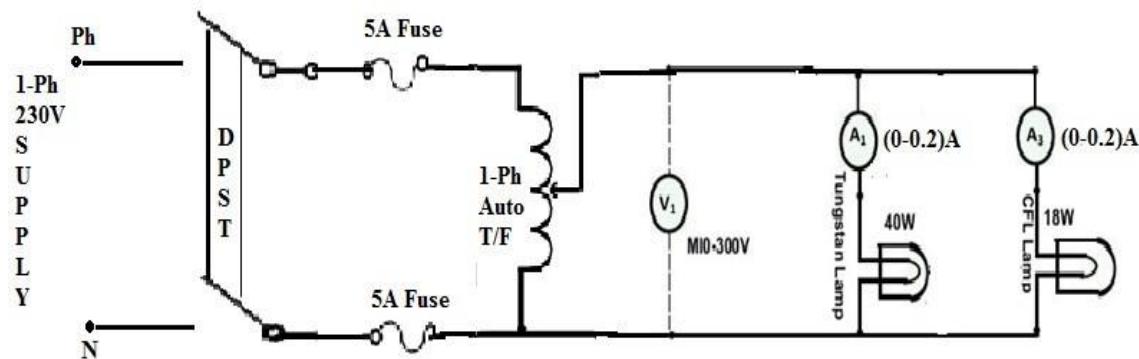


Fig. 1 V-I characteristics of the Tungsten and CFL Lamp

### Procedure for V-I characteristics of the Tungsten and CFL Lamp

- Choose the appropriate ratings of the Ammeters, Voltmeters and Fuse wire.
- Setup the circuit as shown in Fig 1 with the lamps and instruments as indicated. Keep the switch S open.
- Set the autotransformer for zero output voltage. Close the switch S.
- Increase the autotransformer output voltage in steps of 20/30V, until the full voltage (i.e. 230V) is obtained. At each step, note the readings of  $V_1$ ,  $A_1$ ,  $A_2$  and record them in Table 1.
- Repeat step (d) decreasing output voltage of autotransformer from full to zero volts.

### Observation Table:

S.No	V <sub>1</sub> (V)	I <sub>1</sub> (A)(Tungsten Lamp)			I <sub>2</sub> (A)(CFL Lamp)		
		Inc	Dec	Mean	Inc	Dec	Mean
1.	40	0.08	0.076	0.078	0.08	0.076	0.078
2	80	0.086	0.102	0.094	0.09	0.086	0.088
3	120	0.11	0.124	0.117	0.086	0.084	0.085
4	160	0.132	0.144	0.138	0.082	0.08	0.081

### Modelgraph

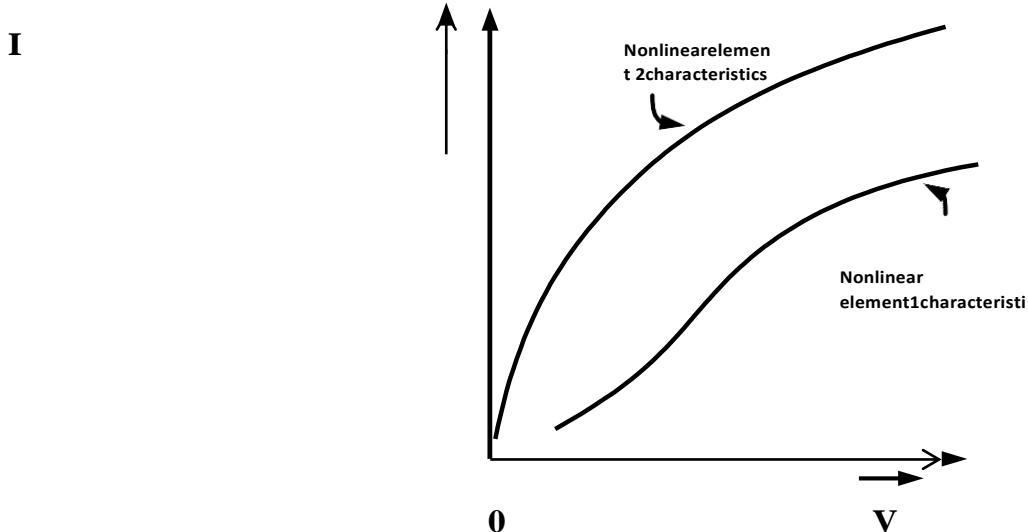


Fig.2 Modelgraph for V-I characteristics

### RESULT:

V-I Characteristics of different lamps have been studied.

### Discussion Questions:

- How will you interpret the V-I characteristics of two different incandescent lamps?
- Why do the readings differ for increasing and decreasing values of the lamp voltages? Discussion Questions:
- Account for the differences, if any, between the predicted and the observed steady state operating points of the circuit.

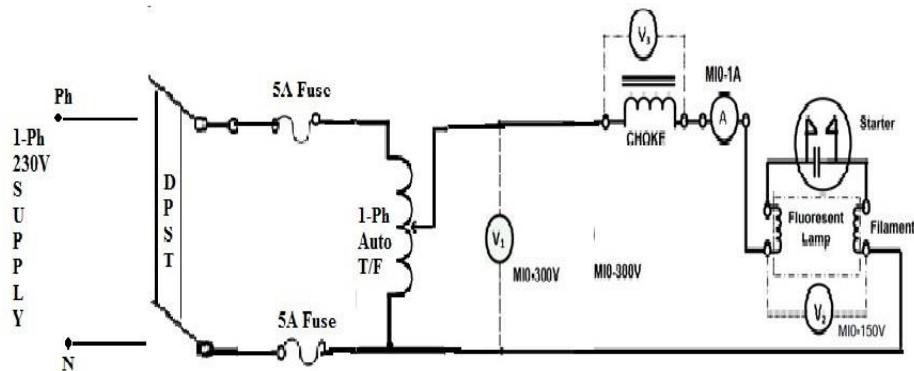
Why source characteristics will be referred as load line characteristics?

## II. FLUORESCENT LAMP:

**Aim:** To obtain the V-I characteristics of a Fluorescent Lamp

## APPARATUS:

Sl.NO.	Name of the equipment	Range	Type	Qty
1	Autotransformer	0-230V		1
2	Ammeter	0-0.2A	MI	2
3	Voltmeter	0-300v, 0-150v	MI	2,1
4	Fluorescent lamp	36w		1



.0

Fig. 7 Circuit diagram for testing of a Fluorescent Lamp

### Procedure:

1. Setup the circuit as shown in Fig. 7. Keep the switch S open.
2. With the autotransformer at zero output position, close the switch S.
3. Increase the autotransformer output gradually until the lamp lights up. Note the meter readings and enter them in the proper column in Table 3.  
When the lamp starts to glow, increase the autotransformer output voltage in steps until the rated voltage is obtained. Enter the readings of the meters in Table 3.
4. Decrease the supply voltage in steps until the lamp extinguishes. Record the meter reading S.

Table 3: Fluorescent lamp characteristics.

S.NO.	$V_s$	$V_L$	$V_c$	$I_L$	Remarks
1	230	106	184	3.6	Fullbright
2	210	114	150	2.4	Lowbright
3	190	128	124	2.2	Dim
4	150	149	20	0	Lightextinguish

$V_s$  = Voltage across the supply = reading of the voltmeter  $V_1$

$V_1$  = Voltage across the lamp = reading of the voltmeter  $V_2$

$V_c$  = Voltage across the choke = reading of the voltmeter  $V_3$

$I_L$  = Current through the lamp = reading of the ammeter A

**RESULT:** The characteristics of fluorescent lamps are studied.

#### Discussion Questions:

1. Plot  $V_L$  versus  $I_L$  and  $V_c$  versus  $I_L$  on the same graph sheet.
2. Comment on the nature of the plots. How are  $V_L$  and  $V_c$  related?
3. Discuss the function of the choke in the lamp circuit. Can it be replaced by a resistor?
4. What is the necessity of a starter? Can a single-pole switch replace it?
5. The voltage needed for starting the glow of the lamp and the voltage when the lamp extinguishes, are not equal. Explain why?
6. If ac supply is replaced by dc, will the circuit work? If not, what changes are to be made?

## Experiment No. 07

### VERIFICATION OF KVL AND KCL

## AIM:

To verify Kirchhoff's Voltage Law (KVL) and Kirchhoff's Current Law (KCL) in a Passive Resistive Network.

## STATEMENT:

**KCL:** Algebraic Sum of All the Currents Entering and Leaving a node

Must be equal to zero

$$I_{\text{exiting}} + I_{\text{entering}} = 0$$

**KVL:** Algebraic sum of all voltages within the loop must be equal to zero

$$V_s = V_1 + V_2 + V_3$$

## APPARATUS:

S. No	Apparatus Name	Range	Type	Quantity
1	RPS			
2	Ammeter			
3	Voltmeter			
4	Resistors			
5	Bread Board	-	-	01
6	Connecting Wires	-	-	As required

## CIRCUIT DIAGRAMS:

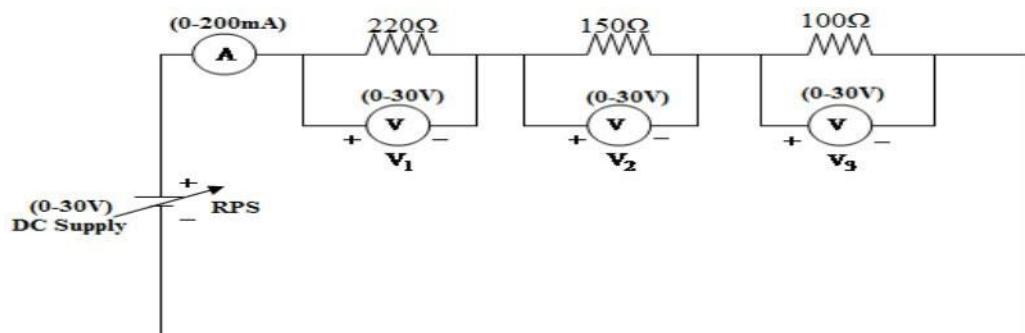
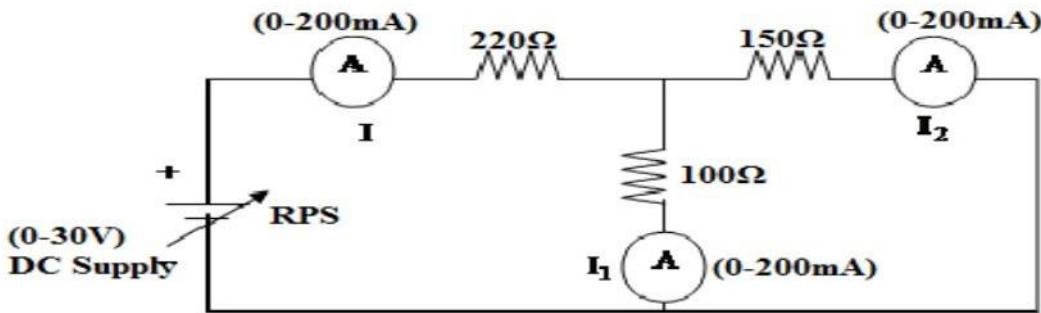


Figure – 1.1 Verification of KVL



**Figure – 1.2 Verification of KCL**

**PROCEDURE:**

**To Verify KVL**

1. Connect the circuit diagram as shown in Figure1.
2. Switch ON the supply toRPS.
3. Apply the voltage (say 5v) and note the voltmeterreadings.
4. Gradually increase the supply voltage insteps.
5. Note the readings ofvoltmeters.
6. sum up the voltmeter readings (voltage drops) , that should be equal to applied voltage.
7. Thus KVL is Verifiedpractically.

**To Verify KCL**

1. Connect the circuit diagram as shown in Figure2.
2. Switch ON the supply toRPS.
3. Apply the voltage (say 5v) and note the Ammeterreadings.
4. Gradually increase the supply voltage insteps.
5. Note the readings ofAmmeters.
6. Sum up the Ammeter readings ( $I_1$  and  $I_2$ ) , that should be equal to total current( $I$ ).
7. Thus KCL is Verifiedpractically

**OBSERVATIONS:**

**ForKVL**

Applied Voltage $V$ (volts)	V <sub>1</sub> (volts)		V <sub>2</sub> (volts)		V <sub>3</sub> (volts)		V <sub>1</sub> +V <sub>2</sub> +V <sub>3</sub> (volts)	
	Theoretical	Practical	Theoretical	Practical	Theoretical	Practical	Theoretical	Practical

## ForKCL

Applied Voltage V (volts)	I (A)		I <sub>1</sub> (A)		I <sub>2</sub> (A)		I <sub>1+I<sub>2</sub></sub> (A)	
	Theoretical	Practical	Theoretical	Practical	Theoretical	Practical	Theoretical	Practical

### PRECAUTIONS:

1. Check for proper connections before switching ON the supply
2. Make sure of proper color coding of resistors
3. The terminal of the resistance should be properly connected.

**RESULT:** Kirchhoff's Voltage Law (KVL) and Kirchhoff's Current Law (KCL) in a Passive Resistive Network Verified.

## Verification of Network Theorems

The objective of this experiment is to verify the

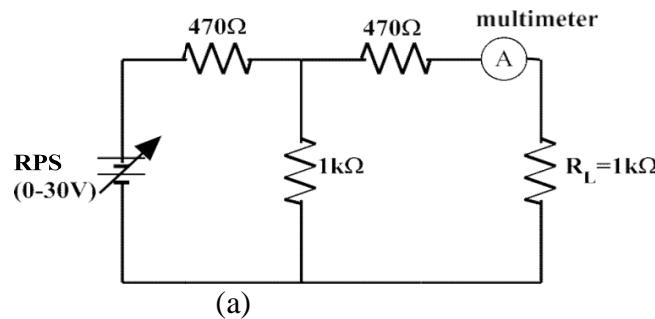
- Thevenin's Theorem
- Superposition Theorem

### **APPARATUS:**

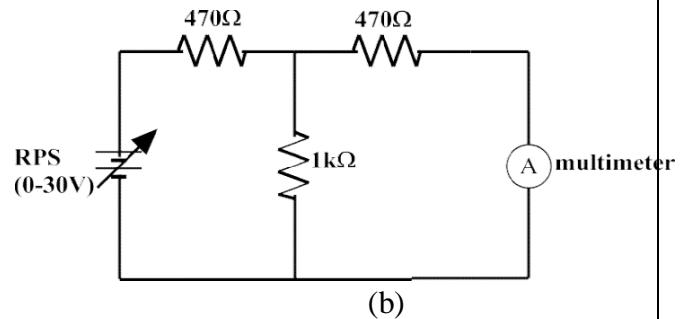
Sl.NO.	Name of the equipment	Range	Type	Qty
1	Resistors	470Ω 470Ω 1kΩ 1kΩ		1 1 1 1
2	Breadboard	-	-	1
3	Regulated power supply	0-30V	-	1
4	Multimeter	-	Digital	1
5	Single Stand Wires			As Required

### a) Thevenin's Theorem

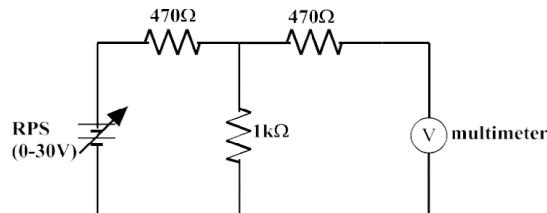
#### **Circuit Diagrams:**



(a)



(b)



(c)

#### **Procedure:**

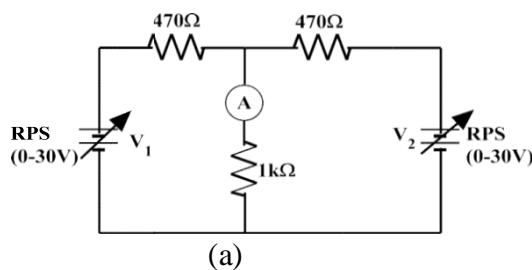
Fig 1: Circuit Diagram for verification of Thevenin's theorem

1. Connect the circuit as shown in fig 1(a)
2. Switch on the RPS and apply some input voltage (say 30V), observe the load current  $I_L$ .
3. Now reconnect the circuit as shown in Fig 1(b) and apply the same input voltage as in step 2 and observe the short circuit current ( $I_{SC}$ ).
4. Now reconnect the circuit as shown in fig 1(c) and apply the same input voltage as in step 2 and observe the voltage ( $V_{Th}$ ). open circuit voltage which is nothing but the thevenin's voltage.
5. Now compute the thevenin's equivalent resistance ( $R_{Th} = V_{Th}/I_{SC}$ ).
6. Compute the load current applying thevenin's theorem as  $I_L = V_{Th}/(R_{Th} + R_L)$ .
7. Compare the above load current with its observed value in step (2) and verify the theorem.
8. Adjust the input voltage to a new value and repeat the procedure from step (2) to step (7) (Take at least five sets of readings).

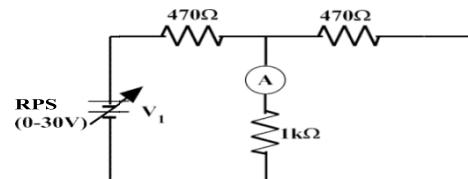
**Table-I Thevenin's Theorem:**

Sl.No	Source voltage $V_s$	Observed load current ( $I_L$ ) m a	$I_{SC}$ ma	$V_{Th}$	$R_{Th} = (V_{Th}/I_{SC})$	Computed load current $(V_{Th}/(R_{Th} + R_L)) = I_{Lm}$ a
1	10	3.8	8.3	6.75	0.813	3.72
2.	20	7.4	16.9	13.4	0.792	7.4

**b) Superposition Theorem: Circuit Diagram:**



(a)



(b)

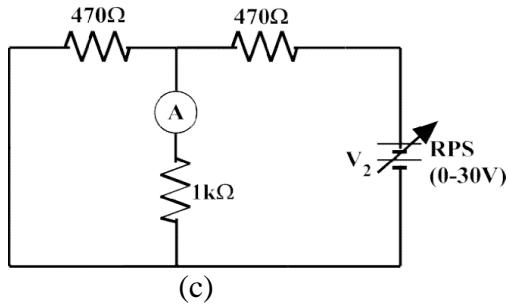


Fig2:CircuitDiagramforSuperpositionTheorem

**Procedure:**

1. Connect the circuit as shown in the Fig2(a), apply some input voltage  $V_1$  and  $V_2$  and observe the current ( $I$ ) through the  $1\text{k}\Omega$  resistor.
2. Connect the circuit as shown in fig2(b), and apply the same voltage  $V_1$  as in step 1 and observe the current ( $I_1$ ) through the  $1\text{k}\Omega$  resistor.
3. Connect the circuit as shown in fig2(c), and apply the same voltage  $V_2$  as in step 1 and observe the current ( $I_2$ ) through the  $1\text{k}\Omega$  resistor.
4. Compare  $I$  with  $(I_1 + I_2)$  taking care of signs properly to verify the theorem.
5. Repeat the procedure from step 1 to step 4 for five different combinations of voltages  $V_1$  and  $V_2$ .

**Table-II Superposition Theorem: For  $V_s = 10, 20\text{V}$**

Sl.No.	$I_T\text{ma}$	$I_1\text{ma}$	$I_2\text{ma}$	Computed current ( $I_T = I_1 + I_2$ ) ma	Error	%Error
1.	8.3	4.1	4.2	8.3	0	0
2	16.18	8.16	7.99	16.15	0.03	3

**RESULT:** Hence Thevenin's and superposition theorems are verified.

**Discussion:**

- 1) Can you suggest any alternative procedure for the determination of thevenin's resistance  $R_{Th}$ ?
- 2) Is there any restriction for the choice of circuit elements?
  - i) While considering the effect of a single source, the other source is short circuited why? How far is it justified?

## **EXPERIMENTNO. 09(a)**

### **V-I CHARACTERISTICS P-NJUNCTIONDIODE**

**AIM:** 1. To observe and draw the Forward and Reverse bias V-I characteristics of a P-N Junction diode.

2. To calculate static and dynamic resistance in both forward and Reverse Bias Condition.

#### **EQUIPMENT REQUIRED:**

SNO	NAME OF THE EQUIPMENT	QUANTITY
1	P-NDiodeIN4007	1 No.
2	Regulated Powersupply(0-30V)	1 No.
3	Resistor 1KΩ	1 No.
4	Ammeter(0-20mA)	1 No
5	Ammeter(0-200μA)	1 No.
6	Voltmeter(0-20V)	2 No.
7	Breadboard	1 No.
8	Connecting wires	Required No.

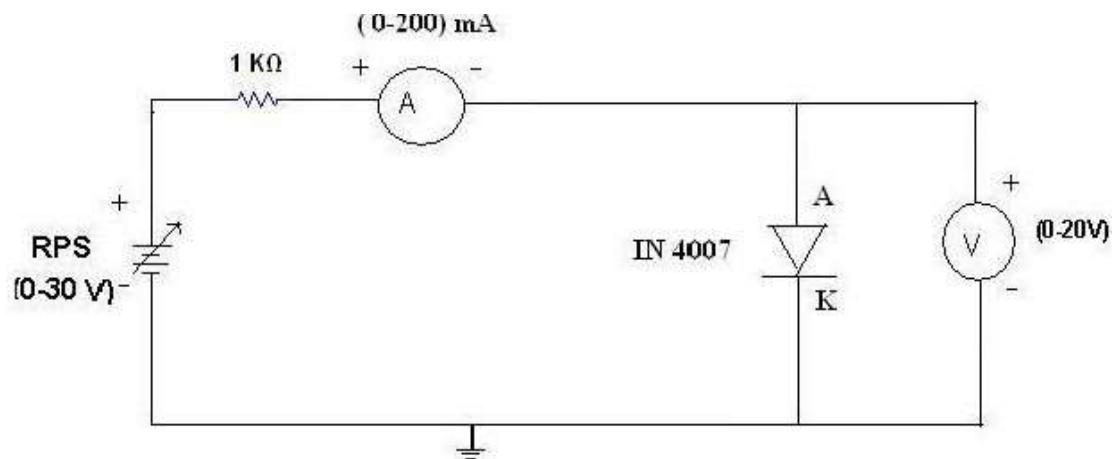
#### **THEORY:**

A P-N junction diode conducts only in one direction. The V-I characteristics of the diode are curve between voltage across the diode and current flowing through the diode. When external voltage is zero, circuit is open and the potential barrier does not allow the current to flow. Therefore, the circuit current is zero. When P-type (Anode) is connected to +ve terminal and N-type (cathode) is connected to -ve terminal of the supply voltage is known as forward bias. The potential barrier is reduced when diode is in the forward biased condition. At some forward voltage, the potential barrier altogether eliminated and current starts flowing through the diode and also in the circuit. Then diode is said to be in ON state. The current increases with increasing forward voltage.

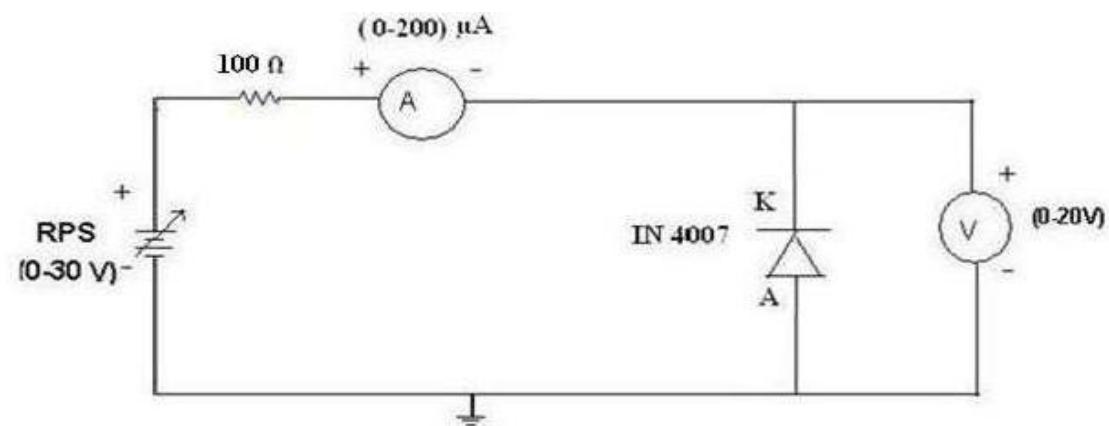
When N-type (cathode) is connected to +ve terminal and P-type (Anode) is connected to -ve terminal of the supply voltage is known as reverse bias and the potential barrier across the junction increases. Therefore, the junction resistance becomes very high and a very small current (reverse saturation current) flows in the circuit. Then diode is said to be in OFF state. The reverse bias current is due to minority charge carriers.

## CIRCUITDIAGRAM:

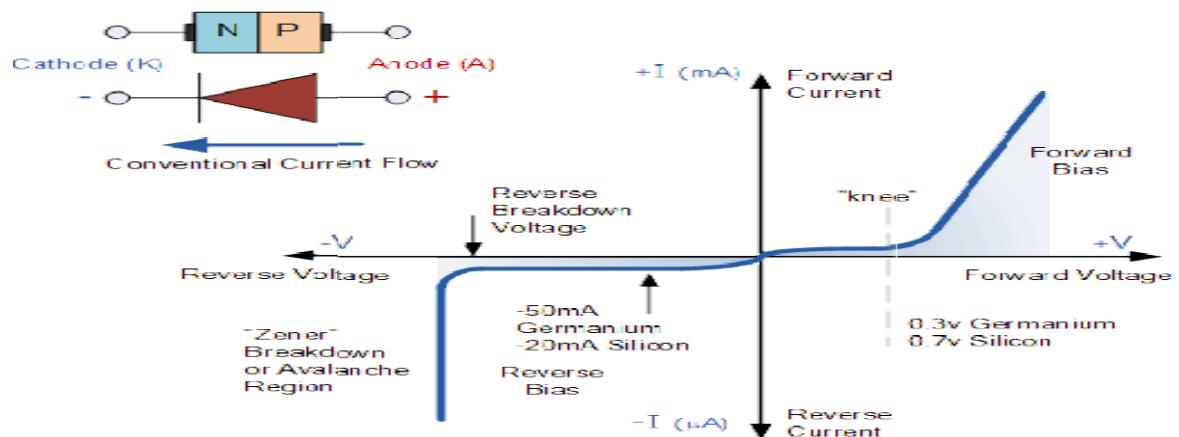
### A) Forwardbias:



### B) ReverseBias:



## MODELGRAPH



## OBSERVATIONS:

### A) FORWARDBIAS:

S.NO	Applied Voltage(V)	ForwardVoltage (V <sub>f</sub> )	ForwardCurrent(I <sub>f</sub> (mA))
1	0V	0V	0mA
2	0.1V	300mV	0mA
3	0.3V	319.2mV	0.04mA
4	0.5V	450mV	0.08mA
5	0.7V	0.495V	0.22mA
6	1V	0.5V	0.47mA
7	2V	0.596V	1.58mA
8	3V	0.621V	2.56mA
9	4V	0.639V	3.63mA
10	5V	0.651V	4.57mA

### B) REVERSEBIAS:

S.NO	Applied Voltage(V)	ReverseVoltage (V <sub>R</sub> )	ReverseCurrent(I <sub>R</sub> (μA))
1	1V	-1.1V	0
2	2 V	-2V	0.1
3	3 V	-3V	0.2
4	4 V	-4.4V	0.3
5	5 V	-5.17V	0.4
6	6 V	-6.27V	0.7

## CALCULATIONS:

Calculation of Static and Dynamic Resistance for a given diode.

$$\text{Static Resistance} \quad , \quad R_s = V_f / I_f =$$

$$\text{Dynamic Resistance}, \quad R_D = \Delta V_f / \Delta I_f =$$

In Reverse bias condition:

Static Resistance,

$R_s = V_R / I_R = \text{Dynamic Resistance}, R$

$D = \Delta V_R / \Delta I_R =$

## **PROCEDURE:**

### **A) FORWARDBIAS:**

1. Connections are made as per the circuit diagram.
2. For forward bias, the  $R_{PS+}$  is connected to the anode of the diode and  $R_{PS-}$  is connected to the cathode of the diode.
3. Switch on the power supply and increases the input voltage (supply voltage) in steps of 0.1V
4. Note down the corresponding current flowing through the diode and voltage across the diode for each and every step of the input voltage.
5. The reading of voltage and current are tabulated.
6. Graph is plotted between voltage ( $V_f$ ) on X-axis and current ( $I_f$ ) on Y-axis.

### **B) REVERSEBIAS:**

1. Connections are made as per the circuit diagram
2. For reverse bias, the  $R_{PS+}$  is connected to the cathode of the diode and  $R_{PS-}$  is connected to the anode of the diode.
3. Switch on the power supply and increase the input voltage (supply voltage) in steps of 1V.
4. Note down the corresponding current flowing through the diode voltage across the diode for each and every step of the input voltage.
5. The readings of voltage and current are tabulated
6. Graph is plotted between voltage ( $V_R$ ) on X-axis and current ( $I_R$ ) on Y-axis.

## **PRECAUTIONS:**

1. All the connections should be correct.
2. Parallax error should be avoided while taking the readings from the Analog meters.

**RESULT:** V-I characteristics of PN junction diode have been observed.

## **POSTLABQUESTIONS:**

1. Define depletion region of a diode?
2. What is meant by transition & space charge capacitance of a diode?
3. Is the V-I relationship of a diode Linear or Exponential?
4. Define cut-in voltage of a diode and specify the values for Si and Ge diodes?
5. What are the applications of a p-n diode?
6. Draw the ideal characteristics of P-N junction diode?
7. What is the diode equation?
8. What is PIV?
9. What is the breakdown voltage?
10. What is the effect of temperature on PN junction diodes?
11. Specifications of diodes

## **Experiment No-09(b)**

### **INPUT AND OUTPUT CHARACTERISTICS OF TRANSISTOR**

#### **CBC CONFIGURATION**

**AIM:** 1. To observe and draw the input and output characteristics of a transistor connected in common base configuration.

#### **EQUIPMENT REQUIRED:**

SNO.	NAME OF THE EQUIPMENT	QUANTITY
1	Transistor, BC107	1 No.
2	Regulated power supply (0-30V)	1 No.
3	Voltmeter (0-20V)	-2 No.
4	Ammeters (0-10mA)	2 No.
5	Resistor, 1KΩ	2 No
6	Breadboard	1 No.
7	Connecting wires	Required No.

#### **THEORY:**

A transistor is a three terminal active device. The terminals are emitter, base, collector. In CB configuration, the base is common to both input (emitter) and output (collector). For normal operation, the E-B junction is forward biased and C-B junction is reverse biased. In CB configuration, IE is +ve, IC is -ve and IB is -ve. So,

$$V_{EB} = F_1(V_{CB}, IE) \text{ and } IC = F_2(V_{EB}, IB)$$

With an increasing the reverse collector voltage, the space-charge width at the output junction increases and the effective base width 'W' decreases. This phenomenon is known as "Early effect". Then, there will be less chance for recombination within the base region. With increase of charge gradient within the base region, the current of minority carriers injected across the emitter junction increases.

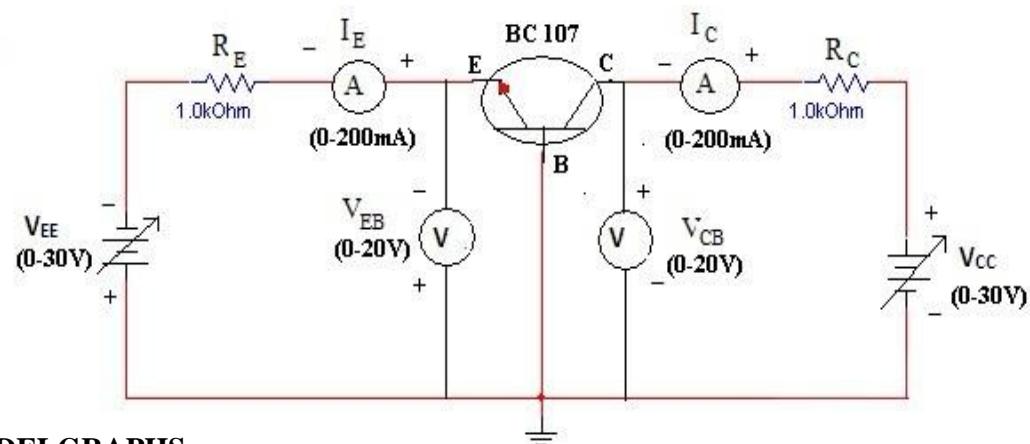
The current amplification factor of CB configuration is given by,

$$\alpha = \Delta IC / \Delta IE$$

Input Resistance,  $r_i = \Delta V_{BE} / \Delta IE$  at Constant  $V_{CB}$

Output Resistance,  $r_o = \Delta V_{CB} / \Delta IC$  at Constant  $IE$

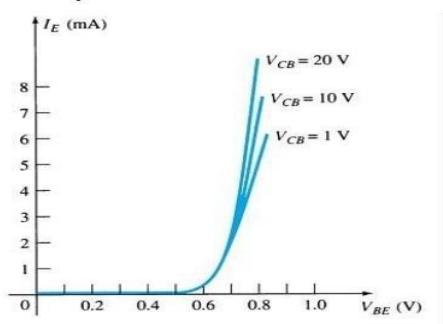
## CIRCUITDIAGRAM:



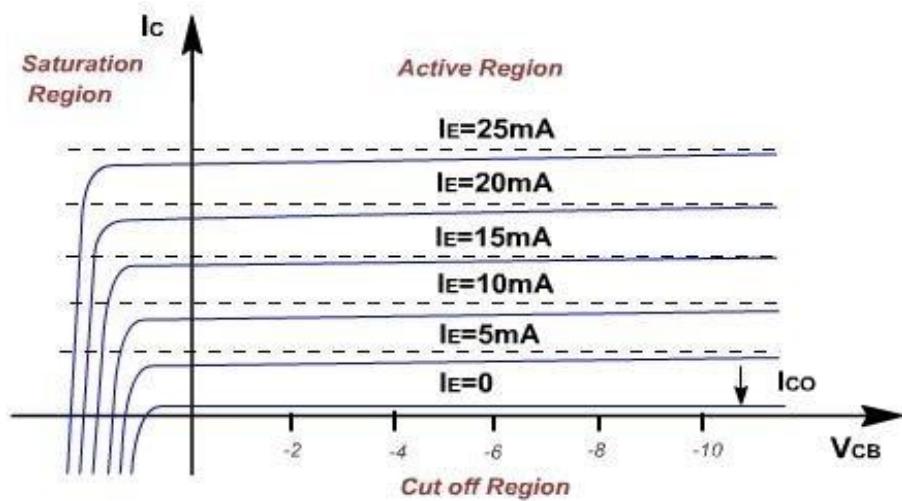
## MODELGRAPHS:

### A) INPUT CHARACTERISTICS

**Input Characteristics**



### B) OUTPUT CHARACTERISTICS



**OBSERVATIONS:****A) INPUT CHARACTERISTICS:**

V <sub>EE</sub> (V)	V <sub>CB</sub> =1V		V <sub>CB</sub> =2V		V <sub>CB</sub> =4V	
	V <sub>EB</sub> (V)	I <sub>E</sub> (mA)	V <sub>EB</sub> (V)	I <sub>E</sub> (mA)	V <sub>EB</sub> (V)	I <sub>E</sub> (mA)

**B) OUTPUT CHARACTERISTICS:**

V <sub>CC</sub> (V)	I <sub>E</sub> =10mA		I <sub>E</sub> =20mA		I <sub>E</sub> =30mA	
	V <sub>CB</sub> (V)	I <sub>C</sub> (mA)	V <sub>CB</sub> (V)	I <sub>C</sub> (mA)	V <sub>CB</sub> (V)	I <sub>C</sub> (mA)

**PROCEDURE:****A) INPUT CHARACTERISTICS:**

1. Connections are made as per the circuit diagram.
2. For plotting the input characteristics, the output voltage V<sub>CE</sub> is kept constant at 0V and for different values of V<sub>EE</sub> note down the values of I<sub>E</sub> and V<sub>BE</sub>.
3. Repeat the above step keeping V<sub>CB</sub> at 2V, 4V, and 6V and all the readings are tabulated.
4. A graph is drawn between V<sub>EB</sub> and I<sub>E</sub> for constant V<sub>CB</sub>.

**B) OUTPUT CHARACTERISTICS:**

1. Connections are made as per the circuit diagram.
2. For plotting the output characteristics, the input I<sub>E</sub> is kept constant at 0.5mA and for different values of V<sub>CC</sub>, note down the values of I<sub>C</sub> and V<sub>CB</sub>.
3. Repeat the above step for the values of I<sub>E</sub> at 1mA, 5mA and all the readings are tabulated.
4. A graph is drawn between V<sub>CB</sub> and I<sub>C</sub> for constant I<sub>E</sub>.

**PRECAUTIONS:**

1. The supply voltages should not exceed the rating of the transistor.

2. Meters should be connected properly according to their polarities.

**RESULT:** Input and output characteristics of a transistor connected in common base configuration.

#### **POSTLAB QUESTIONS:**

1. What is the range of  $\alpha$  for the transistor?
2. Draw the input and output characteristics of the transistor in CB configuration?
3. Identify various regions in output characteristics?
4. What is the relation between  $\alpha$  and  $\beta$ ?
5. What are the applications of CB configuration?
6. What are the input and output impedances of CB configuration?
7. Define  $\alpha$  (alpha)?
8. What is early effect?
9. Draw Circuit diagram of CB configuration for PNP transistor?
10. What is the power gain of CB configuration?

#### **EXPERIMENT NO. 10(a)**

#### **HALF-WAVE RECTIFIER WITH AND WITHOUT FILTER**

**AIM:** To examine the input and output waveforms of half wave Rectifier and also Calculate its load regulation and ripple factor.

1. With Filter
2. Without Filter

#### **EQUIPMENT REQUIRED:**

SNO.	NAME OF THE EQUIPMENT	QUANTITY
1	Digital Multimeter	1 No.
2	Transformer (6V-0-6V)	1 No.
3	Diode, 1N4007	1 No.
4	Capacitor 100µF/470µF	1 No.
5	Decade Resistance Box	1 No.
6	Breadboard	1 No.
7	CRO	1 No.
8	CRO Probes Connecting wires	Required No.

#### **THEORY:**

In Half Wave Rectification, When AC supply is applied at the input, only Positive Half Cycle appears across the load whereas, the negative Half Cycle is suppressed. How this can be explained as follows:

During positive half-cycle of the input voltage, the diode D1 is in forward bias and conducts through the load resistor  $R_L$ . Hence the current produces an output voltage across the load resistor  $R_L$ , which has the same shape as the +ve half cycle of the input voltage.

During the negative half-cycle of the input voltage, the diode is reverse biased and there is no current through the circuit. i.e., the voltage across  $R_L$  is zero. The net result is that only the +ve half cycle of the input voltage appears across the load. The average value of the half wave rectified o/p voltage is the value measured on dc voltmeter.

For practical circuits, transformer coupling is usually provided for two reasons.

The voltage can be stepped-up or stepped-down, as needed.

The ac source is electrically isolated from the rectifier. Thus, preventing shock hazards in the secondary circuit.

The efficiency of the Half Wave Rectifier is 40.6%

#### **Theoretical calculations for Ripple factor: Without Filter:**

$$V_{rms} = V_m/2 \quad V_m = 2V_{rms} \quad V_{dc} =$$

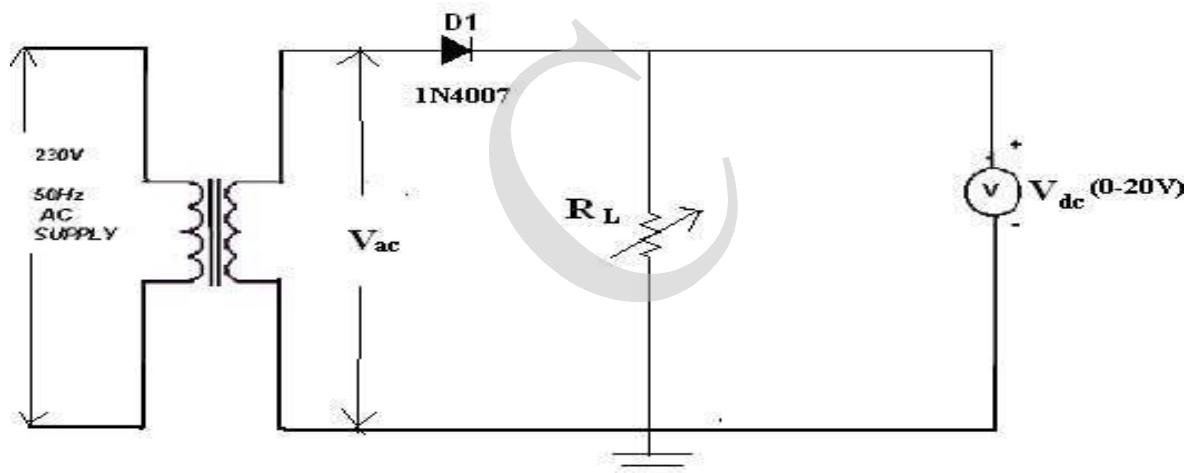
$$V_m/\pi \text{ Ripple factor} = \sqrt{(V_{rms}/V_{dc})^2 - 1} = 1.21$$

#### **With Filter:**

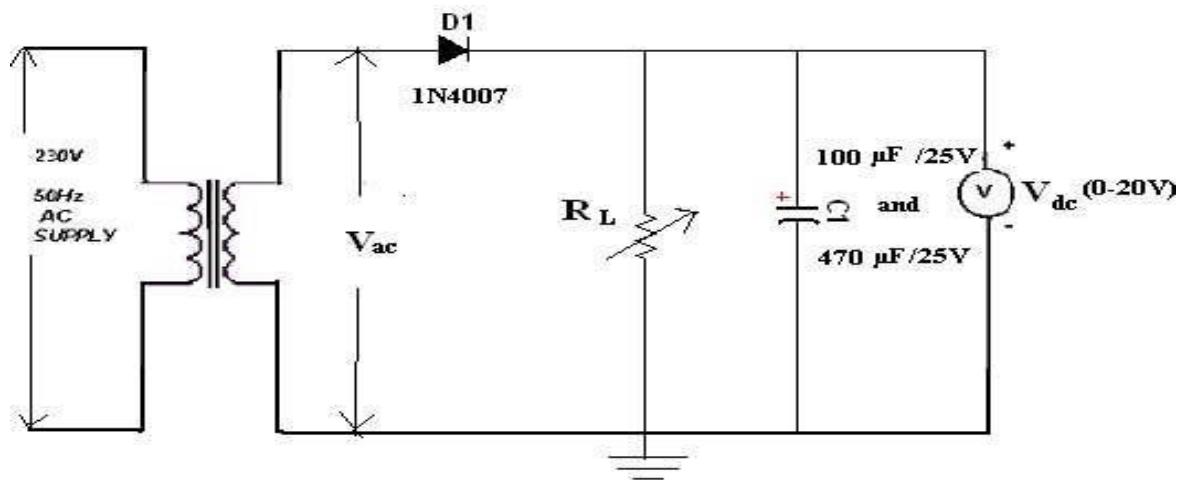
$$\text{Ripple factor, } r = 1/(2\sqrt{3} f C R)$$

#### **CIRCUIT DIAGRAM:**

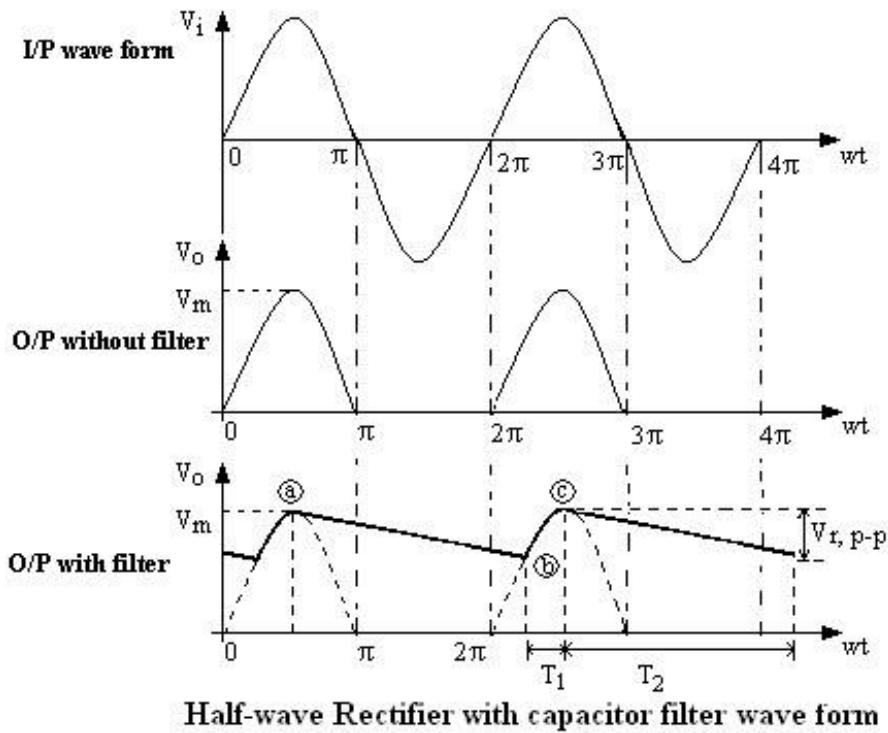
##### **A) HALF WAVE RECTIFIER WITHOUT FILTER**



B) HALFWAVE RECTIFIER WITH FILTER



## MODEL WAVEFORMS: A WAVEFORMS:



Half-wave Rectifier with capacitor filter wave form

## PROCEDURE:

1. Connections are made as per the circuit diagram.
2. Connect the primary side of the transformer to ac mains and the secondary side to the rectifier input.
3. By the multimeter, measure the ac input voltage of the rectifier and ac and dc voltage at the output of the rectifier.
4. Find the theoretical value of dc voltage by using the formula,  $V_{dc} = V_m/\Pi$ 
  - a. Where,  $V_m = 2V_{rms}$ , ( $V_{rms}$  = output ac voltage.)
5. The Ripple factor is calculated by using the formula
6.  $r = \text{ac output voltage}/\text{dc output voltage}$ .

## WITHOUT FILTER:

S.No.	Load Resistance( $R_L$ ) in Kilo-ohms	Output voltage			
		$V_{AC}$ (volts)	$V_{DC}$ (volts)		
1	$R_1$	4.24	3.75		
2	$R_2$	4.3	3.5		
3	$R_3$	4.4	3.58		

#### WITH CAPACITOR FILTER:

S.No.	Load Resistance( $R_L$ ) in Kilo-ohms	Output voltage			
		$V_{AC}$ (volts)	$V_{DC}$ (volts)		
1	$R_1$				
2	$R_2$				
3	$R_3$				

#### REGULATION CHARACTERISTICS:

1. Connections are made as per the circuit diagram.
2. By increasing the value of the rheostat, the voltage across the load and current flowing through the load are measured.
3. The reading is tabulated.
4. From the value of no-load voltages, the % regulation is calculated using the formula,

$$\% \text{Regulation} = [(V_{NL} - V_{FL}) / V_{FL}] * 100$$

#### PRECAUTIONS:

1. The primary and secondary side of the transformer should be carefully identified
2. The polarities of all the diodes should be carefully identified.
3. While determining the % regulation, first full load should be applied and then it should be decremented in steps

#### RESULT:

#### POST LAB QUESTIONS:

1. What is the PIV of Half wave rectifier?
2. What is the efficiency of half wave rectifier?
3. What is the rectifier?
4. What is the difference between the half wave rectifier and Full Wave Rectifier?
5. What is the o/p frequency of Bridge Rectifier?
6. What are the ripples?
7. What is the function of the filters?
8. What is TUF?
9. What is the average value of o/p voltage for HWR?

## **EXPERIMENTNO.10(b)**

### **FULL-WAVERECTIFIERWITHANDWITHOUTFILTER**

**AIM:** To Examine the input and output waveforms of Full Wave Rectifier and also calculate its load regulation and ripple factor with Filter and without Filter.

#### **EQUIPMENT REQUIRED:**

SNO.	NAME OF THE EQUIPMENT	QUANTITY
1	Digital Multimeter	1 No.
2	Transformer (6V-0-6V)	1 No.
3	Diode, 1N4007	2 No.
4	Capacitor 100µF/470µF	1 No.
5	Decade Resistance Box	1 No.
6	Breadboard	1 No.
7	CRO	1 No.
8	CRO Probes Connecting wires	Required No.

#### **THEORY:**

The circuit of a center-tapped full wave rectifier uses two diodes D1 & D2. During positive half cycle of secondary voltage (input voltage), the diode D1 is forward biased and D2 is reverse biased. So, the diode D1 conducts and current flows through load resistor RL.

During negative half cycle, diode D2 becomes forward biased and D1 reverse biased. Now, D2 conducts and current flows through the load resistor RL in the same direction. There is a continuous current flow through the load resistor RL, during both the half cycles and will get unidirectional current as shown in the model graph. The difference between full wave and half wave rectification is that a full wave rectifier allows unidirectional (one way) current to the load during the entire 360 degrees of the input signal and half-wave rectifier allows this only during one half cycle (180 degree).

#### **THEORITICAL CALCULATIONS:**

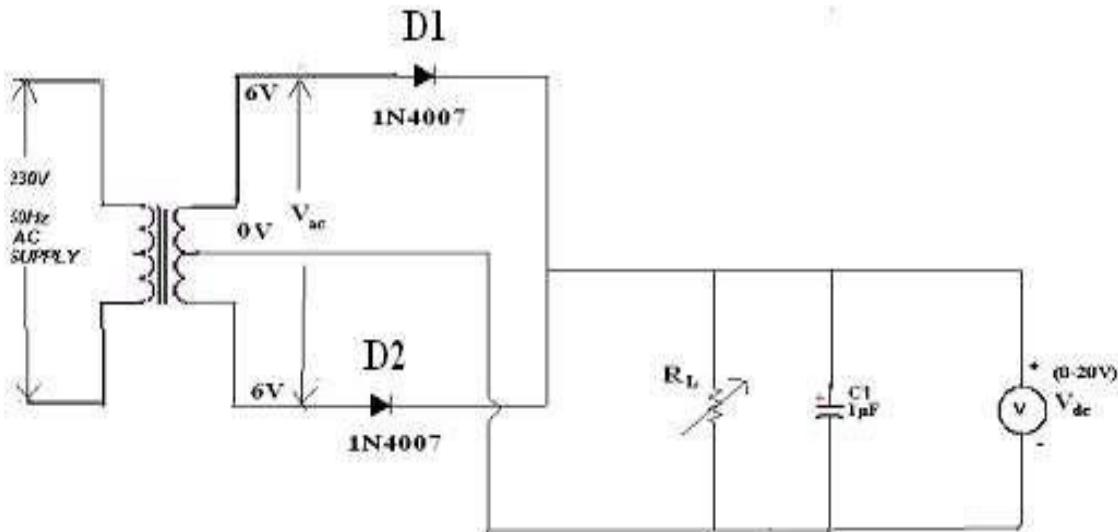
$$V_{rms} = V_m / \sqrt{2} \quad V_m = V_{rms} \sqrt{2} \quad V_{dc} = 2V_m / \pi$$

**Without filter:** Ripple factor, r =  $\sqrt{(V_{rms}/V_{dc})^2 - 1} = 0.812$

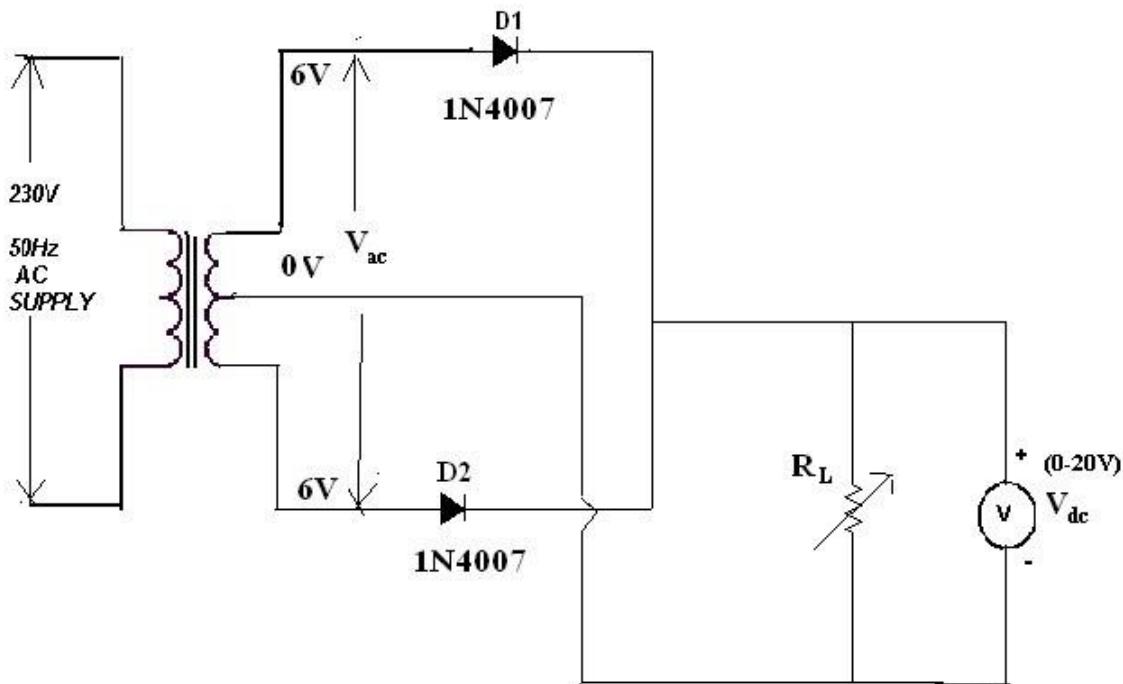
**With filter:** Ripple factor, r =  $1/(4\sqrt{3}fCRL)$

## CIRCUITDIAGRAM:

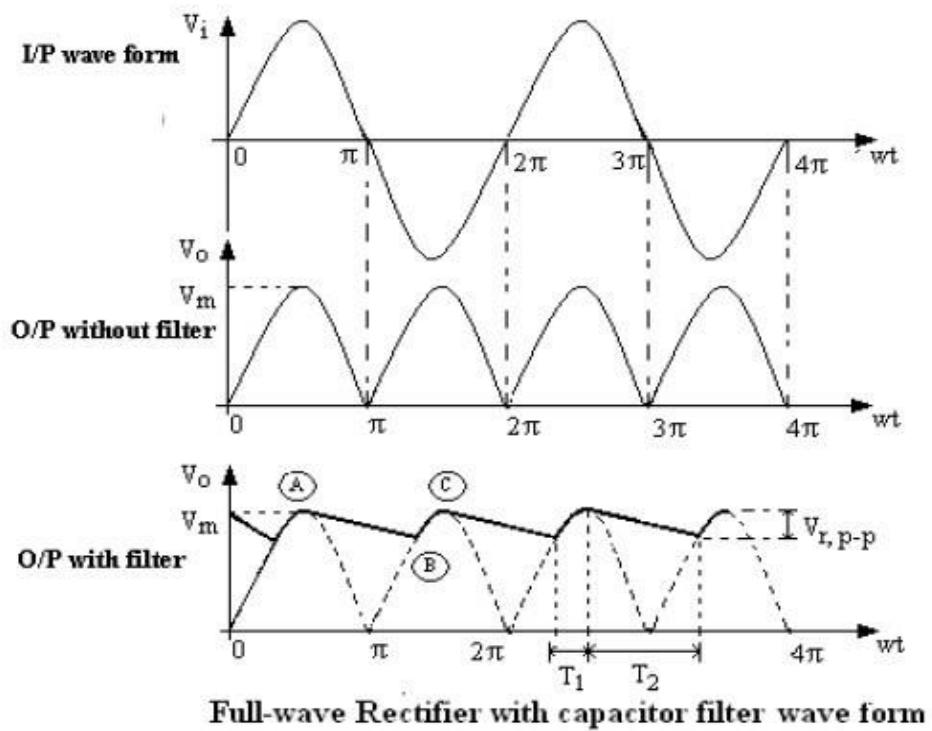
### FULLWAVERECTIFIERWITHOUTFILTER:



### FULLWAVERECTIFIERWITHFILTER:



## MODELWAVEFORMS:



Full-wave Rectifier with capacitor filter wave form

## WITHOUT FILTER:

S.No	Load Resistance $R_L$ kilo-ohm	O/P Voltage ( $V_o$ )		Ripple factor $\left( \gamma = \frac{V_{ac}}{V_{dc}} \right)$	% of Regulation $\left( \frac{V_{NL} - V_{FL}}{V_{NL}} * 100\% \right)$
		$V_{ac}$ (V)	$V_{dc}$ (V)		
1	1				
2	2				
3	3				
4	4				
5	5				
6	6				
7	7				
8	8				

## WITH CAPACITOR FILTER:

S.No	Load Resistance $R_L$ kilo-ohm	O/P Voltage ( $V_o$ )		Ripple factor $\left( \gamma = \frac{V_{ac}}{V_{dc}} \right)$	% of Regulation $\left( \frac{V_{NL} - V_{FL}}{V_{NL}} * 100\% \right)$
		$V_{ac}$ (V)	$V_{dc}$ (V)		
1	1				
2	2				
3	3				
4	4				
5	5				
6	6				
7	7				
8	8				

## **PROCEDURE:**

1. Connections are made as per the circuit diagram.
2. Connect the ac mains to the primary side of the transformer and the secondary side to the rectifier.
3. Measure the ac voltage at the input side of the rectifier.
4. Measure both ac and dc voltages at the output side of the rectifier.
5. Find the theoretical value of the dc voltage by using the formula  $V_{dc} = 2V_m/\pi$
6. Connect the filter capacitor across the load resistor and measure the values of  $V_{ac}$  and  $V_{dc}$  at the output.
7. The theoretical values of Ripple factors with and without capacitor are recalculated.
8. From the values of  $V_{ac}$  and  $V_{dc}$  practical values of Ripple factors are recalculated. The practical values are compared with theoretical values.

## **PRECAUTIONS:**

1. The primary and secondary side of the transformer should be carefully identified.
2. The polarities of all the diodes should be carefully identified.

## **RESULT:**

## **POST LAB QUESTIONS:**

1. Define regulation of the full wave rectifier?
2. Define peak inverse voltage (PIV)? And write its value for Full-wave rectifier?
3. If one of the diodes is changed in its polarities what waveform would you get?
4. Does the process of rectification alter the frequency of the waveform?
5. What is ripple factor of the Full-wave rectifier?
6. What is the necessity of the transformer in the rectifier circuit?
7. What are the applications of a rectifier?
8. What is meant by ripple and define Ripple factor?
9. Explain how capacitor helps to improve the ripple factor?
10. Can a rectifier made in INDIA ( $V=230v, f=50Hz$ ) be used in USA ( $V=110v, f=60Hz$ )?

## **EXPERIMENTNO.11**

### **STUDY OF LOGIC GATES**

**AIM:** To study and verify the truth table of logic gates

**OBJECTIVE:**

Identify various ICs and their specification

- a. OR gate
- b. AND gate
- c. NAND gate
- d. NOR gate

**COMPONENTS REQUIRED:**

- Breadboard.
- Connecting wires.
- IC 7400, IC 7408, IC 7432, IC 7406, IC 7402, IC 7404, IC 7486

**THEORY:**

The basic logic gates are the building blocks of more complex logic circuits. These logic gates perform the basic Boolean functions, such as AND, OR, NAND, NOR, Inversion, Exclusive-OR, Exclusive-NOR. Fig. below shows the circuit symbol, Boolean function, and truth. It is seen from the Fig that each gate has one or two binary inputs, A and B, and one binary output, C. The small circle on the output of the circuit symbols designates the logic complement. The AND, OR, NAND, and NOR gates can be extended to have more than two inputs. A gate can be extended to have multiple inputs if the binary operation it represents is commutative and associative. These basic logic gates are implemented as small-scale integrated circuits (SSICs) or as part of more complex medium scale (MSI) or very large-scale (VLSI) integrated circuits. Digital IC gates are classified not only by their logic operation, but also by the specific logic circuit family to which they belong. Each logic family has its own basic electronic circuit upon which more complex digital circuits and functions are developed. The following logic families are the most frequently used.

TTL Transistor-transistor logic

ECL Emitter-coupled logic

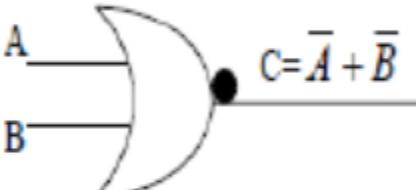
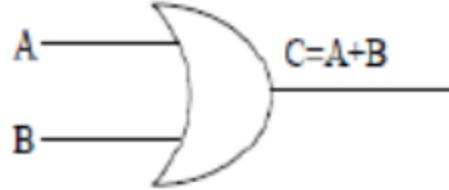
MOS Metal-oxide semiconductor

CMOS Complementary metal-oxide semiconductor

TTL and ECL are based upon bipolar transistors. TTL has a popularity among logic families. ECL is used only in systems requiring high-speed operation. MOS and CMOS, are based on field effect transistors. They are widely used in large scale integrated circuits because of their high component density and relatively low power consumption. CMOS logic consumes far less power than MOS logic. There are various commercial integrated circuit chips available. TTL ICs are usually distinguished by numerical designation as the 5400 and 7400 series.

**PROCEDURE:**

1. Check the components for their working.
2. Insert the appropriate IC into the IC base.
3. Make connections as shown in the circuit diagram.
4. Provide the input data via the input switches and observe the output on output LEDs

S.NO	GATE	SYMBOL	INPUTS		OUTPUT
			A	B	C
1.	NAND IC 7400	 A B $C = \overline{AB}$	0	0	1
			0	1	1
			1	0	1
			1	1	0
2.	NOR IC 7402	 A B $C = \overline{A} + \overline{B}$	0	0	1
			0	1	0
			1	0	0
			1	1	0
3.	AND IC 7408	 A B $C = AB$	0	0	0
			0	1	0
			1	0	0
			1	1	1
4.	OR IC 7432	 A B $C = A + B$	0	0	0
			0	1	1
			1	0	1
			1	1	1

**RESULT:** Logic gates have been studied.

#### QUESTIONS:

1. Why NAND & NOR gates are called universal gates?
2. Realize the EX – OR gates using minimum number of NAND gates.
3. Give the truth table for EX-NOR and realize using NAND gates?
4. What are the logic low and High levels of TTL IC's and CMOS IC's?
5. Compare TTL logic family with CMOS family?
6. Which logic family is fastest and which has low power dissipation?

