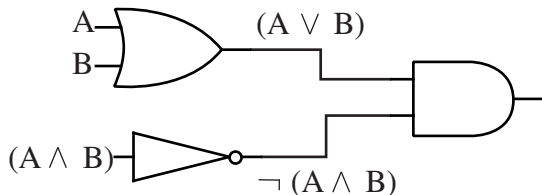
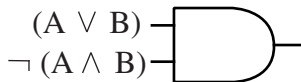


1. We know that the final output of the circuit is computed by an AND gate, whose inputs are as shown.



2. These inputs, in turn come from an OR gate and a NOT gate, with inputs as shown.

3. The circuit is completed by adding an AND gate to compute the input for the NOT gate, and connecting the circuit inputs, A and B , to the appropriate gate inputs.

