**CDAC Feb 2015**

D FLIP-FLOP

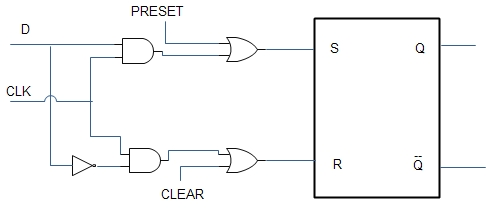
Name: Bhrigu Bhargava

**D FLIP-FLOP**

# Design Approach:

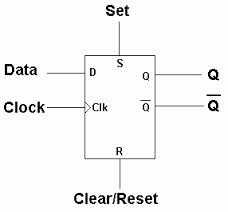
# In electronics, a flip-flop or latch is a circuit that has two stable states and can be used to store state information. A flip-flop is a beatable multi-vibrator. The circuit can be made to change state by signals applied to one or more control inputs and will have one or two outputs. It is the basic storage element in sequential logic. Flip-flops and latches are a fundamental building block of digital electronics systems used in computers, communications, and many other types of systems.

# Circuit Diagram:

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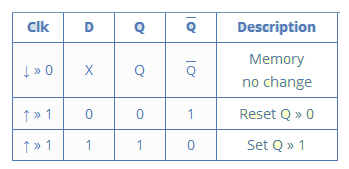
**Fig 1:- Circuit Diagram of D latch**

**Block Diagram:**

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**Fig 2:- Block Diagram of D latch**

**Truth Table:**

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**Source Code:**

module dff2 (output reg op, input d,clk,rst,pst);

always @ (posedge clk or posedge rst or posedge pst)

begin

if (pst==1'b1)

op<=1'b1;

else if (rst==1'b1)

op<=1'b0;

else

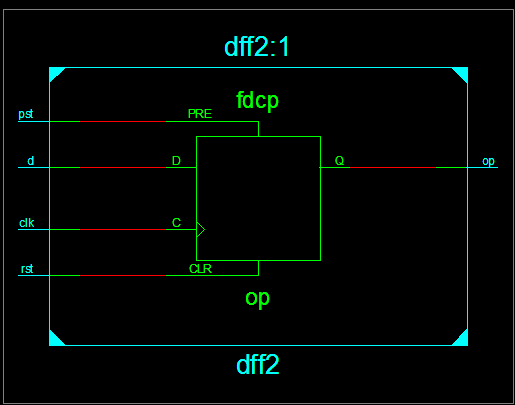
op<=d;

end

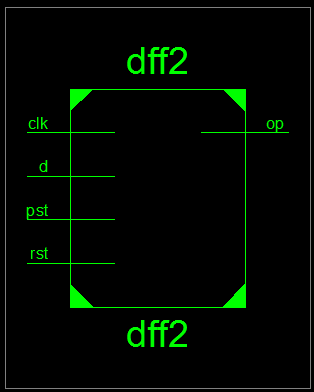
endmodule

**Synthesis:**

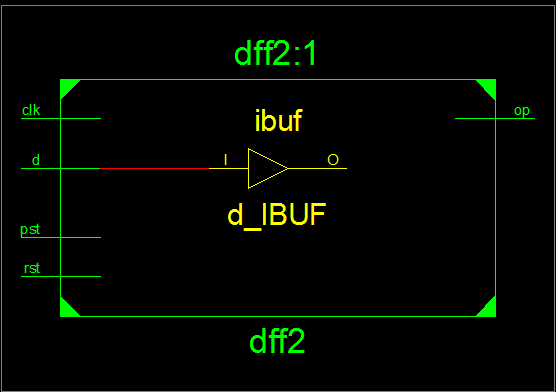
1. RTL Schematic



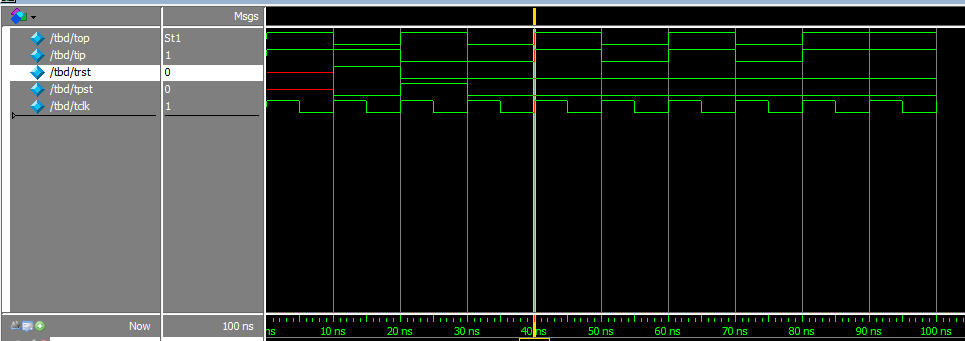
1. Block Diagram

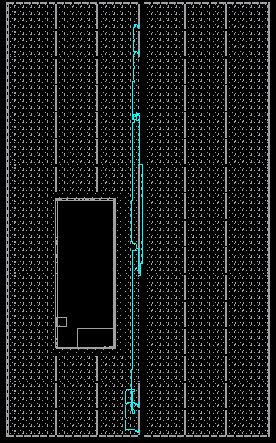
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1. Tech Schematic



1. Simulation Waveform Result

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**Error:**

None.

**Verified by:**

Dharamveer Chundawat (150240133007)