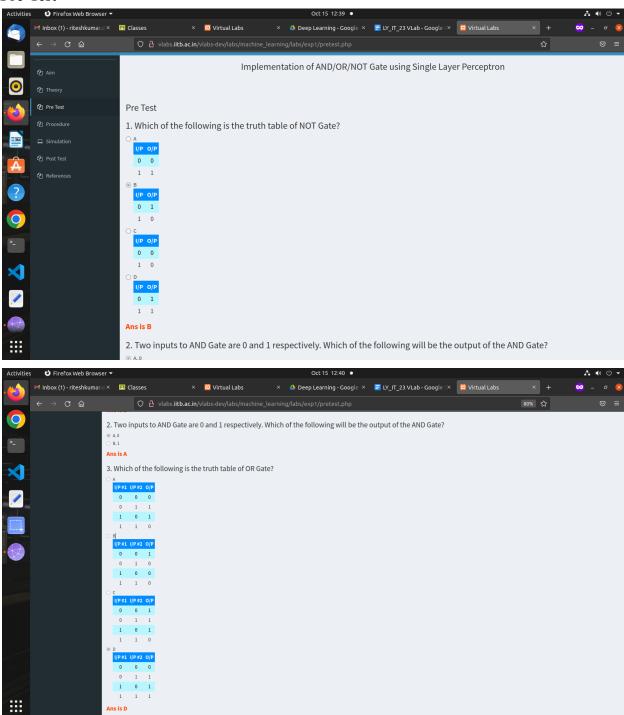
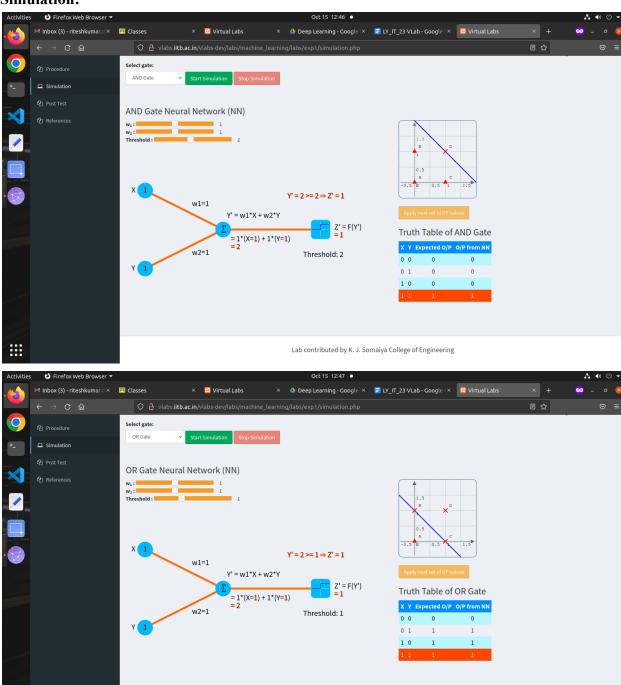
Experiment 1: Implementation of AND/OR/NOT Gate using Single Layer Perceptron

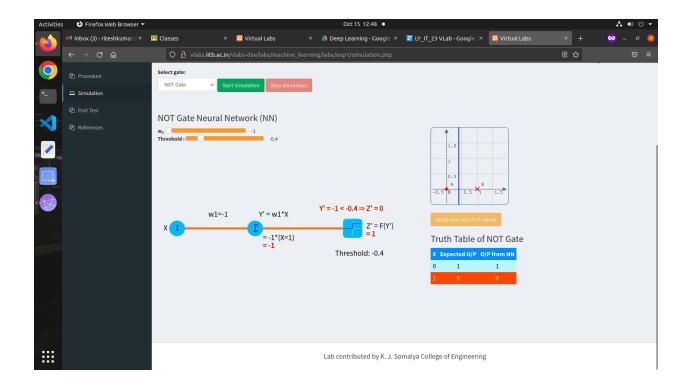
Pre-Test



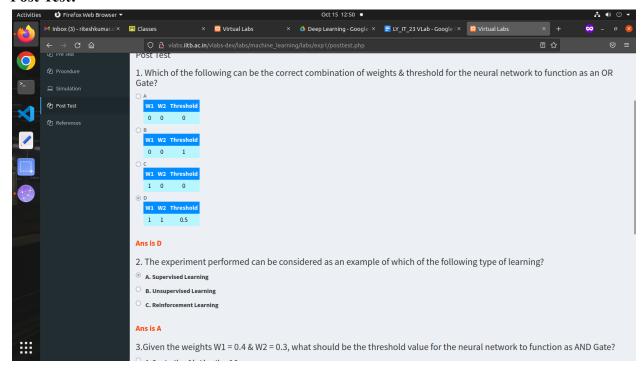
Simulation:

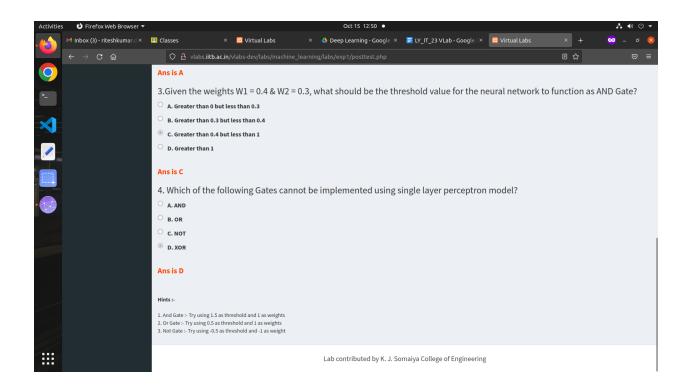


Lab contributed by K. J. Somaiya College of Engineering



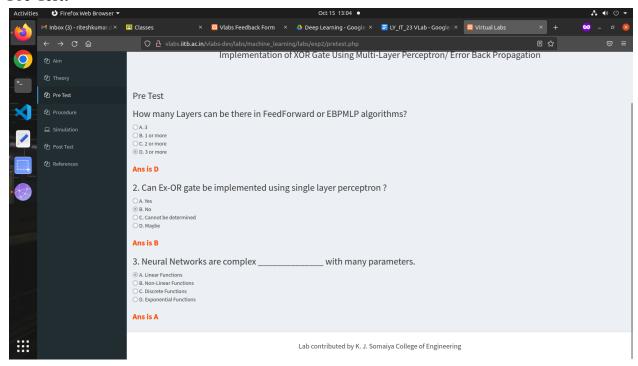
Post Test:



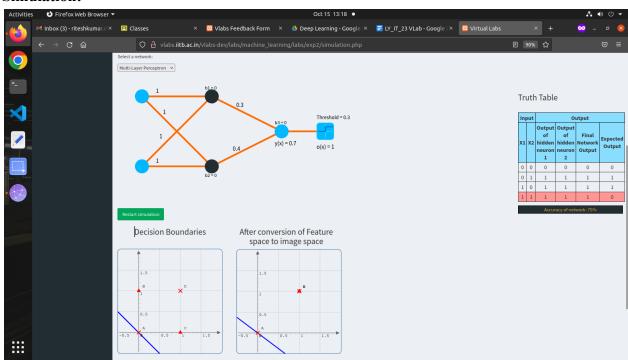


Experiment 2: Implementation of AND/OR/NOT Gate using Single Layer Perceptron

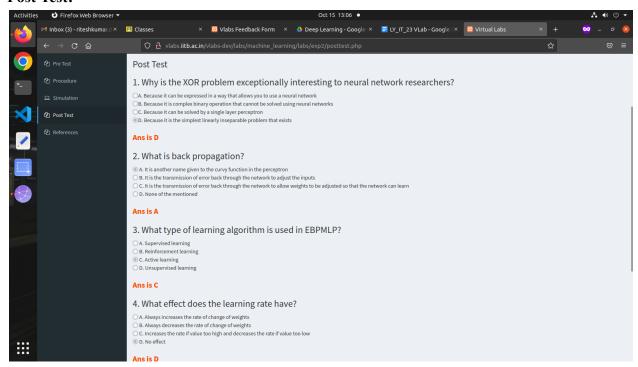
Pre-Test:



Simulation:

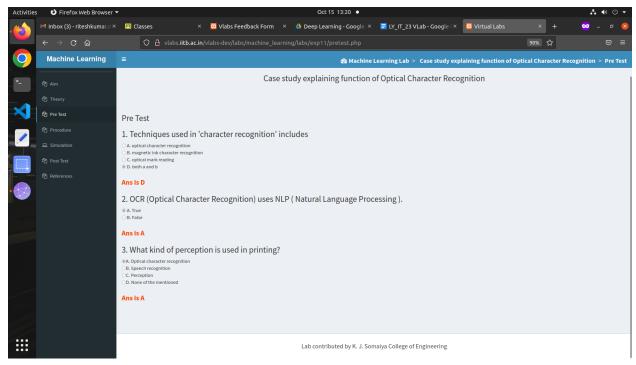


Post Test:

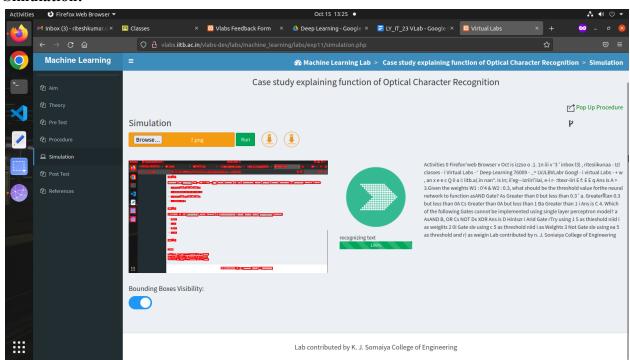


Experiment 3: Case study explaining function of Optical Character Recognition

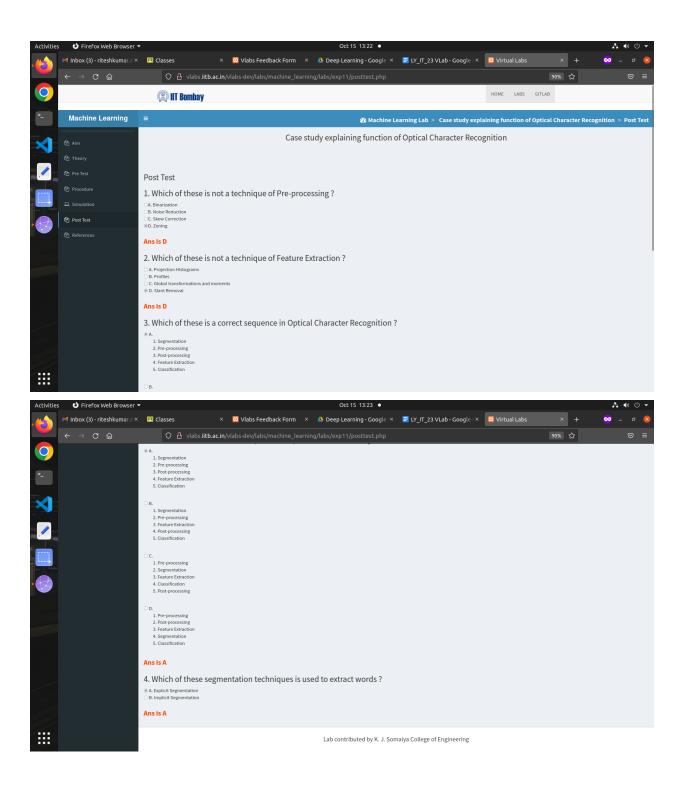
Pre-Test:



Simulation:



Post Test:



Feedback

