Lab 5.2 Testing Descriptions

Our testing methodology consisted of completely testing each instruction code using the multiple test files and assembler program. Because there were so many instructions to test, this time around we split up files to test different groupings of instructions. Instructions like branch and jump needed a bit more creativity to test if they worked so it made sense to split them into different files. We used the waveform to check each relevant internal signal as well as the final output signals during each state to check if they had the correct values. We confirmed the reliability of the processor after it correctly evaluated every instruction code.