1) If B has be leading Os, shift all registers (P.A.B) left k pontions 2 For 1=0 to n-1 (a) It top 3 bits of P are equal, set 2 =0, shift (P.A) one lithet (6) If top 3 laits of ? are not equal and P = 0, but 21 = 1, Shift (P.A) one lit lift, and subtact & from ? (c) If top 3 lite of P an not equal and PLO, set 25 = 1, Shift (P.A) one lost left, and add B to P End loop 3) If the final reminder is negative (PCO), correct the reminder by adding B to ?, and correct the propient by subhacting I from the gratient (4) Shift the reminder (in register?) le boits regett. 1.5.3 Implementing the SRT radix-2 algorithm on a Hayes HIX platform Q (0) 8-64 Date External Couhol

