1.1.2 Hardware implementation

Begin

Start/
Stop

Begin

c_0

A := 0, COUNTER := 0,
Q[-1] := 0, M := INBUS;

c_1

Q := INBUS;

Q[0] Q[-1]

01   10   0 0
                        1 1

c_2 c_2, c_3

A := A + M;   A := A - M;

c_4

Shift

A[6:0].Q[7:-1] := A[7:0].Q[7:0],
A[7] := A[7];

X

c_5

COUNTER := COUNTER + 1;

COUNT 7

1

c_6

OUTBUS := A;
Q[0] := '0';

c_7

OUTBUS := Q[7:0]

End   Start/
      stop

A[6:0].Q[7:-1] := A[7:0].
                  Q[7:0],
A[7] := FA[7];

Changes for fractional
operands