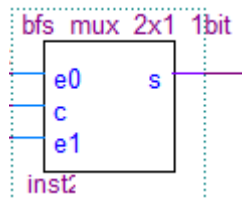


Aluna: Beatriz Forneck Soviero  
Cartão: 00342227

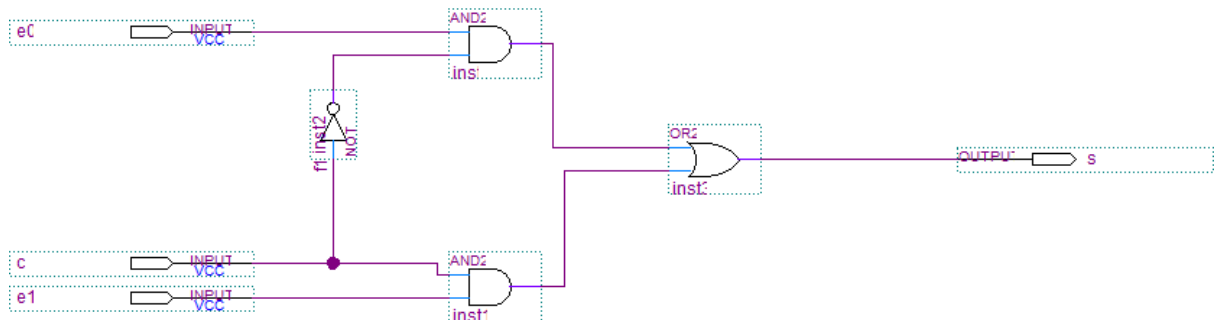
Nome do Projeto: bfs\_mux\_2x1\_1bit

Descrição: Circuito do multiplexador de 2 entradas de 1 bit

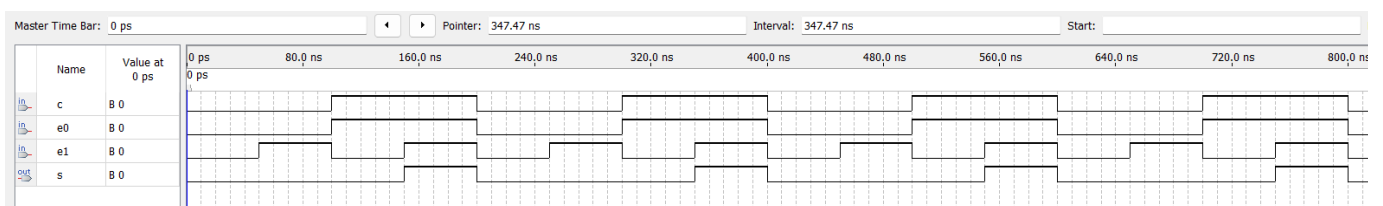
Símbolo:



Circuito:



Simulação:

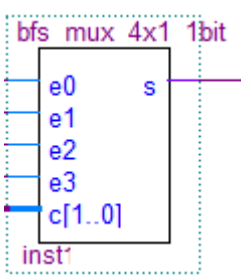


Aluna: Beatriz Forneck Soviero  
Cartão: 00342227

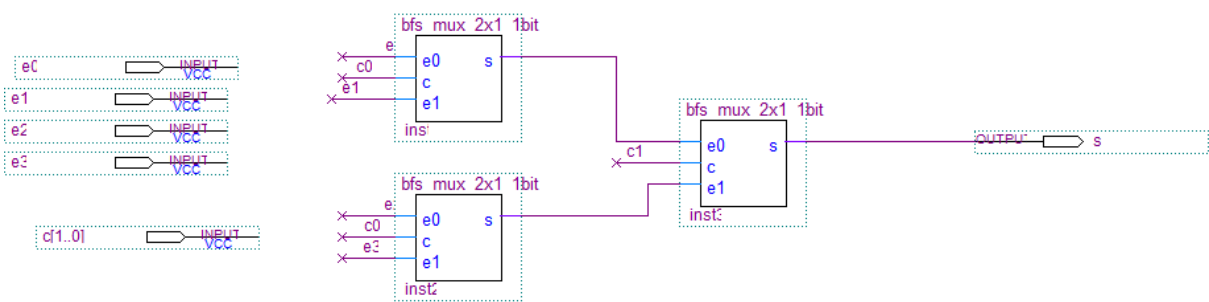
Nome do Projeto: bfs\_mux\_4x1\_1bit

Descrição: Circuito do multiplexador de 4 entradas de 1 bit

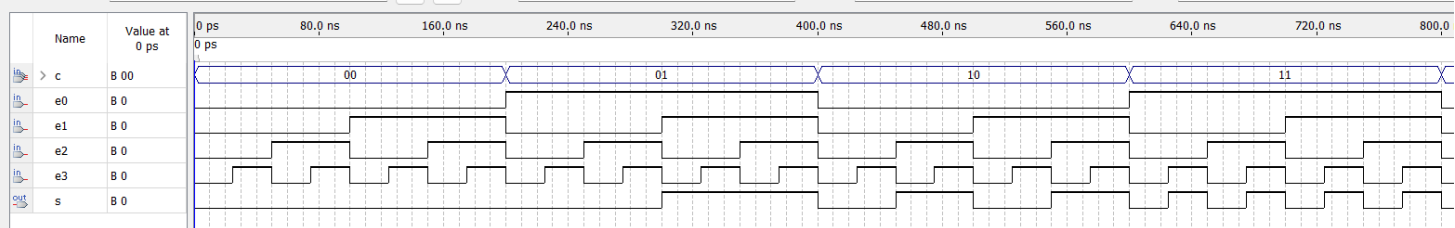
Símbolo:



Circuito:



Simulação:

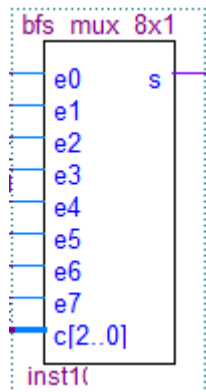


Aluna: Beatriz Forneck Soviero  
Cartão: 00342227

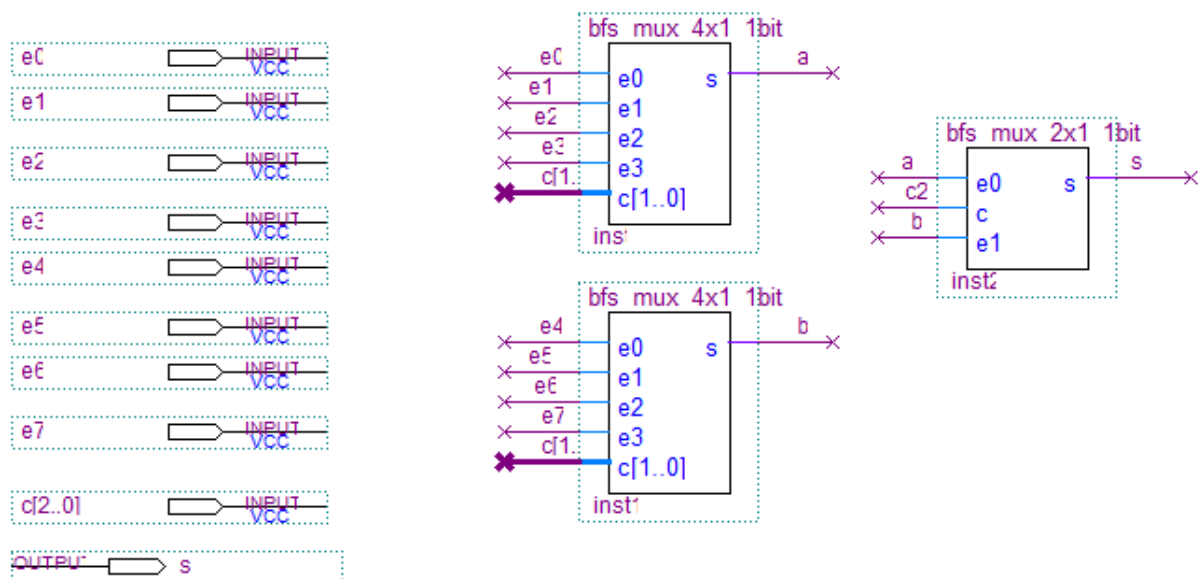
Nome do Projeto: bfs\_mux\_8x1

Descrição: Circuito do multiplexador de 8 entradas

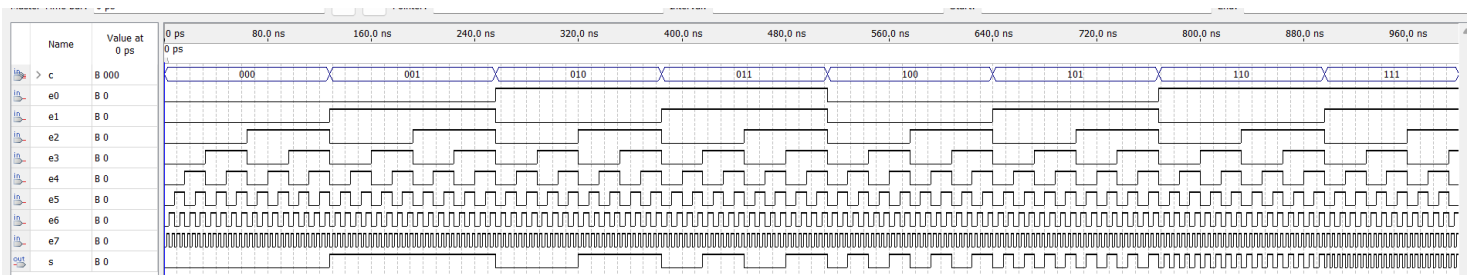
Símbolo:



Circuito:



Simulação:

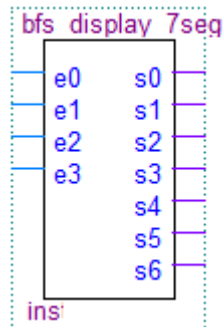


Aluna: Beatriz Forneck Soviero  
Cartão: 00342227

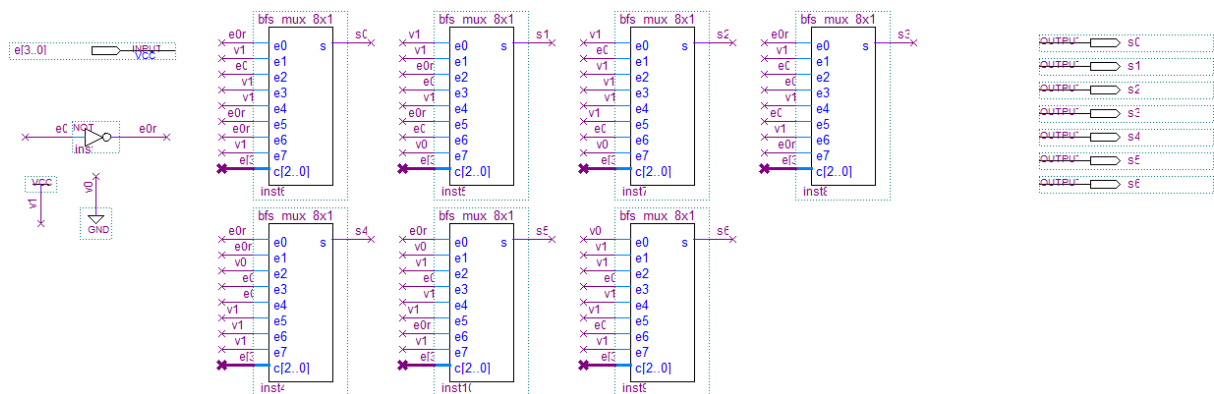
Nome do Projeto: bfs\_display\_7seg

Descrição: Circuito do display para 7 segmentos

Símbolo:



Circuito:



Simulação:

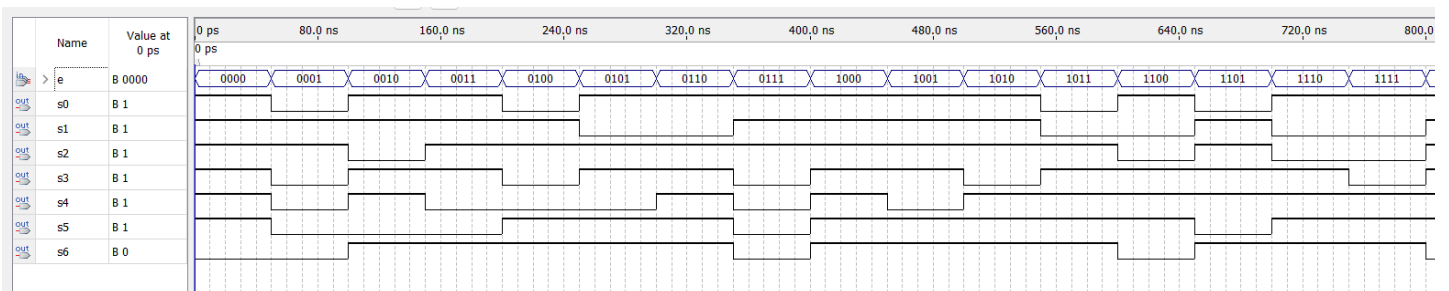


Tabela verdade:

e3	e2	e1	e0	s0	s1	s2	s3	s4	s5	s6
0	0	0	0	1	1	1	1	1	1	0
0	0	0	1	0	1	1	0	0	0	0
0	0	1	0	1	1	0	1	1	0	1
0	0	1	1	1	1	1	1	0	0	1
0	1	0	0	0	1	1	0	0	1	1
0	1	0	1	1	0	1	1	0	1	1
0	1	1	0	1	0	1	1	1	1	1
0	1	1	1	1	1	1	0	0	0	0
1	0	0	0	1	1	1	1	1	1	1
1	0	0	1	1	1	1	1	0	1	1
1	0	1	0	1	1	1	0	1	1	1
1	0	1	1	0	0	1	1	1	1	1
1	1	0	0	1	0	0	1	1	1	0
1	1	0	1	0	1	1	1	1	0	1
1	1	1	0	1	0	0	1	1	1	1
1	1	1	1	1	0	0	0	1	1	1