

SPRING SEMESTER EXAMINATION-2020(online)

4th Semester B. Tech, 2nd Semester B. Tech &2nd Semester M. Tech

SCHOOL OF COMPUTER ENGINEERING DEPARTMENT ELECTIVE / OPEN ELECTIVE

OPERATING SYSTEM CS 2002

Time: 2 Hours Full Marks: 50

(SECTION-A:1 Hour, SECTION-B:1 Hour)

Question paper consists of two sections-A, B.

Section A is compulsory.

Attempt any TWO questions from Sections B.

The figures in the margin indicate full marks.

Candidates are required to give their answers in their own words

	SECTION-A (Time: 1 Hour)									
1.	. (a) Justify whether "Creation of multiple threads is [2. preferred over multiple processes or not".									
	(b)	Consider process arrival as given below where n = right most significant digit of your Roll No.(ex:- for Roll No. 180854, n=4):								
		Process CPU Burst Arrival Time(ms) Time								
		A 4 0								
		В	3 6							
		C 6 n								

		D	1+(n%9)	3						
		Е								
		Calculate the following for <i>round robin</i> (time quantum								
		= 2 ms) CPU scheduling algorithm:								
		i. Average waiting time								
		ii. Turnaround time for each process								
		iii. Order of completion								
		iv. Minimum time quantum so that <i>round robin</i> will act as <i>first come first serve</i> scheduling.								
2.	(a)	Write down WAI	T() and SIGNAL()	operations on	[2.5]					
		counting semapho	re.							
	(1.)	A	<i>C</i> 1	20 34	[10]					
	(b)	J 1								
		process IDs [0, <i>n</i> -1]. A function <i>void Barrier(int pid)</i> is								
		defined over the set X such that if anyone process P_0 in								
		X with a process ID pid_0 calls $Barrier(int \ pid_0)$, it is								
		blocked until all processes in X have called Barrier()								
		with their process IDs. Write the pseudo-code for the								
		Barrier() function using only semaphores. List the								
		semaphores that you have used with their initial values								
		without concerning who creates and initializes. Show								
		the code for Barrier() that uses the semaphores								
		declared by making WAIT() and SIGNAL() calls on								
		them.								

SECTION-B(Time:1 Hour)											
3.	(a)	In a system n processes, P_1 ,, P_n share m resources of same type, R. Each resource can be acquired and released one at a time. If the maximum requirement of resources for each process P_i is r_i , where $r_i > 0$, then find the sufficient condition in extreme situation to ensure							[4.5]		
		deadlock does not occur in the system.									
	(b)	An operating system uses the Banker's algorithm for deadlock avoidance when managing the allocation of three resource types R ₀ , R ₁ , and R ₂ to three processes P ₀ , P ₁ , and P ₂ . The snapshot of current system state is shown in the following table. The Allocation matrix shows the number of resources of each type already allocated to each process and the Max matrix shows the maximum number of resources of each type required by each process during its execution.							[8]		
		Process		Max Allocation							
		P_0	R ₀	R ₁	R ₂		R ₀	R_1	R ₂		
		$\frac{\mathbf{P}_{0}}{\mathbf{P}_{1}}$	5	2	1		2	2	1		
		P ₂	6	3	3		2	1	1		
	There are 3 instances of resource type R_0 , 2 instances of resource type R_1 and 2 instances of resource type R_3										

		still available in the system. Check the system is					
		currently in a safe state. Find out which process will					
		complete its execution at end? Consider the following					
		independent requests for additional resources in the					
		current state:					
		REQ1: P0 requests 2 instances of resource type R3					
		only.					
		REQ2: P1 requests 1 instances of resource type R0					
		only.					
		Explain, which of the above request can be permitted?					
4.	(a)	How can not a process be allowed to access the	[4.5]				
		memory locations that it does not have its own?					
	(b)	Compare the address translation mechanism of paging	[8]				
		and segmentation for convection of logical addresses to					
		physical addresses. Consider a system with 32 bits					
		logical address and 4KB of the page size. The system					
		has also 512MB of physical memory. Find the					
		following:					
		i. Number of entries in a single-level page table					
		ii. Size of the single-level page table					
		iii. Maximum size of the program for two level					
		paging?					
5	(a)	A manufacturer wishes to design a hard disk with a	[4.5]				
		capacity of M GB. If the technology used to					
	1		L				

	manufacture the disks allows 1024-byte sectors, 2048 sectors/track, and 4096 tracks/platter, how many platters are required? (Assume M=10*(1+(right most digit of your Roll No % 9)); Ex:- for Roll No. 183452; M=10*(1+(2 % 9)) i. e 30)	
(b)	Explain the purpose and importance of system calls related to device management. Discuss the structure of directory and its implementation in detail *****	[8]

(Subhasis Dash)
Name of the Moderator/Course Coordinator