



# FORMAT-1

Semester: 4<sup>th</sup> Semester.

Programme: B.Tech.

Branch/Specialization: SCE.

## SPRING END SEMESTER EXAMINATION-2023

Spring Semester, 2023 (Programme)

**SUBJECT: OPERATING SYSTEMS**

**CODE: CS2002**

**(For 2021 Admitted Batches)**

**Time: 3 Hours**

**Full Marks: 50**

*Answer any SIX questions.*

*Question paper consists of four SECTIONS i.e. A, B, C and D.*

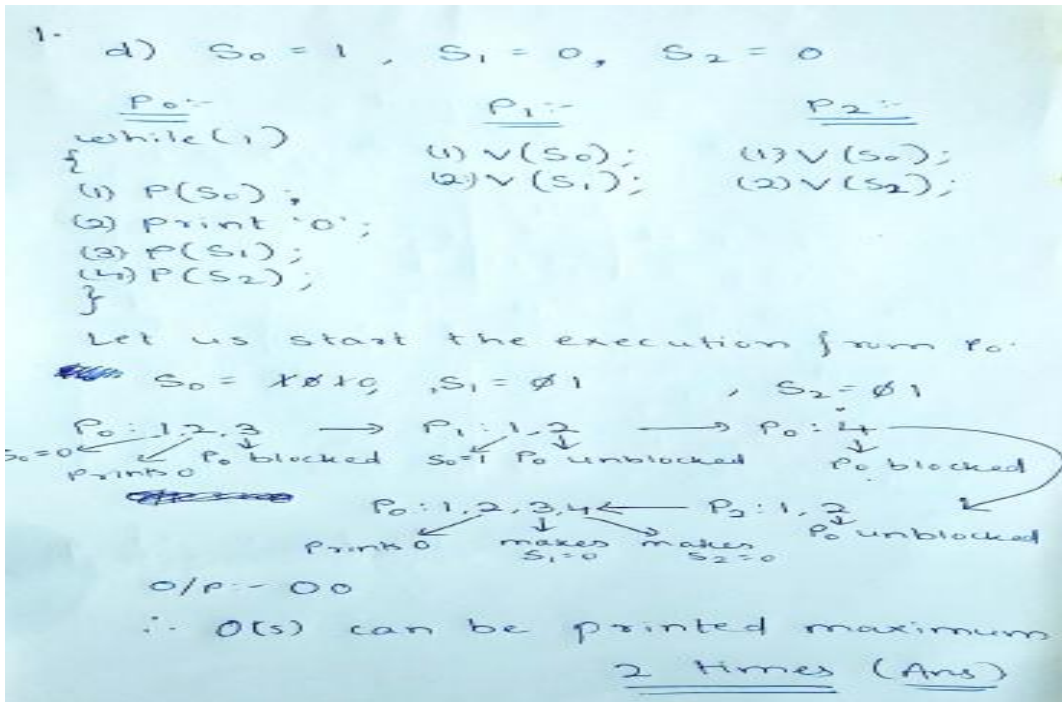
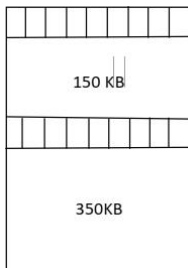
*Section A is compulsory.*

*Attempt minimum one question each from Sections B, C, D.*

*The figures in the margin indicate full marks.*

*Candidates are required to give their answers in their own words as far as practicable and all parts of a question should be answered at one place only.*

SECTION-A																						
1	Answer the following questions.																					
a	<p>The Shortest Job First (SJF) process scheduling algorithm leads to the problem of starvation. Discuss some of the ways to resolve this issue.</p> <p>Ans:</p> <p>Starvation in SJF exist only if processes with Lower Burst Time appears in queue before the process with Higher Burst time is executed. Since the algorithm will always choose the process with lowest Burst Time, the process with higher Burst Time will never be able to get the share of CPU. It is difficult to find a solution to starvation problem in SJF scheduling algorithm.</p> <p>One possible solution to this problem is to avoid more process to be scheduled in job pool once it is full i.e., Scheduler must wait until job pool is completely empty before it can get more process for execution.</p>																					
b	<p>There are some similarities between Semaphores and condition variables (of Monitor). What are the major differences between semaphores and condition variables?</p> <p>Ans:</p> <table><tr><td>Does not allow threads to wait. Each thread keeps running and last thread sets the semaphore value to zero and goes to sleep.</td><td>Allows threads to wait until particular condition occurs.</td></tr><tr><td>Used to solve problem of some critical sections in process synchronization.</td><td>Used with mutex to signal changing states from one thread to another.</td></tr><tr><td>Main aim is to control access to common resource by multiple processes and avoid critical section problem in concurrent system like multitasking operating system.</td><td>Main aim is to support operations that wake one or wake all waiting threads.</td></tr><tr><td>Can be used anywhere except in a monitor.</td><td>Can only be used in monitors.</td></tr><tr><td>wait() does not always block its caller.</td><td>wait () usually blocks its caller always.</td></tr><tr><td>They are sticky as they have memory and sem_post() will increment semaphore even if no one has called sem_wait().</td><td>They are non-sticky as signal () is not saved if there is no one waiting for signal ().</td></tr><tr><td>It is simply a counter + mutex + wait queue.</td><td>It is simply a wait-queue.</td></tr><tr><td>It can be used as a conditional variable and therefore is more lightweight and flexible.</td><td>It cannot be used as a semaphore and therefore is not flexible.</td></tr><tr><td>Signals are not lost even if there is nobody waiting on the queue.</td><td>Signals are lost if there is nobody waiting on the queue.</td></tr><tr><td>In this, we cannot unlock all waiting threads in same instant using broadcast unlocking.</td><td>In this, we can unlock all waiting threads in same instant using broadcast unlocking.</td></tr></table>		Does not allow threads to wait. Each thread keeps running and last thread sets the semaphore value to zero and goes to sleep.	Allows threads to wait until particular condition occurs.	Used to solve problem of some critical sections in process synchronization.	Used with mutex to signal changing states from one thread to another.	Main aim is to control access to common resource by multiple processes and avoid critical section problem in concurrent system like multitasking operating system.	Main aim is to support operations that wake one or wake all waiting threads.	Can be used anywhere except in a monitor.	Can only be used in monitors.	wait() does not always block its caller.	wait () usually blocks its caller always.	They are sticky as they have memory and sem_post() will increment semaphore even if no one has called sem_wait().	They are non-sticky as signal () is not saved if there is no one waiting for signal ().	It is simply a counter + mutex + wait queue.	It is simply a wait-queue.	It can be used as a conditional variable and therefore is more lightweight and flexible.	It cannot be used as a semaphore and therefore is not flexible.	Signals are not lost even if there is nobody waiting on the queue.	Signals are lost if there is nobody waiting on the queue.	In this, we cannot unlock all waiting threads in same instant using broadcast unlocking.	In this, we can unlock all waiting threads in same instant using broadcast unlocking.
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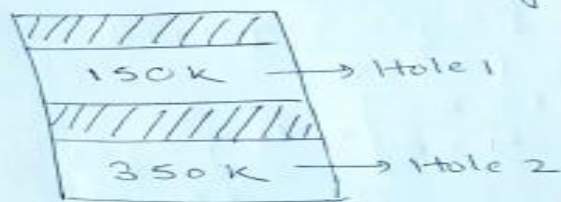
c	<p>Explain the difference between the logical/virtual and physical address. Discuss the benefits of virtual memory.</p> <p>Ans:</p> <table><tr><th>Logical Address</th><th>Physical Address</th></tr><tr><td>Generated by the CPU.</td><td>A location in the memory unit.</td></tr><tr><td>The address space consists of the set of all logical addresses.</td><td>This address is a set of all physical addresses.</td></tr><tr><td>Generated by CPU with reference to a specific program.</td><td>Computed using Memory Management Unit (MMU).</td></tr><tr><td>User has the ability to view the logical address of a program.</td><td>User can't view the physical address of program directly.</td></tr><tr><td>User can use the logical address in order to access the physical address.</td><td>User can indirectly access the physical address.</td></tr></table>	Logical Address	Physical Address	Generated by the CPU.	A location in the memory unit.	The address space consists of the set of all logical addresses.	This address is a set of all physical addresses.	Generated by CPU with reference to a specific program.	Computed using Memory Management Unit (MMU).	User has the ability to view the logical address of a program.	User can't view the physical address of program directly.	User can use the logical address in order to access the physical address.	User can indirectly access the physical address.
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d	<p>The code for three concurrent processes P0, P1 and P2 is as follows:</p> <table><tr><th>Process P0</th><th>Process P1</th><th>Process P2</th></tr><tr><td><pre>while(1) {     P(S0);     print '0';     P(S1);     P(S2); }</pre></td><td><pre>V(S0); V(S1);</pre></td><td><pre>V(S0); V(S2);</pre></td></tr></table> <p>The binary semaphores S0, S1 and S2 in the above code are initialized to 1, 0 and 0 respectively. What is the maximum number of zeroes to be printed? justify.</p> <p>Ans:</p> 	Process P0	Process P1	Process P2	<pre>while(1) {     P(S0);     print '0';     P(S1);     P(S2); }</pre>	<pre>V(S0); V(S1);</pre>	<pre>V(S0); V(S2);</pre>						
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e	<p>If the memory requests from processes are in the order: 300K, 25K, 125K and 50K and the current memory availability is as shown below:</p> <div></div> <p>Then which approach, between “first fit” and “best fit”, can satisfy the above memory requests?</p>												

Ans:

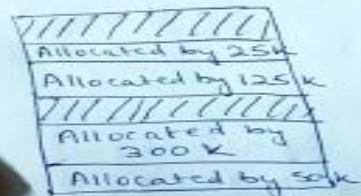
e) Memory requirement -

300K, 25K, 125K, 50K

Current Memory Availability:-



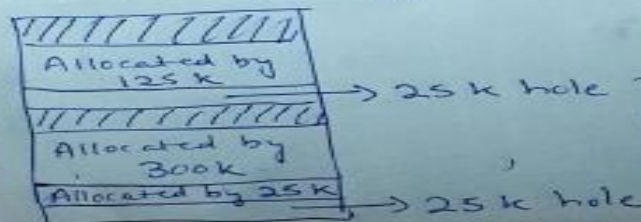
In First-fit approach:-



All memory requests are satisfied using first-fit approach

(Ans)

In best-fit approach:-



The last 50K memory reqn cannot be satisfied in best fit approach.

- f) Consider a process of size 13KB and page size is 4KB. How many pages would this process need? Calculate the % of unused space after allocation of the required pages.

Ans:

1.

f) Process size = 13KB

Page size = 4KB

So, no of pages required by this process

$$= \left\lceil \frac{13 \text{ KB}}{4 \text{ KB}} \right\rceil = 4 \text{ pages}$$

(Ans)

However, the 4<sup>th</sup> page will not be fully utilized as it will hold only 1KB of the process, that is, only 25% of the page.

Hence % of unused space after allocation of the pages is 75%

(Ans)

g	A Resource Allocation Graph (RAG) contains a directed cycle. Then, under what circumstances is there a possibility of deadlock? Ans: If there is only one unit per resource type, there is a deadlock. Otherwise, a deadlock is only a possibility.	
h	When a detection algorithm determines a deadlock has occurred? What are some ways to recover the system from this deadlock? Ans: Process Termination, Resource Preemption. Explain each of them.	
i	Rotational latency = $0.5 \times$ time to take one rotation true or false. Justify your answer. Ans: True. The average rotational latency for a disk is half the amount of time it takes for the disk to make one revolution.	
j	Let a Hard Disk has 6 surfaces, each surface has 8 tracks, and each track has 16 sectors per track. Find the cylinder, head and sector address of the logical block address 610. Ans: <6, 2, 2> 1 cylinder = $6 \times 16 = 96$ sectors $610/96 = 6^{\text{th}}$ cylinder Remainder = 32, advance 2 surfaces, 2 sector.	

#### SECTION-B (Learning levels 1, 2, and 3)

2	a	<p>Write the pseudo code for Peterson solution to the critical section problem. Explain the correctness of the solution which satisfies the 3 conditions for critical section problem.</p> <p>Ans:</p> <pre>P(i) {     flag[i] = TRUE;     turn = j;     while(flag[j] and turn=j);     critical Section     flag[i] := FALSE;     Remainder Section }</pre> <p><u>Mutual exclusion is preserved: (Proof by contradiction)</u>  Assume both processes <math>P_i</math> and <math>P_j</math> are in their CS.  Each <math>P_i</math> enters its critical section only if either <math>\text{flag}[j] = \text{false}</math> or <math>\text{turn} = i</math>.  <math>P_0</math> and <math>P_1</math> are both in CS only if <math>\text{flag}[0] = \text{flag}[1] = \text{true}</math> and <math>\text{turn} = i</math> for each <math>P_i</math> (which is impossible), <math>\text{turn}</math> cannot be both values <math>i</math> and <math>j</math> at a time, therefore one process must have entered its CS first (without loss of generality, say <math>P_0</math>), but this means that <math>P_1</math> could not have found <math>\text{turn} = 1</math> and therefore could not have entered its CS (i.e. contradiction).</p> <p><u>Progress requirement is satisfied:</u>  <math>P_i</math> cannot enter CS only if stuck in while() with condition <math>\text{flag}[j] = \text{true}</math> and <math>\text{turn} = j</math>.  Case-I: If <math>P_j</math> is not ready to enter CS then <math>\text{flag}[j] = \text{false}</math> and <math>P_i</math> can then enter its CS  Case-II: If <math>P_j</math> is ready to enter CS then <math>P_j</math> has set <math>\text{flag}[j] = \text{true}</math> and is in its while(), then either <math>\text{turn} = i</math> or <math>\text{turn} = j</math>. If <math>\text{turn} = i</math>, then <math>P_i</math> enters CS.  If <math>\text{turn} = j</math> then <math>P_j</math> enters CS, will reset <math>\text{flag}[j] = \text{false}</math> on exit: allowing <math>P_i</math> to enter CS</p> <p><u>Bounded waiting requirements is satisfied:</u>  <math>P_1</math> is executing its CS repeatedly upon exiting its CS, <math>P_1</math> sets <math>\text{flag}[1] = \text{false}</math>, hence the while loop is false for <math>P_0</math> and it can go.</p> <p>However, <math>P_1</math> may attempt to re-enter its CS before <math>P_0</math> has a chance to run but to re-enter, <math>P_1</math> sets <math>\text{flag}[1] = \text{true}</math> and sets <math>\text{turn} = 0</math>, hence the while loop is true for <math>P_1</math> and it waits but the while loop is now false for <math>P_0</math> and it can enter into CS at most one CS entry by <math>P_0</math> (bounded waiting).</p> <p>.</p>	4
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b		Available				
		R1	R2	R3	R4	
		2	1	0	0	

	Current Allocation					Maximum Demand			
Process	R1	R2	R3	R4		R1	R2	R3	R4
P1	0	0	1	2		0	0	1	2
P2	2	0	0	0		2	7	5	0
P3	0	0	3	4		6	6	5	6
P4	2	3	5	4		4	3	5	6
P5	0	3	3	2		0	6	5	2

Consider the above snapshot of a system. There are no current outstanding queued unsatisfied requests.

Is this system currently in safe or unsafe? Justify?

If safe write down the safe sequence. If unsafe mention the processes in unsafe state.

If a request from P3 arrives for (0, 1, 0, 0), can that request be safely granted immediately? In what state (deadlock, safe, unsafe) would immediately granting that whole request leave the system? Which processes, if any, are or may become deadlocked if this whole request is granted immediately?

Ans:

(i) & (ii)

Need Matrix				
	R1	R2	R3	R4
P1	0	0	0	0
P2	0	7	5	0
P3	6	6	2	2
P4	2	0	0	2
P5	0	3	2	0

(iii)

Allocation Matrix					Need Matrix				Available			
	R1	R2	R3	R4	R1	R2	R3	R4	R1	R2	R3	R4
P1	0	0	1	2	0	0	0	0	2	0	0	0
P2	2	0	0	0	0	7	5	0				
P3	0	1	3	4	6	5	2	2				
P4	2	3	5	4	2	0	0	2				
P5	0	3	3	2	0	3	2	0				

Allocation, available and need matrix: 1 mark

Procedure (explanation step by step): 1 mark

Unsafe state. Now P1, P4, P5 can finish, but with available now (4, 6, 9, 8) neither P2 nor P3 can be satisfied.

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		<div><p style="text-align: center;"><b>Gantt Chart</b></p><table><tr><td>A</td><td>A</td><td>B</td><td>A</td><td>C</td><td>B</td><td>D</td><td>A</td><td>C</td><td>B</td><td>D</td><td>C</td><td>B</td><td>D</td><td>C</td><td>B</td><td>D</td><td>C</td><td>B</td></tr><tr><td>0</td><td>3</td><td>6</td><td>9</td><td>12</td><td>15</td><td>18</td><td>21</td><td>24</td><td>27</td><td>30</td><td>33</td><td>36</td><td>39</td><td>42</td><td>45</td><td>48</td><td>51</td><td>54</td><td>57</td></tr></table> <table><tr><td>D</td><td>C</td><td>B</td><td>C</td></tr><tr><td>57</td><td>58</td><td>61</td><td>62</td><td>65</td></tr></table></div>	A	A	B	A	C	B	D	A	C	B	D	C	B	D	C	B	D	C	B	0	3	6	9	12	15	18	21	24	27	30	33	36	39	42	45	48	51	54	57	D	C	B	C	57	58	61	62	65	
A	A	B	A	C	B	D	A	C	B	D	C	B	D	C	B	D	C	B																																	
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		<table><tr><th>Job</th><th>Arrival Time</th><th>Burst Time</th><th>Finish Time</th><th>Turnaround Time</th><th>Waiting Time</th></tr><tr><td>A</td><td>0</td><td>12</td><td>24</td><td>24</td><td>12</td></tr><tr><td>B</td><td>5</td><td>19</td><td>62</td><td>57</td><td>38</td></tr><tr><td>C</td><td>8</td><td>21</td><td>65</td><td>57</td><td>36</td></tr><tr><td>D</td><td>11</td><td>13</td><td>58</td><td>47</td><td>34</td></tr><tr><td colspan="4">Average</td><td>185 / 4 = 46.25</td><td>120 / 4 = 30</td></tr></table>	Job	Arrival Time	Burst Time	Finish Time	Turnaround Time	Waiting Time	A	0	12	24	24	12	B	5	19	62	57	38	C	8	21	65	57	36	D	11	13	58	47	34	Average				185 / 4 = 46.25	120 / 4 = 30													
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D	11	13	58	47	34																																														
Average				185 / 4 = 46.25	120 / 4 = 30																																														
b	<p>Explain the various stages of Process State Transition with a neat diagram.</p> <p>Ans:</p> <p>Each process may be in any one of the following states:</p> <p><u>New</u>: Program going to be picked up by the OS into the main memory is called a new process i.e. a Process being created.</p> <p><u>Ready</u>: New process after being picked up by OS from the secondary memory is put in the main memory for execution i.e. the state of the process which is ready for the execution and resides in the main memory.</p> <p><u>Running</u>: One of the processes from the ready state will be chosen by the OS depending upon the scheduling algorithm to be allocated to CPU for execution.</p> <p><u>Waiting</u>: process waits for a certain resource to be assigned or for the input from the user then the OS move this process to the block or wait state and assigns the CPU to the other processes.</p> <p><u>Terminated</u>: process finishes its execution, then it comes in the termination state. All the context of the process (Process Control Block) will also be deleted and the process will be terminated by the Operating system.</p> <div></div>	4																																																	
<p style="text-align: center;"><b>SECTION-C (Learning Levels 3 and 4)</b></p>																																																			
4	a	<p>Consider the page reference string: 2, 3, 1, 2, 4, 5, 1, 2, 3, 4, 5, 2, 6, 2, 1, 2, 3, 1, 4, 5, 6. How many page faults would occur for the following page fault algorithms, assuming 3 and 4 available frames. Initially all the frames are empty.</p> <p>Optimal replacement</p> <p>LRU replacement</p> <p>Ans:</p> <p>(i) Optimal 12 and 9      (ii) LRU 17 and 14</p>	4																																																
	b	<p>Implement Reader-Writer problem with reader dominance using Critical region and Monitor. (Write the Pseudocode)</p>	4																																																

Ans:

Code for Writer Process	Code for Reader Process
<pre>wait(write); Write into the file signal(wrt);</pre>	<pre>int readcount=0; wait(mutex); readcount++; if(readcount==1) wait(wrt); signal(mutex); Read from file wait(mutex); readcount--; if (readcount == 0) signal(wrt); signal(mutex);</pre>

Monitor:

monitor ReadersWriters

condition OKtoWrite, OKtoRead;

int ReaderCount = 0;

Boolean busy = false;

```
procedure StartRead() {
    if(busy) OKtoRead.wait;
    ReaderCount++;
    OKtoRead.signal();
}
```

```
procedure EndRead() {
    ReaderCount-- ;
    if(ReaderCount==0) OKtoWrite.signal();
}
```

```
procedure StartWrite() {
    if(busy || ReaderCount!=0) OKtoWrite.wait();
    busy = true;
}
```

```
procedure EndWrite() {
    busy = false;
    If(OKtoRead.Queue) OKtoRead.signal();
    else OKtoWrite.signal();
}
```

```
Reader() {
    while(TRUE) {
        ReadersWriters.StartRead();
        readDatabase();
        ReadersWriters.EndRead();
    }
}
```

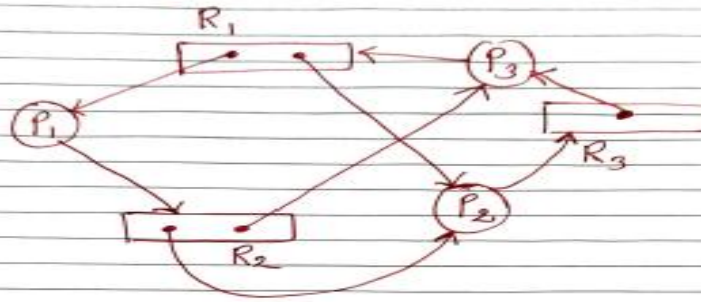
```
Writer() {
    while (TRUE) {
        make_data(&info);
        ReaderWriters.StartWrite();
        writeDatabase();
        ReadersWriters.EndWrite();
    }
}
```

5	a	A system has three processes (P1, P2, P3) and three resource types (R1, R2, R3). There are two instances of R1 and R2 and one instance of R3. P1 holds a R1 and is requesting a R2. P2	4
---	---	--	---



Ans:

Q. 5(a)



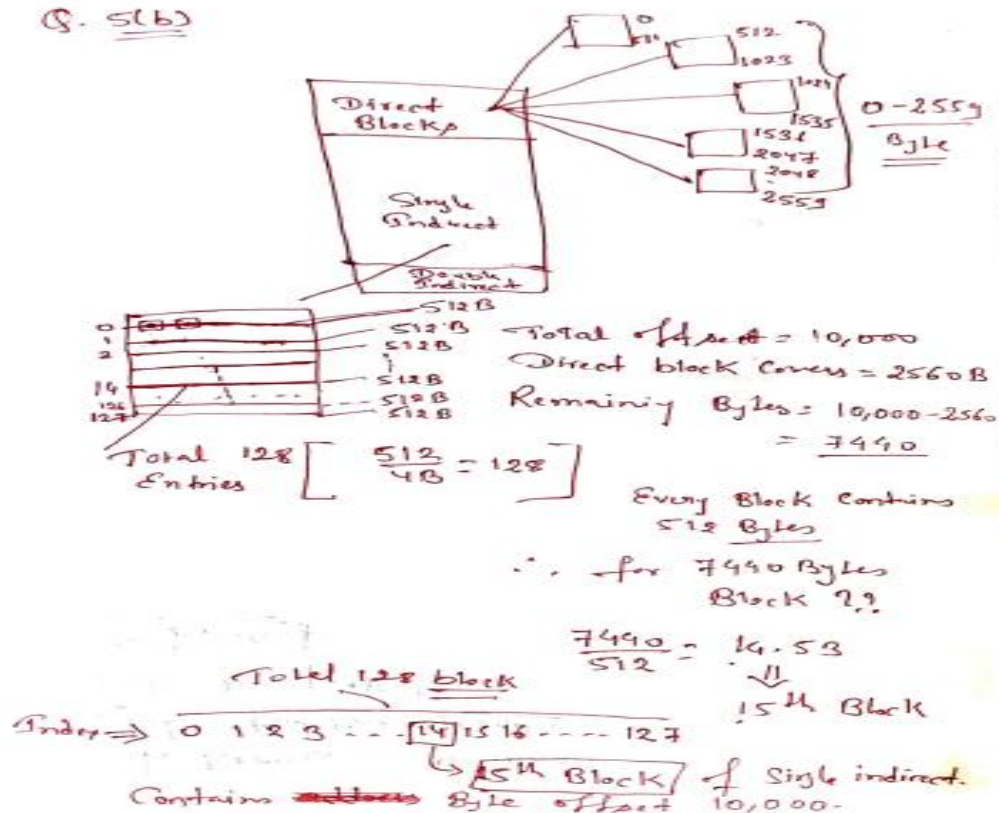
	Allocation	Need/Request	TOTAL
	$R_1 \ R_2 \ R_3$	$R_1 \ R_2 \ R_3$	$R_1 \ R_2 \ R_3$
$P_1$	1 0 0	0 1 0	2 2 1
$P_2$	1 1 0	0 0 1	
$P_3$	0 1 1	1 0 0	
	<u>2 2 1</u>		

Available  $\Rightarrow$  [TOTAL - Allocation]  
 $R_1 \ R_2 \ R_3$   
 0 0 0      Unsafe

With the available resources, we can not satisfy the need of any process. So, no safe sequence.

- Ans:

Q. 5(b)





6	<p>a What do you mean by multilevel feedback scheduling? Briefly describe its characteristics, features, pros and cons of the scheduling algorithm. Explain the algorithm by using a neat diagram with a suitable example.</p> <p><b>Ans:</b>  Multilevel feedback scheduling is a CPU scheduling algorithm that allows a process to be moved between different priority queues. Processes are initially placed in a high-priority queue and are moved to a lower-priority queue after a certain amount of time. If a process uses more CPU time than expected, it can be moved to a lower priority queue, while a process that waits for an I/O operation to complete can be moved to a higher priority queue.</p> <p><b>Characteristics:</b>  Multilevel feedback scheduling is a preemptive approach.  It allows the priority of a process to change during its execution.  It supports multiple queues with different priorities.  It can handle both CPU-bound and I/O-bound processes.</p> <p><b>Features:</b>  Multilevel feedback scheduling is a dynamic algorithm that adapts to changes in the workload.  It allows for better response time for interactive processes, as they can be given higher priority and moved to the higher priority queue.  The implementation of multiple queues with different priorities allows for better usage of system resources.  The algorithm is able to handle a mix of both short and long running processes.</p> <p><b>Pros:</b>  Multilevel feedback scheduling provides a good balance between performance and fairness in CPU scheduling.  It is able to handle both CPU and I/O intensive tasks well.  The use of multiple queues allows for better performance and resource utilization.</p> <p><b>Cons:</b>  Multilevel feedback scheduling can be complex to implement.  The parameter settings for the algorithm can greatly affect its performance.  The algorithm may be less efficient than other algorithms for certain types of workloads.</p> <p><b>Example:</b>  Suppose we have three queues: Queue1, Queue2, and Queue3. Queue1 has the highest priority and uses round-robin scheduling with a time quantum of 8. Queue2 uses priority scheduling, and Queue3 uses FCFS (First Come First Serve) scheduling. A new process enters the system and is placed in Queue1. It receives 8 milliseconds of CPU time, but it does not complete in that time. Therefore, it is moved to Queue2. After some time, it is demoted to Queue3, where it completes its execution. Meanwhile, another process enters the system and is placed in Queue1. It completes its execution in one round-robin time quantum, so it exits the system. Finally, a third process enters the system and is placed in Queue1. It receives 8 milliseconds of CPU time, but it does not complete in that time. Therefore, it is moved to Queue2. It continues to run in Queue2 until it completes its execution.</p> <div data-bbox="293 1675 730 2060"> <p>The diagram illustrates the Multilevel Feedback Scheduling algorithm with three queues arranged vertically. Queue 1 is at the top, labeled 'High Priority'. Queue 2 is in the middle. Queue 3 is at the bottom, labeled 'Low Priority'. A vertical arrow on the left points downwards from 'High Priority' to 'Low Priority'. Horizontal arrows show the movement of processes between queues: from Queue 1 to Queue 2, from Queue 2 to Queue 3, and from Queue 3 back to Queue 2. This represents the demotion of processes that use more CPU time than expected and the promotion of processes that wait for I/O operations to complete.</p> </div>	4
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b	There is a memory with four partitions 4K, 8K, 20K, and 2K (in order). The eight processes come at time 0 ms with different request size and the execution time (ms) as given in the following table.
---	---

Process no.	P1	P2	P3	P4	P5	P6	P7	P8
Req. Size	2K	14K	3K	6K	6K	10K	7K	4K
Execution time	4	9	2	8	1	6	5	3

Calculate the time at which process P7 will be completed if Best-Fit method is used in fixed partition memory allocation. Explain with proper diagram.

Ans:

time 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

[illegible]

So P7 will be completed at 14ms.

## SECTION-D (Learning levels 4, 5, 6)

7	a	Disk requests come into the disk driver for cylinders: 10, 22, 55, 2, 28, 12, 6, 40 and 38 in that order. The disk drive has 60 cylinders numbered 0 to 59 and the current head position is 26. A seek takes 6 millisecond / cylinder moved. How much seek time is needed for:
---	---	--

- |     |      |     |      |
|-----|------|-----|------|
| (A) | FCFS | (B) | SSTF |
| (C) | SCAN | (D) | LOOK |

Ans:

(A) In the FCFS (First-Come-First-Serve) scheduling algorithm, disk requests are serviced in the order they arrive. Given the current head position is 26 and the requests for cylinders are 10, 22, 55, 2, 28, 12, 6, 40, and 38, we can calculate the seek time as follows:

Move from cylinder 26 to 10:  $\text{abs}(26 - 10) = 16$  cylinders

Move from cylinder 10 to 22:  $\text{abs}(10 - 22) = 12$  cylinders

Move from cylinder 22 to 55:  $\text{abs}(22 - 55) = 33$  cylinders

Move from cylinder 55 to 2:  $\text{abs}(55 - 2) = 53$  cylinders

Move from cylinder 2 to 28:  $\text{abs}(2 - 28) = 26$  cylinders

Move from cylinder 28 to 12:  $\text{abs}(28 - 12) = 16$  cylinders

Move from cylinder 12 to 6:  $\text{abs}(12 - 6) = 6$  cylinders

Move from cylinder 6 to 40:  $\text{abs}(6 - 40) = 34$  cylinders

Move from cylinder 40 to 38:  $\text{abs}(40 - 38) = 2$  cylinders

Total cylinders moved:  $16 + 12 + 33 + 53 + 26 + 16 + 6 + 34 + 2 = 198$  cylinders

Given that a seek takes 6 ms per cylinder moved, the total seek time for FCFS would be:

$$198 \text{ cylinders} * 6 \text{ ms/cylinder} = 1188 \text{ ms}$$

(B) The SSTF (Shortest Seek Time First) scheduling algorithm services disk requests in the order of the shortest seek time from the current head position. Given the current head position is 26 and the requests for cylinders are 10, 22, 55, 2, 28, 12, 6, 40, and 38, we can calculate the seek time as follows:

Move from cylinder 26 to 28:  $\text{abs}(26 - 28) = 2$  cylinders

Move from cylinder 28 to 22:  $\text{abs}(28 - 22) = 6$  cylinders

Move from cylinder 22 to 12:  $\text{abs}(22 - 12) = 10$  cylinders

	<p>Move from cylinder 12 to 10: <math>\text{abs}(12 - 10) = 2</math> cylinders  Move from cylinder 10 to 6: <math>\text{abs}(10 - 6) = 4</math> cylinders  Move from cylinder 6 to 2: <math>\text{abs}(6 - 2) = 4</math> cylinders  Move from cylinder 2 to 38: <math>\text{abs}(2 - 38) = 36</math> cylinders  Move from cylinder 38 to 40: <math>\text{abs}(38 - 40) = 2</math> cylinders  Move from cylinder 40 to 55: <math>\text{abs}(40 - 55) = 15</math> cylinders  Total cylinders moved: <math>2 + 6 + 10 + 2 + 4 + 4 + 36 + 2 + 15 = 81</math> cylinders  Given that a seek takes 6 ms per cylinder moved, the total seek time for SSTF would be:  81 cylinders * 6 ms/cylinder = 486 ms</p> <p>(C) The SCAN scheduling algorithm services disk requests by moving the disk arm towards one end of the disk until it reaches the end, then reversing the direction and servicing the remaining requests as it moves in the opposite direction. Given the current head position is 26 and the requests for cylinders are 10, 22, 55, 2, 28, 12, 6, 40, and 38, we can calculate the seek time as follows:  2, 6, 10, 12, 22, 28, 38, 40, 55  Now we'll separate the requests into two groups based on the current head position (26):  Left: 2, 6, 10, 12, 22  Right: 28, 38, 40, 55  The SCAN algorithm will first service the right group because, head movement direction is not there and for this we have to choose the shortest distance and move in that direction. Then go all the way to the end (cylinder 59), and then reverse direction to service the left group:  Move from cylinder 26 to 28: <math>\text{abs}(26 - 28) = 2</math> cylinders  Move from cylinder 28 to 38: <math>\text{abs}(28 - 38) = 10</math> cylinders  Move from cylinder 38 to 40: <math>\text{abs}(38 - 40) = 2</math> cylinders  Move from cylinder 40 to 55: <math>\text{abs}(40 - 55) = 15</math> cylinders  Move from cylinder 55 to 59: <math>\text{abs}(55 - 59) = 4</math> cylinders (reaching the end of the disk)  Move from cylinder 59 to 22: <math>\text{abs}(59 - 22) = 37</math> cylinders  Move from cylinder 22 to 12: <math>\text{abs}(22 - 12) = 10</math> cylinders  Move from cylinder 12 to 10: <math>\text{abs}(12 - 10) = 2</math> cylinders  Move from cylinder 10 to 6: <math>\text{abs}(10 - 6) = 4</math> cylinders  Move from cylinder 6 to 2: <math>\text{abs}(6 - 2) = 4</math> cylinders  Total cylinders moved: <math>2 + 10 + 2 + 15 + 4 + 37 + 10 + 2 + 4 + 4 = 90</math> cylinders  Given that a seek takes 6 ms per cylinder moved, the total seek time for SCAN would be:  90 cylinders * 6 ms/cylinder = 540 ms</p> <p>(D) The LOOK scheduling algorithm is similar to SCAN, but instead of going all the way to the end of the disk, it only goes as far as the last requested cylinder in each direction before reversing. Given the current head position is 26 and the requests for cylinders are 10, 22, 55, 2, 28, 12, 6, 40, and 38, we can calculate the seek time as follows:  We'll first sort the requests in ascending order:  2, 6, 10, 12, 22, 28, 38, 40, 55  Now we'll separate the requests into two groups based on the current head position (26):  Left: 2, 6, 10, 12, 22  Right: 28, 38, 40, 55  The LOOK algorithm will first service the right group because, head movement direction is not there and for this we have to choose the shortest distance and move in that direction. Then reverse direction to service the left group:  Move from cylinder 26 to 28: <math>\text{abs}(26 - 28) = 2</math> cylinders  Move from cylinder 28 to 38: <math>\text{abs}(28 - 38) = 10</math> cylinders  Move from cylinder 38 to 40: <math>\text{abs}(38 - 40) = 2</math> cylinders  Move from cylinder 40 to 55: <math>\text{abs}(40 - 55) = 15</math> cylinders  Move from cylinder 55 to 22: <math>\text{abs}(55 - 22) = 33</math> cylinders  Move from cylinder 22 to 12: <math>\text{abs}(22 - 12) = 10</math> cylinders  Move from cylinder 12 to 10: <math>\text{abs}(12 - 10) = 2</math> cylinders  Move from cylinder 10 to 6: <math>\text{abs}(10 - 6) = 4</math> cylinders  Move from cylinder 6 to 2: <math>\text{abs}(6 - 2) = 4</math> cylinders  Total cylinders moved: <math>2 + 10 + 2 + 15 + 33 + 10 + 2 + 4 + 4 = 82</math> cylinders  Given that a seek takes 6 ms per cylinder moved, the total seek time for LOOK would be:  82 cylinders * 6 ms/cylinder = 492 ms</p>	
b	<p>A manufacturer wishes to design a hard disk with a capacity of <math>2^{10}</math> GB. If the technology used to manufacture the disks allows 1 KB sector size, 4 K sectors per track, and having <math>2^{10}</math></p>	4

		platters, then how many cylinders are required in the hard disk? Ans: To determine the number of cylinders required for the hard disk, we first need to find the total number of sectors needed. Given capacity: $2^{10}$ GB = 1024 GB Convert GB to KB: $1024 \text{ GB} \times 1024 \text{ MB/GB} \times 1024 \text{ KB/MB} = 2^{30} \text{ KB}$ Now, we can calculate the number of sectors needed: Sector size: 1 KB Total number of sectors: $(2^{30} \text{ KB}) / (1 \text{ KB/sector}) = 2^{30}$ sectors Next, we need to find the number of tracks required: Sectors per track: 4 K sectors Total number of tracks: $(2^{30} \text{ sectors}) / (4 \text{ K sectors/track}) = (2^{30} \text{ sectors}) / (2^{12} \text{ sectors/track}) = 2^{(30-12)} \text{ tracks} = 2^{18} \text{ tracks}$ We are given that there are $2^{10}$ platters. We can calculate the total number of tracks per platter: Total tracks per platter: $2^{18} \text{ tracks} / 2^{10} \text{ platters} = 2^{(18-10)} \text{ tracks/platter} = 2^8 \text{ tracks/platter}$ Finally, we can find the number of cylinders required. Since each cylinder consists of two tracks per platter(one on the top and one on the bottom), the number of cylinders is equal to the number of tracks per platter: Number of cylinders: $2^8 / 2 = 128 \text{ cylinders}$ So, the hard disk requires 128 cylinders.																																																																																																																																																																																																																																																																																																																																																																									
8	a	Suppose that pages in a virtual address space are referenced in the order: 1 2 4 1 1 5 6 8 1 2 5 3 4 6 7 1 5. There are 3 empty frames available. Show the contents of frames after each memory reference and find the total number of page faults using “FIFO” and “LFU (Least Frequently Used)” page replacement algorithms. Also write the number of page faults for both the algorithms if the reference string is reversed. Ans: <u>FIFO</u> <table><tr><td></td><td>1</td><td>2</td><td>4</td><td>1</td><td>1</td><td>5</td><td>6</td><td>8</td><td>1</td><td>2</td><td>5</td><td>3</td><td>4</td><td>6</td><td>7</td><td>1</td><td>5</td></tr><tr><td>F1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>5</td><td>5</td><td>5</td><td>1</td><td>1</td><td>1</td><td>3</td><td>3</td><td>3</td><td>7</td><td>7</td><td>7</td></tr><tr><td>F2</td><td></td><td>2</td><td>2</td><td>2</td><td>2</td><td>2</td><td>6</td><td>6</td><td>6</td><td>2</td><td>2</td><td>2</td><td>4</td><td>4</td><td>4</td><td>1</td><td>1</td></tr><tr><td>F3</td><td></td><td></td><td>4</td><td>4</td><td>4</td><td>4</td><td>4</td><td>8</td><td>8</td><td>8</td><td>5</td><td>5</td><td>5</td><td>6</td><td>6</td><td>6</td><td>5</td></tr><tr><td></td><td>F</td><td>F</td><td>F</td><td>H</td><td>H</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td></tr></table> # of Page fault: 15 <u>FIFO (Reverse Order)</u> <table><tr><td></td><td>5</td><td>1</td><td>7</td><td>6</td><td>4</td><td>3</td><td>5</td><td>2</td><td>1</td><td>8</td><td>6</td><td>5</td><td>1</td><td>1</td><td>4</td><td>2</td><td>1</td></tr><tr><td>F1</td><td>5</td><td>5</td><td>5</td><td>6</td><td>6</td><td>6</td><td>5</td><td>5</td><td>5</td><td>8</td><td>8</td><td>8</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr><tr><td>F2</td><td></td><td>1</td><td>1</td><td>1</td><td>4</td><td>4</td><td>4</td><td>2</td><td>2</td><td>2</td><td>6</td><td>6</td><td>6</td><td>6</td><td>4</td><td>4</td><td>4</td></tr><tr><td>F3</td><td></td><td></td><td>7</td><td>7</td><td>7</td><td>3</td><td>3</td><td>3</td><td>1</td><td>1</td><td>1</td><td>5</td><td>5</td><td>5</td><td>5</td><td>2</td><td>2</td></tr><tr><td></td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>H</td><td>F</td><td>F</td><td>H</td></tr></table> # of Page fault: 15  <u>LFU</u> <table><tr><td></td><td>1</td><td>2</td><td>4</td><td>1</td><td>1</td><td>5</td><td>6</td><td>8</td><td>1</td><td>2</td><td>5</td><td>3</td><td>4</td><td>6</td><td>7</td><td>1</td><td>5</td></tr><tr><td>F1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr><tr><td>F2</td><td></td><td>2</td><td>2</td><td>2</td><td>2</td><td>5</td><td>5</td><td>8</td><td>8</td><td>8</td><td>5</td><td>5</td><td>4</td><td>4</td><td>7</td><td>7</td><td>7</td></tr><tr><td>F3</td><td></td><td></td><td>4</td><td>4</td><td>4</td><td>4</td><td>6</td><td>6</td><td>6</td><td>2</td><td>2</td><td>3</td><td>3</td><td>6</td><td>6</td><td>6</td><td>5</td></tr><tr><td></td><td>F</td><td>F</td><td>F</td><td>H</td><td>H</td><td>F</td><td>F</td><td>F</td><td>H</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>H</td><td>F</td></tr></table> # of Page fault: 13  <u>LFU (Reverse Order)</u> <table><tr><td></td><td>5</td><td>1</td><td>7</td><td>6</td><td>4</td><td>3</td><td>5</td><td>2</td><td>1</td><td>8</td><td>6</td><td>5</td><td>1</td><td>1</td><td>4</td><td>2</td><td>1</td></tr><tr><td>F1</td><td>5</td><td>5</td><td>5</td><td>6</td><td>6</td><td>6</td><td>5</td><td>5</td><td>5</td><td>8</td><td>8</td><td>8</td><td>1</td><td>1</td><td>1</td><td>1</td><td>1</td></tr><tr><td>F2</td><td></td><td>1</td><td>1</td><td>1</td><td>4</td><td>4</td><td>4</td><td>2</td><td>2</td><td>2</td><td>6</td><td>6</td><td>6</td><td>6</td><td>4</td><td>4</td><td>4</td></tr><tr><td>F3</td><td></td><td></td><td>7</td><td>7</td><td>7</td><td>3</td><td>3</td><td>3</td><td>1</td><td>1</td><td>1</td><td>5</td><td>5</td><td>5</td><td>5</td><td>2</td><td>2</td></tr><tr><td></td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>F</td><td>H</td><td>F</td><td>F</td><td>H</td></tr></table> # of Page fault: 15		1	2	4	1	1	5	6	8	1	2	5	3	4	6	7	1	5	F1	1	1	1	1	1	5	5	5	1	1	1	3	3	3	7	7	7	F2		2	2	2	2	2	6	6	6	2	2	2	4	4	4	1	1	F3			4	4	4	4	4	8	8	8	5	5	5	6	6	6	5		F	F	F	H	H	F	F	F	F	F	F	F	F	F	F	F	F		5	1	7	6	4	3	5	2	1	8	6	5	1	1	4	2	1	F1	5	5	5	6	6	6	5	5	5	8	8	8	1	1	1	1	1	F2		1	1	1	4	4	4	2	2	2	6	6	6	6	4	4	4	F3			7	7	7	3	3	3	1	1	1	5	5	5	5	2	2		F	F	F	F	F	F	F	F	F	F	F	F	F	H	F	F	H		1	2	4	1	1	5	6	8	1	2	5	3	4	6	7	1	5	F1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	F2		2	2	2	2	5	5	8	8	8	5	5	4	4	7	7	7	F3			4	4	4	4	6	6	6	2	2	3	3	6	6	6	5		F	F	F	H	H	F	F	F	H	F	F	F	F	F	F	H	F		5	1	7	6	4	3	5	2	1	8	6	5	1	1	4	2	1	F1	5	5	5	6	6	6	5	5	5	8	8	8	1	1	1	1	1	F2		1	1	1	4	4	4	2	2	2	6	6	6	6	4	4	4	F3			7	7	7	3	3	3	1	1	1	5	5	5	5	2	2		F	F	F	F	F	F	F	F	F	F	F	F	F	H	F	F	H	4
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b	<p>Ans:</p> <p>Each potassium thread waits to be grouped with one other potassium and oxygen before returning</p> <p>Each oxygen thread waits for two other potassium before returning</p> <p>Only one thread access shared state at a time</p> <p>There is only one condition any thread will wait for, i.e. a Potassium Oxide molecule being formed. However, it will be necessary to signal potassium and oxygen threads independently, so we will use two condition variables, waitingP and waitingO.</p> <p>It will be necessary to know the number of potassium and oxygen threads in the monitor. But it would be more useful to know how many potassium and oxygen threads have been assigned and have not been assigned to Potassium Oxide molecules; let these be int wP (number of waiting potassium), wO (number of waiting oxygen), aP (number of assigned potassium), and aO (number of assigned oxygen). These are all initialized to 0.</p> <pre> Potassium() {     lock.acquire();     wP++;     while(aP==0) {         if(wP&gt;=2 &amp;&amp; wO&gt;=1) {             wP-=2; aP+=2;             wO-=1; aO+=1;             waitingP.signal();             waitingO.signal();         }         else {             waitingP.wait();         }     }     aP--;     lock.release(); }  Oxygen() {     lock.acquire();     wO++;     while(a==0) {         if(wP&gt;=2 &amp;&amp; wO&gt;=1) {             wP-=2; aP+=2;             wO-=1; aO+=1;             waitingP.signal();             waitingP.signal();         }         else {             waitingO.wait();         }     }     aO--;     lock.release(); } </pre> <p>Partly correct, part marks can be given.</p>	4
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