



Sample Question Format
(For all courses having end semester Full Mark=50)

KIIT Deemed to be University
Online End Semester Examination(Autumn Semester-2021)

Subject Name & Code: Operating Systems (CS 2002)
Applicable to Courses: All brances

Full Marks=50

Time:2 Hours

SECTION-A(Answer All Questions. Each question carries 2 Marks)

Time:30 Minutes

(7×2=14 Marks)

<u>Question No</u>	<u>Question Type (MCQ /SAT)</u>	<u>Question</u>	<u>CO Mapping</u>	<u>Answer Key (For MCQ Questions only)</u>
<u>Q.No:1</u>	<u>MCQ</u>	Which one of the following is not true? A. Kernel is made of various modules which cant be loaded in running operating system B. Kernel remains in the memory during the entire computer session C. Kernel is the first part of os to loaded into memory during booting. D. Kernel is a program that constitutes the central core of os.	CO1	A
	<u>MCQ</u>	System calls are usually invoked by using: A. Polling B. An direct jump C. A priviledged instruction D. A software interrupt	CO1	D
	<u>MCQ</u>	Which of the following is not a function of Operating system? A. Process management B. Coordination between other software and users	CO1	C

		C. Anti virus protection D. Resource management														
	<u>MCQ</u>	Consider the following statements with respect to user-level threads and kernel-supported threads. I. Context switch is faster with kernel- supported threads. II. For user-level threads, a system call can block the entire process III. Kernel-supported threads can be scheduled independentl IV. Use-level threads are transparent to the kernel Which of the above statements are true? A. II,IIIonly B. II,III,IV only C. I and IV only D. All of the above	CO1	B												
<u>Q.No:2</u>	<u>MCQ</u>	Which scheduling policy is most suitable for a time-shared operating systems? <u>A.</u> First come first serve <u>B.</u> Shortest job first <u>C.</u> Priority scheduling <u>D.</u> Round robin	CO2	D												
	<u>MCQ</u>	Consider the following table of arrival time and burst time for three processes Po, P1 and P2. <table border="1"><tr><td>P Id</td><td>Arrival time</td><td>Burst time</td></tr><tr><td>Po</td><td>0ms</td><td>9ms</td></tr><tr><td>P1</td><td>1ms</td><td>4ms</td></tr><tr><td>P2</td><td>2ms</td><td>9ms</td></tr></table> The pre-emptive shortest job first scheduling algorithm is used. Scheduling is carried out only at arrival or completion of processes. What is the average waiting time for the three processes? A. 6ms B. 4ms C. 5ms D. 9ms	P Id	Arrival time	Burst time	Po	0ms	9ms	P1	1ms	4ms	P2	2ms	9ms	CO2	C
P Id	Arrival time	Burst time														
Po	0ms	9ms														
P1	1ms	4ms														
P2	2ms	9ms														
	<u>MCQ</u>	Which of the following statements are true? I. Shortest remaining time first scheduling may cause starvation II. Preemptive scheduling may cause	CO2	B												

		starvation III. Round robin is better than FCFS in terms of response time A. I and II B. I,II and III C. III only D. All of the above														
	MCQ	In which of the following scheduling criteria, context switching will never take place ? A. Priority B. Preemptive SJF C. Non preemprive SJF D. Round robin	CO2	C												
Q.No:3	MCQ	Consider the following set of processes, assumed to have arrived at time 0. Consider the CPU scheduling algorithms Shortest Job First (SJF) and Round Robin (RR). For RR, assume that the processes are scheduled in the order P ₁ , P ₂ , P ₃ , P ₄ . <table border="1"><tr><td>PId</td><td>Burst time</td></tr><tr><td>P1</td><td>8</td></tr><tr><td>P2</td><td>7</td></tr><tr><td>P3</td><td>2</td></tr><tr><td>P4</td><td>4</td></tr></table> If the time quantum for RR is 4 ms, then the absolute value of the difference between the average turnaround times (in ms) of SJF and RR (round off to one decimal places) is _____ A. 5.5ms B. 5.3ms C. 5.2ms D. 5.7ms	PId	Burst time	P1	8	P2	7	P3	2	P4	4	CO2	C		
PId	Burst time															
P1	8															
P2	7															
P3	2															
P4	4															
	MCQ	consider the 3 processes, S1, S2 and S3 shown in the table <table border="1"><tr><td>pid</td><td>Arrival time</td><td>Burst Time</td></tr><tr><td>S1</td><td>0</td><td>5</td></tr><tr><td>S2</td><td>1</td><td>7</td></tr><tr><td>S3</td><td>3</td><td>4</td></tr></table> The completion order of the 3 processes under the policies FCFS and RRS (round robin scheduling with CPU quantum of 2 time units) are	pid	Arrival time	Burst Time	S1	0	5	S2	1	7	S3	3	4	CO2	C
pid	Arrival time	Burst Time														
S1	0	5														
S2	1	7														
S3	3	4														

		(A) FCFS:S1,S2,S3 RR2: S1, S2, S3 (B) FCFS:S1,S3,S2 RR2: S1, S3, S2 (C) FCFS:S1,S2,S3 RR2: S1, S3, S2 (D) FCFS: S1, S3, S2 RR2: S1, S2, S3		
	MCQ	A thread is usually defined as a 'light weight process' because an operating system (OS) maintains smaller data structures for a thread than for a process. In relation to this, which of the followings is TRUE? A. On per-thread basis, the OS maintains only CPU register state B. The OS does not maintain a separate stack for each thread C. On per-thread basis, the OS does not maintain virtual memory state D. On per thread basis, the OS maintains only scheduling and accounting information.	CO3	C
	MCQ	Consider three processes (process id 0, 1, 2 respectively) with compute time bursts 2, 4 and 8 time units. All processes arrive at time zero. Consider the longest remaining time first (LRTF) scheduling algorithm. In LRTF ties are broken by giving priority to the process with the lowest process id. The average turn around time is: A. 12 B. 13 C. 14 D. 15	CO3	B
Q.No:4	MCQ	A counting semaphore was initialized to 10. Then 8 P (wait) operations and 6 V (signal) operations were completed on this semaphore. The resulting value of the semaphore A. 6 B. 6 C. 8 D. 12	CO4	C
	MCQ	When several processes access the same data concurrently and the outcome of the execution depends on	CO4	A

		the particular order in which the access takes place is called <u>A.</u> Race condition <u>B.</u> Data inconsistency <u>C.</u> essential condition <u>D.</u> critical condition		
	MCQ	If a process is executing in its critical section, then no other processes can be executing in their critical section. What is this condition called? A) mutual exclusion B) critical exclusion C) synchronous exclusion D) asynchronous exclusion	CO4	A
	MCQ	Which of the following for Mutual exclusion can be provided by the A. mutex lock B. binary semaphore C. mutex lock and binary semaphore <u>D.</u> none of the above	CO4	C
Q.No:5	MCQ	Consider a system having P resources of the same type. These resources are shared by 3 processes M, N and O, which have peak demands of 3, 4 and 6 respectively. For what value of P deadlock will not occur? <u>A.</u> 12 <u>B.</u> 13 <u>C.</u> 14 <u>D.</u> 15	CO5	B
	MCQ	Which of the following is NOT a necessary condition for deadlock to occur? A. Hold and wait B. Non preemption C. Bounded waiting D. Circular wait	CO5	C
	MCQ	Which of the following is NOT a valid deadlock prevention scheme? A. No of resources uniquely and never request a lowered number resources than the last one requested B. Releasing all acquired resources before requesting for new resource C. All requested and required resources will be allocated prior to execution	CO5	D

		D. Never request a new resource after releasing old resource		
	MCQ	<p>A system is in the safe state if</p> <hr/> <p>A. There exist a safe sequence B. the system can allocate resources to each process in some order and still avoid a deadlock C. all of the mentioned D. none of the mentioned</p>	CO5	B
Q.No:6	MCQ	<p>Let the page fault service time be 10ms in a computer with average memory access time being 20ns. If one page fault is generated for every 10^6 memory accesses, what is the effective access time for the memory?</p> <p>A. 35ns B. 23ns C. 30ns D. 10ns</p>	CO6	C
	MCQ	<p>Which page replacement policy sometimes leads to more page faults when size of memory is increased?</p> <p>A. LRU B. Optimal C. LIFO D. FIFO</p>	CO6	D
	MCQ	<p>Consider a system where main memory is 32 MB and length of logical address is 32 bit, if size of each page is 1 KB, find the number of offset bit.</p> <p>A. 10 B. 12 C. 14 D. 16</p>	CO6	A
	MCQ	<p>A process to collect all free block spaces to form a large memory chunk for allocating new process is called as</p> <p>A. External fragmentation B. Compaction C. Internal fragmentation D. Local fragmentation</p>	CO6	B
Q.No:7	MCQ	<p>A process refers to 5 pages, A, B, C, D, E in the order : A, B, C, D, A, B, E, A, B, C, D, E. If the page replacement algorithm is FIFO, the number of page transfers with an empty internal store of 3 frames is?</p> <p>A. 6</p>	CO6	D

		B. 7 C. 8 <u>D. 9</u>		
	MCQ	A virtual memory system uses First In First Out (FIFO) page replacement policy and allocates a fixed number of frames to a process. Consider the following statements. S1 :Increasing the number of page frames allocated to a process sometimes increases the page fault rate S2 :Some programs do not exhibit locality of reference <u>A.</u> Both S1 and S2 are true and S2 is the reason for S1 <u>B.</u> S1 is true and S2 is false <u>C.</u> S2 is true and S1 is false <u>D.</u> Both are false	CO6	B
	MCQ	In which of the disk scheduling algorithm,the disk head moves from one end to other end of the disk,serving the requests along the way.when the head reaches the other end,it immediately returns to the beginning of the disk without servicing any request on the return trip. A. C-LOOK B. LOOK C. SCAN D. C-SCAN	CO7	D
	MCQ	The content of the matrix Need in Bankers Algorithm can be evaluated as _____ <u>A.</u> MAX-ALLOCATION <u>B.</u> MAX-AVAILABLE <u>C.</u> ALLOCATION-MAX <u>D.</u> ALLOCATION-AVAILABLE		

SECTION-B(Answer Any Three Questions. Each Question carries 12 Marks)

Time: 1 Hour and 30 Minutes
(3×12=36 Marks)

<u>Question No</u>	<u>Question</u>	<u>CO Mapping (Each question should be from the same CO(s))</u>																																										
<u>Q.No: 8</u>	<p>Assume following processes arrive in the system with a single processor.</p> <table><tr><th>Process Id</th><th>Execution time</th><th>Arrival time</th><th>Priority no</th></tr><tr><td>P</td><td>55</td><td>0</td><td>4</td></tr><tr><td>Q</td><td>20</td><td>10</td><td>3</td></tr><tr><td>R</td><td>10</td><td>10</td><td>2</td></tr><tr><td>S</td><td>25</td><td>40</td><td>4</td></tr><tr><td>T</td><td>65</td><td>55</td><td>1</td></tr></table> <p>(a)What will be the average waiting time and average turn around time if Round Robin CPU scheduling algorithm is followed with a time quantum 15units? [5marks]</p> <p>(b)Considering the lowest number as the highest priority find the sequence of execution of processes along with individual waiting time of the processes for Preemptive priority Scheduling algorithm. [5marks]</p> <p>(c)Find the CPU utilization if context switch time is 2unit by following FCFS CPU scheduling algorithm(consider the arrival time given in the question) [2marks]</p> <p>Consider the following set of processes with length of CPU burst time is given in milliseconds</p> <table><tr><th>Process Id</th><th>Burst time</th><th>Arrival time</th></tr><tr><td>P0</td><td>3</td><td>0</td></tr><tr><td>P1</td><td>6</td><td>2</td></tr><tr><td>P2</td><td>4</td><td>4</td></tr><tr><td>P3</td><td>5</td><td>6</td></tr><tr><td>P4</td><td>2</td><td>8</td></tr></table> <p>(a) Draw the gantt charts to illustrate the execution of the following processes using Shortest remaining time first(SRTF) CPU scheduling algorithm.(i.e preemptive SJF) [5marks]</p> <p>(b) Draw the gantt charts to illustrate the execution of the following processes using Round Robin scheduling with a time slice 2ms. [5marks]</p> <p>(c) What will be the average turn around time (TAT)and average waiting time(WT) for both the scheduling</p>	Process Id	Execution time	Arrival time	Priority no	P	55	0	4	Q	20	10	3	R	10	10	2	S	25	40	4	T	65	55	1	Process Id	Burst time	Arrival time	P0	3	0	P1	6	2	P2	4	4	P3	5	6	P4	2	8	CO2
Process Id	Execution time	Arrival time	Priority no																																									
P	55	0	4																																									
Q	20	10	3																																									
R	10	10	2																																									
S	25	40	4																																									
T	65	55	1																																									
Process Id	Burst time	Arrival time																																										
P0	3	0																																										
P1	6	2																																										
P2	4	4																																										
P3	5	6																																										
P4	2	8																																										

	<p>algorithm and find the best algorithm for this problem.[2 marks]</p> <p>Consider the set of 6 processes whose arrival time and burst time are given below</p> <table><tr><th>Process Id</th><th>Arrival time</th><th>Burst time</th></tr><tr><td>P1</td><td>0</td><td>4</td></tr><tr><td>P2</td><td>1</td><td>5</td></tr><tr><td>P3</td><td>2</td><td>2</td></tr><tr><td>P4</td><td>3</td><td>1</td></tr><tr><td>P5</td><td>4</td><td>6</td></tr><tr><td>P6</td><td>6</td><td>3</td></tr></table> <p>(a) If the CPU scheduling policy is Round Robin with time quantum = 2, calculate the average waiting time and average turn around time [5marks]</p> <p>(b) AVG WT,AVG TAT,Response time for both preemptive and non preemptive SJF [5marks]</p> <p>(c) Calculate the CPU utilization if FCFS cpu scheduling algorithm is followed with a context switching time as 3ms.[2marks]</p>	Process Id	Arrival time	Burst time	P1	0	4	P2	1	5	P3	2	2	P4	3	1	P5	4	6	P6	6	3														
Process Id	Arrival time	Burst time																																		
P1	0	4																																		
P2	1	5																																		
P3	2	2																																		
P4	3	1																																		
P5	4	6																																		
P6	6	3																																		
<p>Q.No: 9</p>	<p>An operating system uses the Banker's algorithm for deadlock avoidance when managing the allocation of three resource types R₀, R₁, and R₂ to three processes P₀, P₁, and P₂. The snapshot of current system state is shown in the following table. The Allocation matrix shows the number of resources of each type already allocated to each process and the Max matrix shows the maximum number of resources of each type required by each process during its execution.</p> <table><tr><th rowspan="2">Proces s</th><th colspan="3">Max</th><th colspan="3">Allocation</th></tr><tr><th>R₀</th><th>R₁</th><th>R₂</th><th>R₀</th><th>R₁</th><th>R₂</th></tr><tr><td>P₀</td><td>7</td><td>3</td><td>3</td><td>1</td><td>0</td><td>1</td></tr><tr><td>P₁</td><td>5</td><td>2</td><td>1</td><td>2</td><td>2</td><td>1</td></tr><tr><td>P₂</td><td>6</td><td>3</td><td>3</td><td>2</td><td>1</td><td>1</td></tr></table> <p>There are 3 instances of resource type R₀, 2 instances of resource type R₁ and 2 instances of resource type R₃ still available in the system. Check the system is currently in a safe state. Find out</p>	Proces s	Max			Allocation			R ₀	R ₁	R ₂	R ₀	R ₁	R ₂	P ₀	7	3	3	1	0	1	P ₁	5	2	1	2	2	1	P ₂	6	3	3	2	1	1	<p>CO5</p>
Proces s	Max			Allocation																																
	R ₀	R ₁	R ₂	R ₀	R ₁	R ₂																														
P ₀	7	3	3	1	0	1																														
P ₁	5	2	1	2	2	1																														
P ₂	6	3	3	2	1	1																														

	<p>which process will complete its execution at end?[10 marks]</p> <p>(b) What is the difference between RAG and WFG? [2 marks]</p> <table><tr><th>Processes</th><th>Allocation A B C</th><th>Max A B C</th><th>Available A B C</th></tr><tr><td>P0</td><td>1 1 2</td><td>4 3 3</td><td>2 1 0</td></tr><tr><td>P1</td><td>2 1 2</td><td>3 2 2</td><td></td></tr><tr><td>P2</td><td>4 0 1</td><td>9 0 2</td><td></td></tr><tr><td>P3</td><td>0 2 0</td><td>7 5 3</td><td></td></tr><tr><td>P4</td><td>1 1 2</td><td>1 1 2</td><td></td></tr></table> <p>(a) Calculate the content of the need matrix? (b) Check if the system is in a safe state? (c) Determine the total sum of each type of resource? [10 marks]</p> <p>(b) What is the difference between safe state and unsafe state?[2 mark]</p> <p>(a) Write the necessary conditions for deadlock. [4 mark]</p> <p>(b) Considering a system with five processes P₀ through P₄ and three resources of type A, B, C. Resource type A has 10 instances, B has 5 instances and type C has 7 instances. Suppose at time t₀ following snapshot of the system has been taken:</p> <table><tr><th rowspan="2">Process</th><th>Allocation</th><th>Max</th><th rowspan="2"></th></tr><tr><th>A B C</th><th>A B C</th></tr><tr><td>P₀</td><td>0 1 0</td><td>7 5 3</td><td rowspan="5"></td></tr><tr><td>P₁</td><td>2 0 0</td><td>3 2 2</td></tr><tr><td>P₂</td><td>3 0 2</td><td>9 0 2</td></tr><tr><td>P₃</td><td>2 1 1</td><td>2 2 2</td></tr><tr><td>P₄</td><td>0 0 2</td><td>4 3 3</td></tr></table> <p>1. What will be the content of the Need matrix? 2. Is the system in a safe state? If Yes, then what is the safe sequence? [8 marks]</p>	Processes	Allocation A B C	Max A B C	Available A B C	P0	1 1 2	4 3 3	2 1 0	P1	2 1 2	3 2 2		P2	4 0 1	9 0 2		P3	0 2 0	7 5 3		P4	1 1 2	1 1 2		Process	Allocation	Max		A B C	A B C	P ₀	0 1 0	7 5 3		P ₁	2 0 0	3 2 2	P ₂	3 0 2	9 0 2	P ₃	2 1 1	2 2 2	P ₄	0 0 2	4 3 3	
Processes	Allocation A B C	Max A B C	Available A B C																																													
P0	1 1 2	4 3 3	2 1 0																																													
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P3	0 2 0	7 5 3																																														
P4	1 1 2	1 1 2																																														
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P ₀	0 1 0	7 5 3																																														
P ₁	2 0 0	3 2 2																																														
P ₂	3 0 2	9 0 2																																														
P ₃	2 1 1	2 2 2																																														
P ₄	0 0 2	4 3 3																																														
Q.No: 10	<p>(a)Given four memory partitions of 100K, 200K , 300K and 200K size(in order) and three processes of 174K, 212K and 190K(in order). In which partition the 190K process will be loaded if the allocation policy is Best fit?</p>	CO6																																														

	<p>[4marks]</p> <p>(b) Consider a system with byte-addressable memory, 32-bit logical addresses, 4KB page size and page table entries of 4bytes each. What would be the size of page map table in the system?</p> <p>[6marks]</p> <p>© Explain how internal fragmentation is different from external fragmentation</p> <p>[2 marks]</p>																			
	<p>a)If the virtual address space is represented by 32 bits, the page size is 4Kbyte, the size of the physical memory is 64Mbyte and only 2 bits are used as control bits, calculate the size of the page table</p> <p>[6marks]</p> <p>b) In the memory four partitions are there of size 4KB, 8KB, 20KB and 2KB (in order) respectively. Total 5 processes arrive at time 0 with memory request size(in Bytes) and usage time (in ms) as given in the following table: Calculate the time at which process P5 will be completed if the Best fit method is used for fixed sized partitioned memory.</p> <table border="1"><tr><td>Request no</td><td>P1</td><td>P2</td><td>P3</td><td>P4</td><td>P5</td></tr><tr><td>Request size</td><td>2K</td><td>12K</td><td>3K</td><td>6K</td><td>10K</td></tr><tr><td>Usage time</td><td>4</td><td>10</td><td>2</td><td>8</td><td>4</td></tr></table> <p>[6 marks]</p>	Request no	P1	P2	P3	P4	P5	Request size	2K	12K	3K	6K	10K	Usage time	4	10	2	8	4	
Request no	P1	P2	P3	P4	P5															
Request size	2K	12K	3K	6K	10K															
Usage time	4	10	2	8	4															
	<p>(a)Consider a logical address space of 8pages of 1024words in each mapped on to a physical memory of 32 frames. How many numbers of bits are needed for logical address?</p> <p>[4 marks]</p> <p>(b) The main memory has been divided into fixed size partitions as-</p> <table border="1"><tr><td>200KB</td><td>400KB</td><td>600KB</td><td>500KB</td><td>300KB</td><td>200KB</td></tr></table> <p>If there 4 processes(P1,P2,P3,P4) request for the size 357KB,210KB,468KB,491KB.Discuss the allocation of processes following First fit and best fit Algorithm</p> <p>[6marks]</p> <p>© What do you mean by demand paging in virtual memory?</p> <p>[2marks]</p>	200KB	400KB	600KB	500KB	300KB	200KB													
200KB	400KB	600KB	500KB	300KB	200KB															
Q.No: 11	<p>(a) Consider the page references 7, 0, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, with 4 page frame. Find number of page fault through LRU and optimal page replacement algorithm.</p> <p>[6 marks]</p> <p>(b)Consider a disk queue with requests for I/O to blocks on cylinders 98, 183, 41, 122, 14, 124, 65, 67. The FCFS scheduling algorithm is used. The head is initially at cylinder number 53. The cylinders are numbered from 0 to 199. Calculate the total head movement (in number of cylinders) incurred while servicing these requests.</p> <p>[6 marks]</p>	CO7																		

	<p>(a) Suppose the time to service a page fault is on the average 10 milliseconds, while a memory access takes 2 microseconds. Then, what is the result of average memory access time of-a 99.99% hit ratio?</p> <p>[4 marks]</p> <p>(b) Consider a disk with 200 tracks and the queue has random requests from different processes in the order: 55, 58, 39, 18, 90, 160, 150, 38, 184. Initially arm is at 100. Find the Average Seek length using SCAN and C-SCAN algorithm</p> <p>[8 marks]</p>	
	<p>(a) Consider the page reference string of size 12: 1, 2, 3, 4, 5, 1, 3, 1, 6, 3, 2, 3 with frame size 4 (i.e. maximum 4 pages in a frame). Calculate total no of page fault and page fault ration for FIFO and LRU cpu scheduling algorithm.</p> <p>[4 mark]</p> <p>(b) Consider a disk with 200 tracks and the queue has random requests from different processes in the order: 55, 58, 39, 18, 90, 160, 150, 38, 184. Initially arm is at 100. Find the Average Seek length using LOOK and C-LOOK algorithm. [8 mark]</p>	