

control

## Program Control Instructions

Name: mnemonic

Branch	BR
Jump	JMP
Skip	SKIPI
Call	CALL
Return	RET

Compare

(Subroutine) COMPARE

- Test.
- Testbit.

Call: is used to call a subroutine

In high level called Function, In assembly language called subroutine.

1000 : Call PL

1004 : Next Instruction.

1. stack[top]  $\leftarrow$  [PC] // Retain address  
i.e 1004 is stored into the stack  
and then
2. PC  $\leftarrow$  Address of the subroutine //  
hence it is represented by P1

Return : It is used to return from  
a subroutine.

- . PC  $\leftarrow$  stack[top] // Retain address  
i.e 1004 is restored from the  
stack.

Compare : It is used to compare two  
numbers

~~Compare sub =  
operation.~~

- Compare Src, dst  
performing the operation: [dst] - [src]

- Sets the condition code flags based  
on the result obtained
- Neither of the operands is changed.

example CMP # 10, P1  
BR  $\neq$  0 L1

Test : Test instruction is used to check  
a particular bit position value of  
the operand.

Test # bit position, operand.

performs non-destructive AND operation. (Flag register affected)

Test #2, Ri

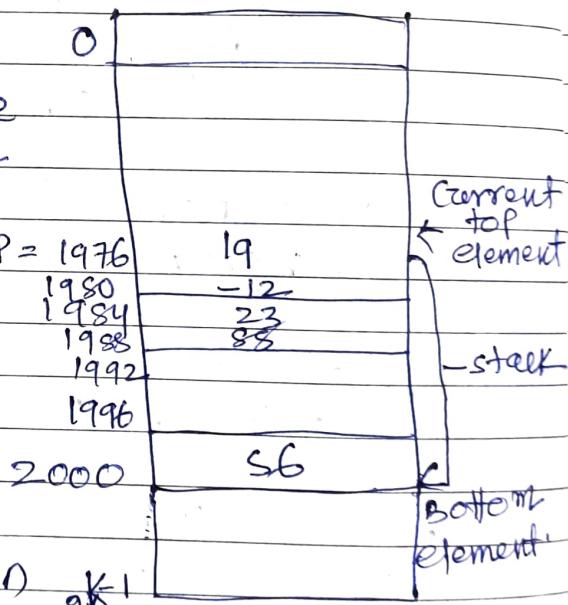
- Neither of the operands is changed.

## (STACK.)

Assumption: The machine is byte addressable and each element in the stack occupies 4 Bytes]

Push operation:

- SP always points to the top element, so to push a new item, first it has to be updated to the location, where a new element can be pushed.



- As stack grows downward in the memory, so first SP is decremented, then the NEWITEM is pushed.

Note: This rule is applicable for pushing any (all) element into the stack.

Subtract #4, SP  
MOVE NEWITEM, -(SP)

### Pop operation.

- SP always points to the top element so to pop an element, first, it has to be popped off, then the SP needs to be updated to the next top element
- As stack grows downward in the memory, so SP is incremented, after the top item is popped off.

MOV (SP), ITEM  
ADD #, SP ] → Move (SP)+, ITEM

Stack [ Assumption: the stack is from 2000 to 1500 ]

### Safe Push operation.

SAFEPUSH Compare #1500, SP  
Branch  $\leq 0$  FULLERROR  
MOVE NEWITEM, -(SP)

In a full stack, Push operation should not be done. So if the value of SP is 1500 or less than 1500, that means already the stack is full. Hence compare operation gives either 0 or a negative value after the operation. If SP is 1500, then next Push operation will be

done at 1996, which is not the part of the stack.

### Safepop operation

Safepop compare #2000, SP  
Branch > 0 Emptyerror

From a empty stack, no element should be popped out. So, if the value of SP is greater than 2000, that mean already the stack is empty. Hence compare operation gives a +ve value after the operation.

### (Subroutines.)

The way in which a computer makes it possible to call and return from subroutines is referred to as subroutine linkage method.

### Linkage Using Link Register -

On call

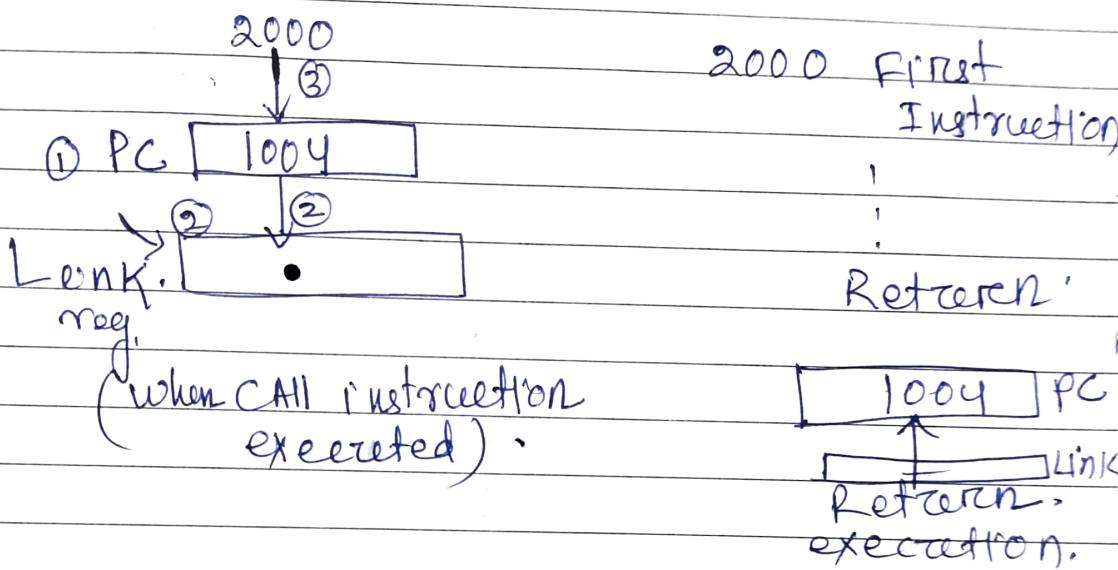
1. Store the content of the PC in the link register.
2. Branch to the target address specified by the instruction.

## On returning from a subroutine

- Branch to the address contained in the link register.

1000 CALL sum,

1004 Next Instruction.



Limitations ??

No support for nesting of functions.

Limitations of Link Register

1020 call p

p

1024 Next Inst.

2000 Inst.

3000 1st Inst

2040 call q

3050 Return

2044 Next Inst.

2070 Return.

Note

There is a single Link register.

2044 over  
1024 - 1st  
Link register

As a summary we can conclude that using link register if it's not possible to support subroutine nesting.

But calling a single subroutine Link register is OK.

Solution: Using stack.

Same Example:

①

1020 call P

1024 Next Instr?

③ Q

3000 1st instr?

② P

3050 Return.

2000 1st Instr?

2040 call Q

2044 Next Instr?

2070 Return

popped

the top

of stack

element

to PC

4992	2044
4996	1024
5000	20

PC = 2044

Initially Present

LIFO  
order

# Stack as Subroutine Linkage Method.

Using stack: calling a function and returning from it.

Assumptions.

- Parameters are passed through general purpose registers.
- Returning values through general purpose registers.

Example :

We are going to add N numbers stored in consecutive memory locations starting from the symbolic address NUM1 using a function. The function is going to return the summation result to the caller.

Stack

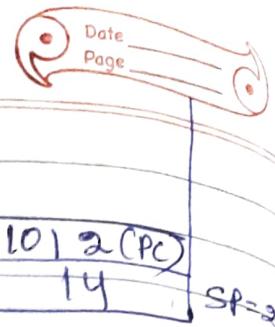
R2	3000	3001	11
R1	10	3000	12
		3004	21

10 data

Calling Program:

```

1000 MOVE N,R1 ; N = counter
1004 MOVE #NUM1,R2.
1008 CALL LISTADD
1012 MOVE R0,SUM
    
```



Subroutine :

LI ST ADD      Clear R<sub>0</sub>  
 LOOP      ADD (R<sub>2</sub>)<sup>t</sup>, R<sub>0</sub> ; [R<sub>0</sub>] = 0 + 12  
                 Decrement R<sub>1</sub>  
                 Branch >0 Loop  
                 Retcn ; PC ← 1012

The result is at R<sub>0</sub>.  
 we are moving here to location [Bem].

## Numerical

(1) The content of the top memory stack is 2452. The content of SP is 1258. A two byte call subroutine instruction is located in memory address 81456 followed by address field of 5490 at location 1457. What are the content of PC, SP and top of stack

- (1) Before call instruction execution
- (2) After call instruction execution
- (3) After return from subroutine

Ans

Given [1456] CALL

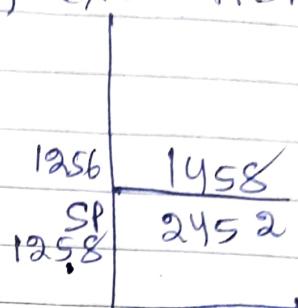
[1457] Addr = 5490

Before call instruction execution.

(i) [PC] = 1456

[SP] = 1258

Top of stack (STACK[SP])  
= 2452,



(ii) After call instruction execution.

Return address will be pushed onto the stack and PC will be loaded with the value of the address field of the call instruction.

[PC] = 5490

[SP] = 1256

STACK[SP] = ~~2452~~, 1458

(iii) After Return from Subroutine.

Top stack content pointed by SP will be popped into PC to return back to the caller

[PC] = 1458

[SP] = 1258

STACK[SP] = 2452

- Q. How many times a subroutine should be called so that the stack becomes full. Assume that the stack address space ranges from 2000 to 1600 and each stack word consumes 4 bytes and machine is byte addressable. [Note No Parameter return value, registers, local variables are stored in the stack due to subroutine call.]

Solution:

2000 to 1600: no of memory locations = 400

- Each word on the stack occupies 4 Bytes, and the machine is byte addressable, meaning is that each byte will occupy one location.
- Each word in the stack occupies 4 locations.
- So in 400 locations,  
 $400/4 = 100$  words can be stored.
- So if we call a subroutine 100 number of times, the stack will become full.

3. Given the following Program fragment.

Main Program	SUBL	SUB2
2000 ADD R1,R2	3000 MOV R1,R2	4000 SUB R6,R1
2004 XOR R3,R4	3004 ADD R5,R1	4008 XOR R1,R5
2008 CALL SUB1	3008 CALL SUB2	4012 RETURN
2012 SUB R4,R5	3012 Return.	

Initially the stack pointer PS containing 5000.  
What are the content of PC, SP, and the top of the stack?

- After the subroutine call instruction is executed in the main program?
- After the subroutine call instruction is executed in the subroutine SUB1?
- After the return from SUB2 subroutine?

Solution:

(i) After the Subroutine call instr' is executed -

- Given, [2008] CALL SUB1  
Given [2012] Next instr'.

- After call instruction will be pushed into the stack and PC will be loaded with the value of the address field of the call instr'.

$$[PC] = 3000$$

$$[SP] = 4996$$

SP=4992	3012
SP=4996	2012
5000	some value

STACK[SP] = 2012

(ii) After the Subroutine call instruction is executed in the subroutine SUB1?

Given [3008] CALL SUB2

[3012] Next Instr

After call instruction execution ID SUB1  
Return address will be pushed onto the stack and PC will be loaded with the value of the address field of the calling

[PC] = 4000

[SP] = 4999

STACK[SP] = 3012

(iii) After the return from SUB2 subroutine?

Given, [4012] RETURN

[PC] - 3012

After the return from SUB2 subroutine  
Top stack content pointed by SP will be popped into PC to return back to the caller.

[SP] = 4996

STACK[SP] = 2012

The content of the top of the memory stack is 5000. The content of the stack pointer SP is 3000. Assume you want to organize a nested subroutine call on a computer as follows:

The routine main calls a subroutine SUB1 by executing a two-word call & subroutine instruction located in memory at address 1000 followed by the address of 6000 at location 1001. Again subroutine SUB1 calls another subroutine SUB2 by executing a two-word call & subroutine instruction located in memory at address 6050 followed by the address field of 8000 at location 6051. What are the content of PC, SP and the top of the stack?

### Solution

(i) After the Subroutine call instruction is executed in the main routine?

Given the following program fragment.

Main Program.

1000 CALL SUB1(6000)  
1002 Next instr'

SUB1(6000)

6050 CALL SUB2(8000)

6052 Next instr'

6080 Return

SUB2

8000 1st Inst?

!

8080 Return

Initially the stack pointer contains 3000

Given [1000] CALL SUB1  
[1002] Next instr?

(i) [PC] = 6000  
[SP] = 2998  
STACK [SP] = 1002.

SP=2998	6052
SP=2998	1002
SP=3000	5000

(ii) After the subroutine call instr is executed in the subroutine SUB1  
Given [6050] CALL SUB2  
[6052] Next Instr?

[PC] = 8000  
[SP] = 2996  
STACK [SP] = 6052.

(iii) After the return from SUB2 Subroutine?

Given, [8080] RETURN

[PC] = 6052  
[SP] = 2998  
STACK [SP] = 1002.

Page

Stack as Siebroeffine Linkage Method  
 [ Parameter Passing and Retracing  
 Values using stack ]

When a large number of parameters are required to pass to a function, we may not have that many general purpose registers.

Solution -

- Parameters are passed onto the task before calling the function.
- Retracing values through the stack.

Calling Program

Name of the function :

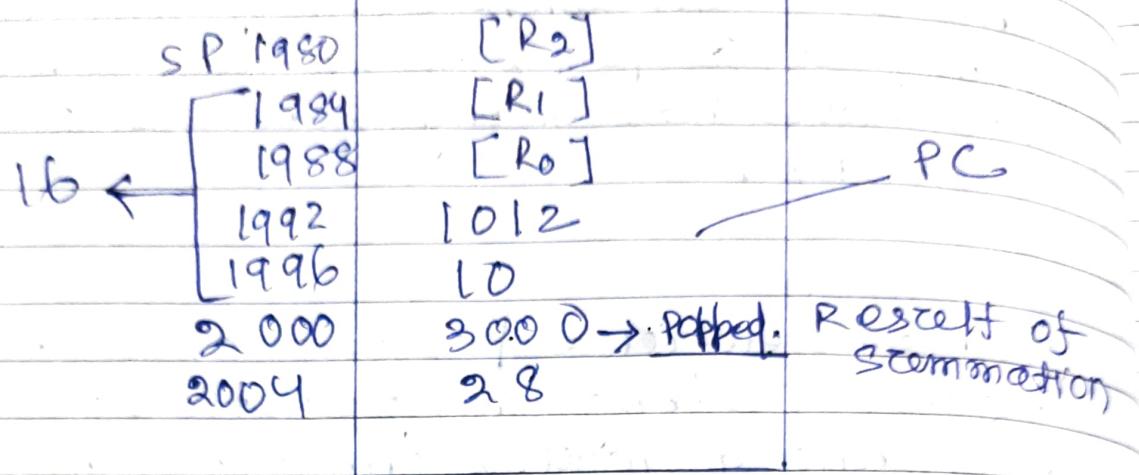
```

1000 MOVE #NUM1, -(SP)
1004 MOVE N, -(SP)
1008 Call Listadd
1012 MOVE 4(SP), R0
2016 ADD #8, SP
    
```

1992	1012 (Ret. addr)
1996	10counter(N)
2000	3000 (Baseaddr)
2004	28 (NUM1)

Siebroeffine

<u>Listadd</u> Move 16(SP), R1 Move 20(SP), R2 CLEAR R0 Loop ADD (R2)+, R0 Decrement R1 Branch >0 Loop Move R0, 20(SP)	MOVE Multiple R0-R2, -(SP) $\leftarrow R_1 = 10$ $\leftarrow R_2 = 3000$
---	--



MOVEMENT (S.P)+, R<sub>0</sub>-R<sub>2</sub>  
RETURN.

### Example - 2

When a large number of Parameters are required to pass to a function, we may not have that many general purpose registers.

If Register R<sub>i</sub> is used in a program to point to the top of a stack. Assume that each stack word consumes 4 bytes and machine is byte addressable. Write a sequence of instructions using the Index, Autoincrement and Autodecrement addressing modes to perform

each of the following tasks:

- (a) Pop the top two items off the stack, add them, and then push the result into the task.
- (b) Copy the fourth item from the top into the register R<sub>2</sub>.
- (c) Remove the top five items from the stack.

Solution:-

a) Move (R<sub>1</sub>)<sup>+</sup>, R<sub>5</sub>  
 ADD (R<sub>1</sub>)<sup>+</sup>, R<sub>5</sub>  
 MOVE R<sub>5</sub>, -(R<sub>1</sub>)

b) Move 16(R<sub>1</sub>), R<sub>2</sub>

c) ADD #20, R<sub>1</sub>