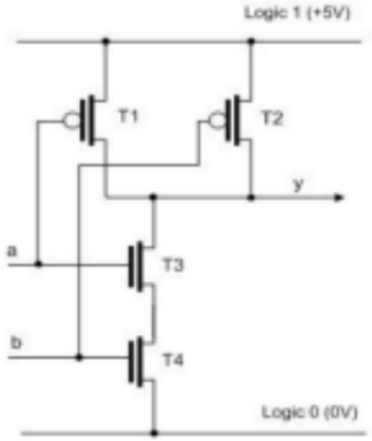
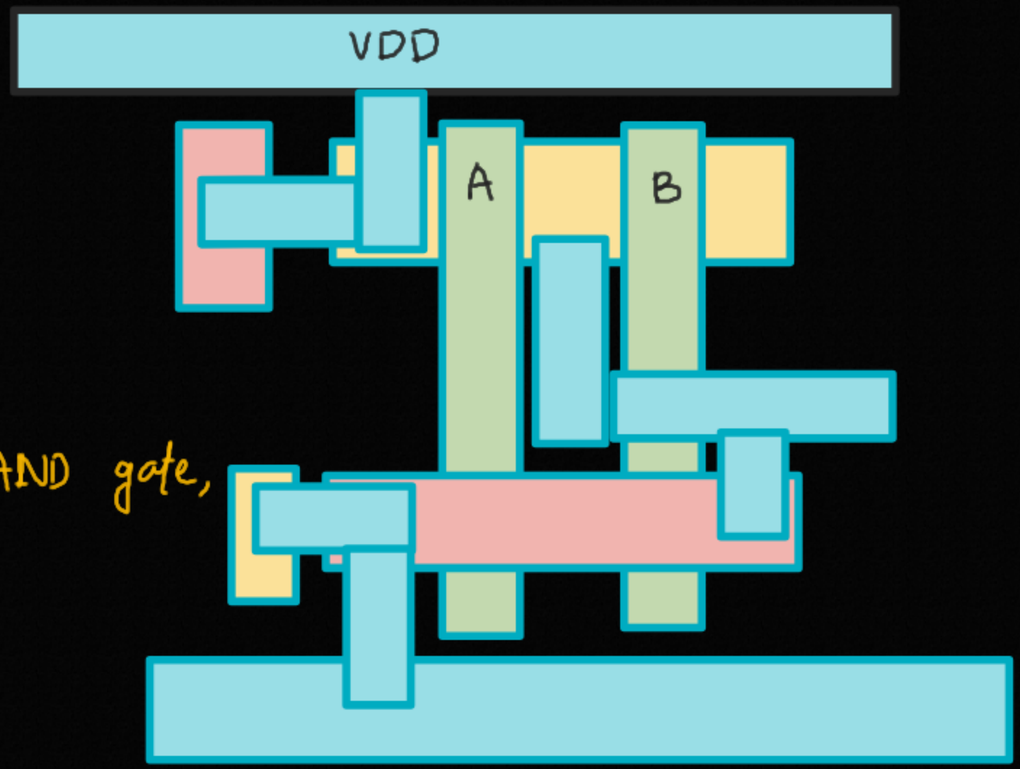
Draw the Stick Diagram and the Layout for the NAND Gate Below

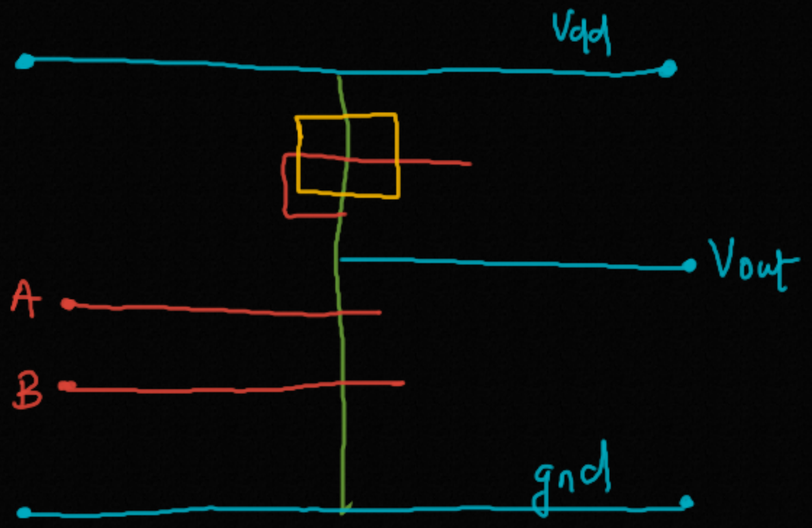


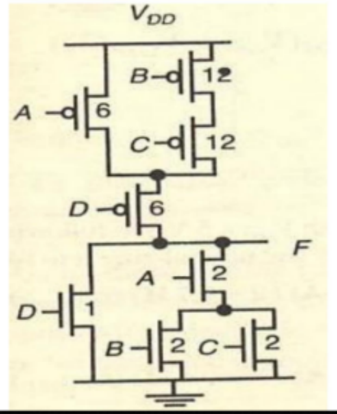
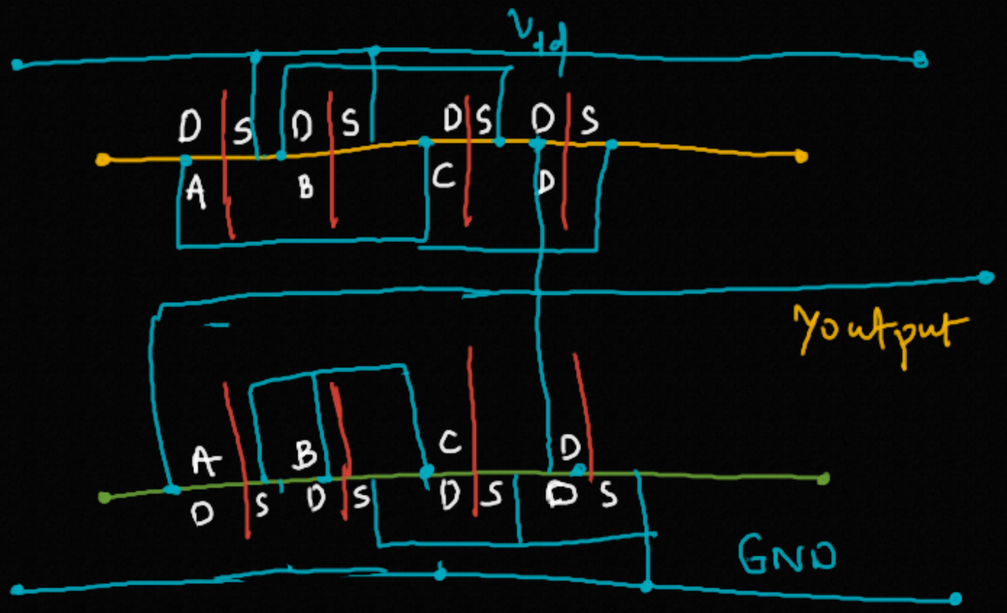
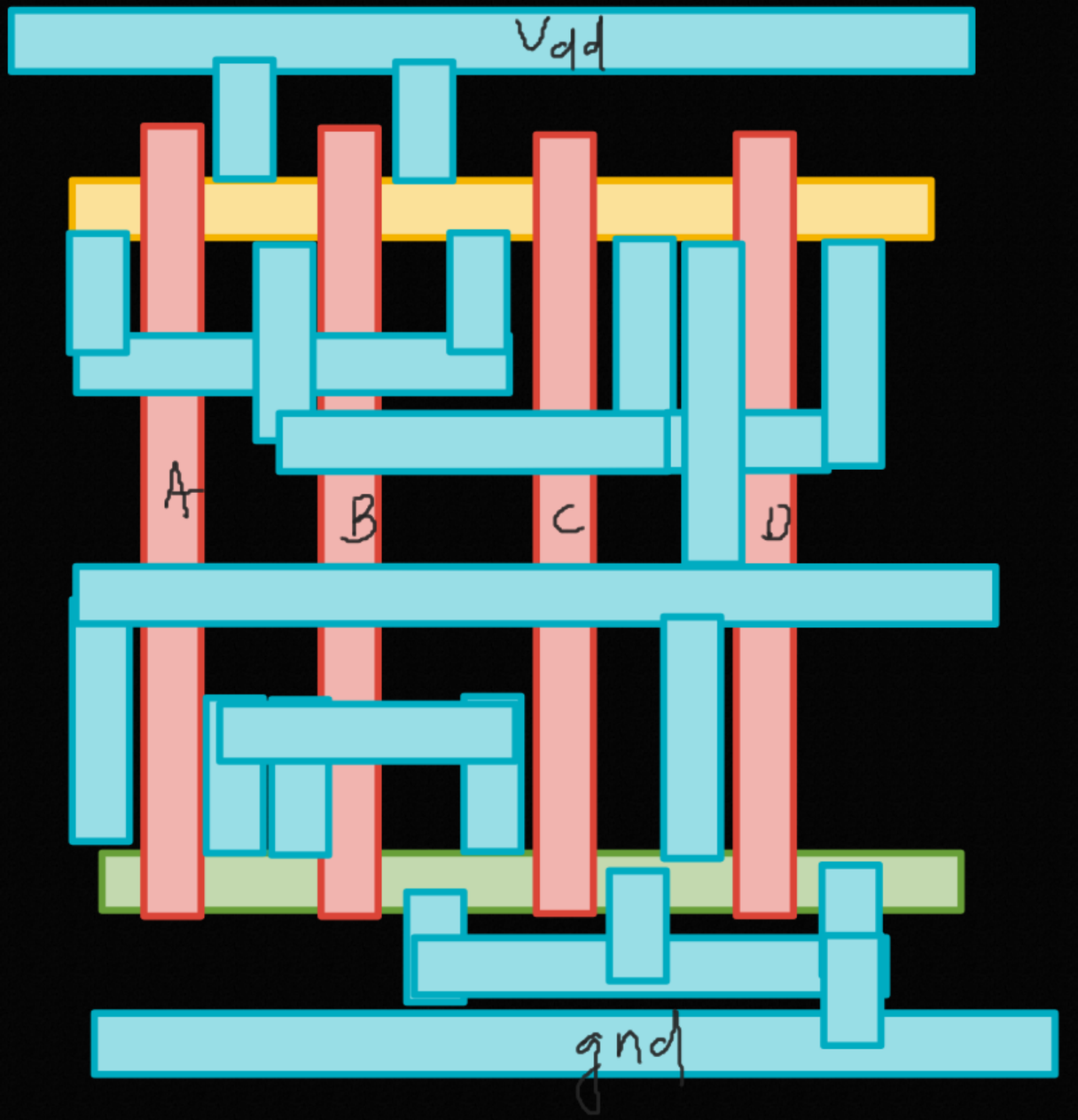
NAND

a	b	y
0	0	1
0	1	1
1	0	1
1	1	0



Solution Stick diagram for 2-input NAND gate,



(Stick Diagram)

Draw the Stick Diagram and the Layout for the Complex Logic Gate Below