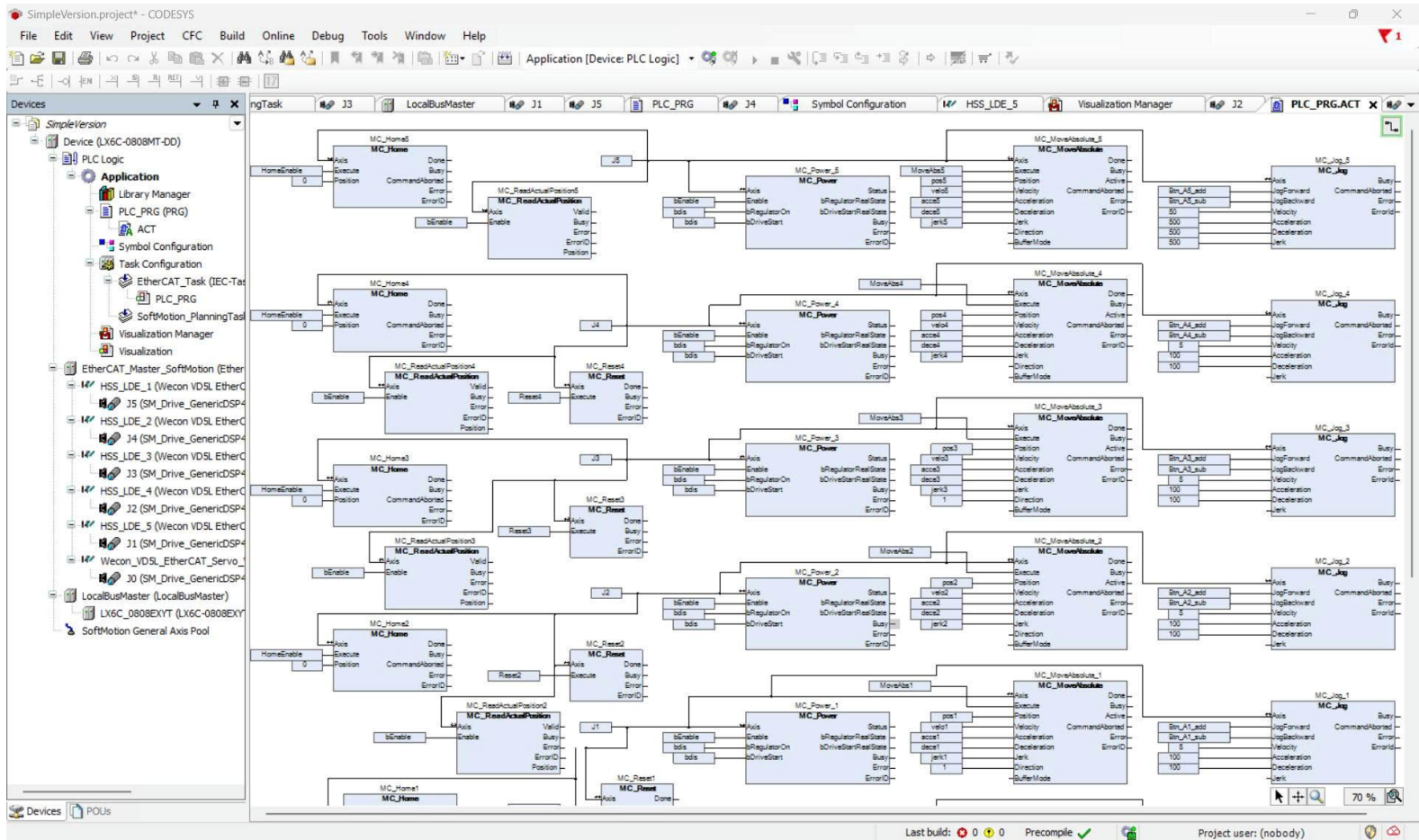


## FDB program:



## ST program:

The screenshot displays the CODESYS IDE interface for a project named "SimpleVersion.project". The main editor window shows an ST program for a PLC, with the following code:

```
1 PROGRAM PLC_PRG
2 VAR
3     MC_Power_0: MC_Power;
4     MC_MoveAbsolute_0: MC_MoveAbsolute;
5     MC_Jog_0: MC_Jog;
6     MC_Power_1: MC_Power;
7     MC_MoveAbsolute_1: MC_MoveAbsolute;
8     MC_Jog_1: MC_Jog;
9     MC_Power_2: MC_Power;
10    MC_MoveAbsolute_2: MC_MoveAbsolute;
11    MC_Jog_2: MC_Jog;
12    MC_Power_3: MC_Power;
13    MC_MoveAbsolute_3: MC_MoveAbsolute;
14    MC_Jog_3: MC_Jog;
15    MC_Power_4: MC_Power;
16    MC_MoveAbsolute_4: MC_MoveAbsolute;
17    MC_Jog_4: MC_Jog;
18    MC_Power_5: MC_Power;
19    MC_MoveAbsolute_5: MC_MoveAbsolute;
20    MC_Jog_5: MC_Jog;
21    A5_pos: LREAL;
22
23
24
25
```

The program continues with the following logic:

```
1 ACT();
2
3 TON_1 (IN:=xIn, PT:=T#1000MS, Q=>xOut);
4 TON_2 (IN:=xIn2, PT:=T#1000MS, Q=>xOut2);
5
6 IF Control1 THEN
7     Break1 := TRUE;
8 ELSE
9     Break1 := FALSE;
10 END_IF
11
12 IF Control2 THEN
13     Gripper := TRUE;
14 ELSE
15     Gripper := FALSE;
16 END_IF
17
18 IF autoR THEN
19     PLC_PRG.irun_step:=1;
20 END_IF
21
22 IF auto2R THEN
23     PLC_PRG.irun_step_new:=1;
24 END_IF
25
```

The status bar at the bottom indicates "Last build: 0 0 0", "Precompile" status, and "Project user: (nobody)".

Visualization:

