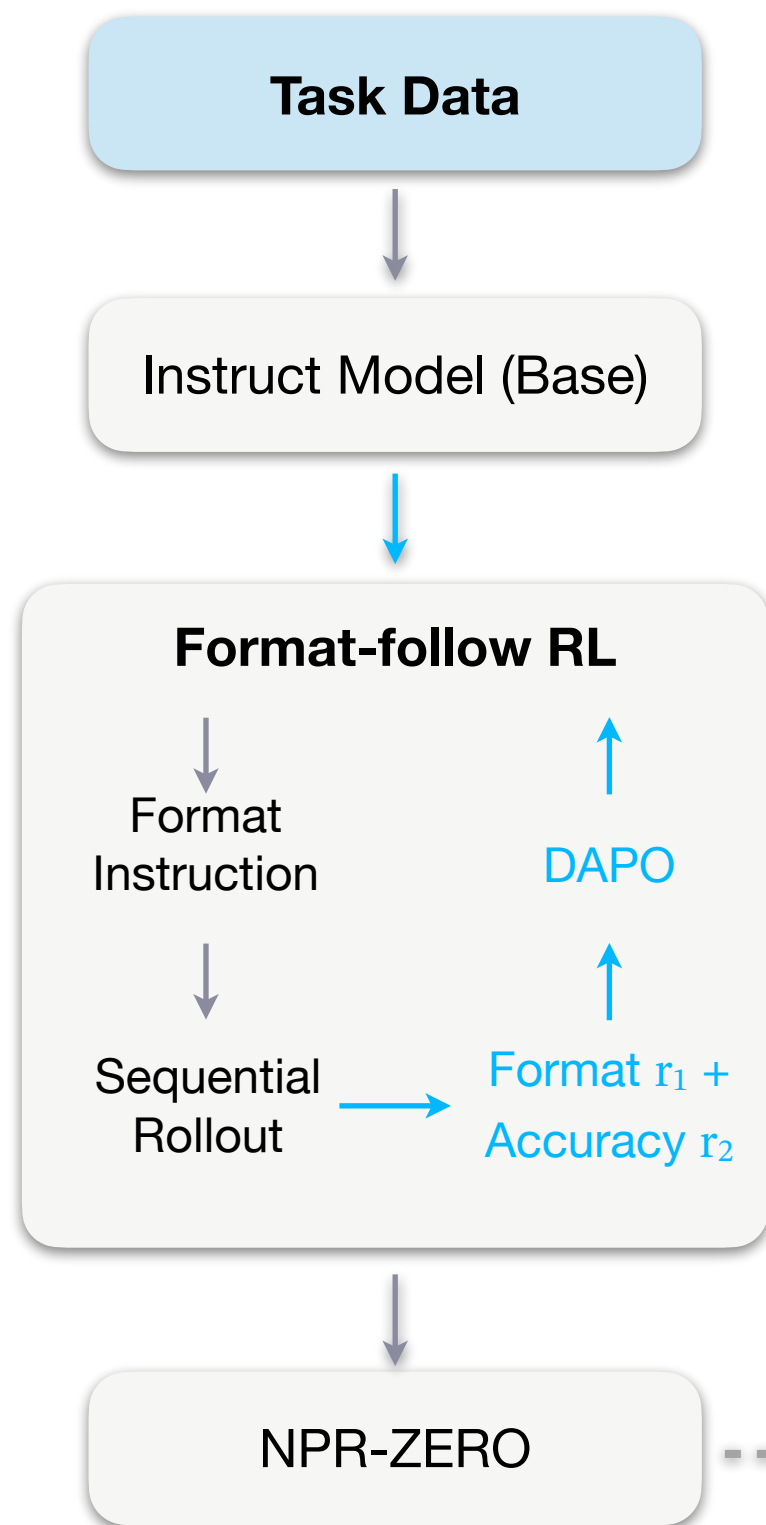


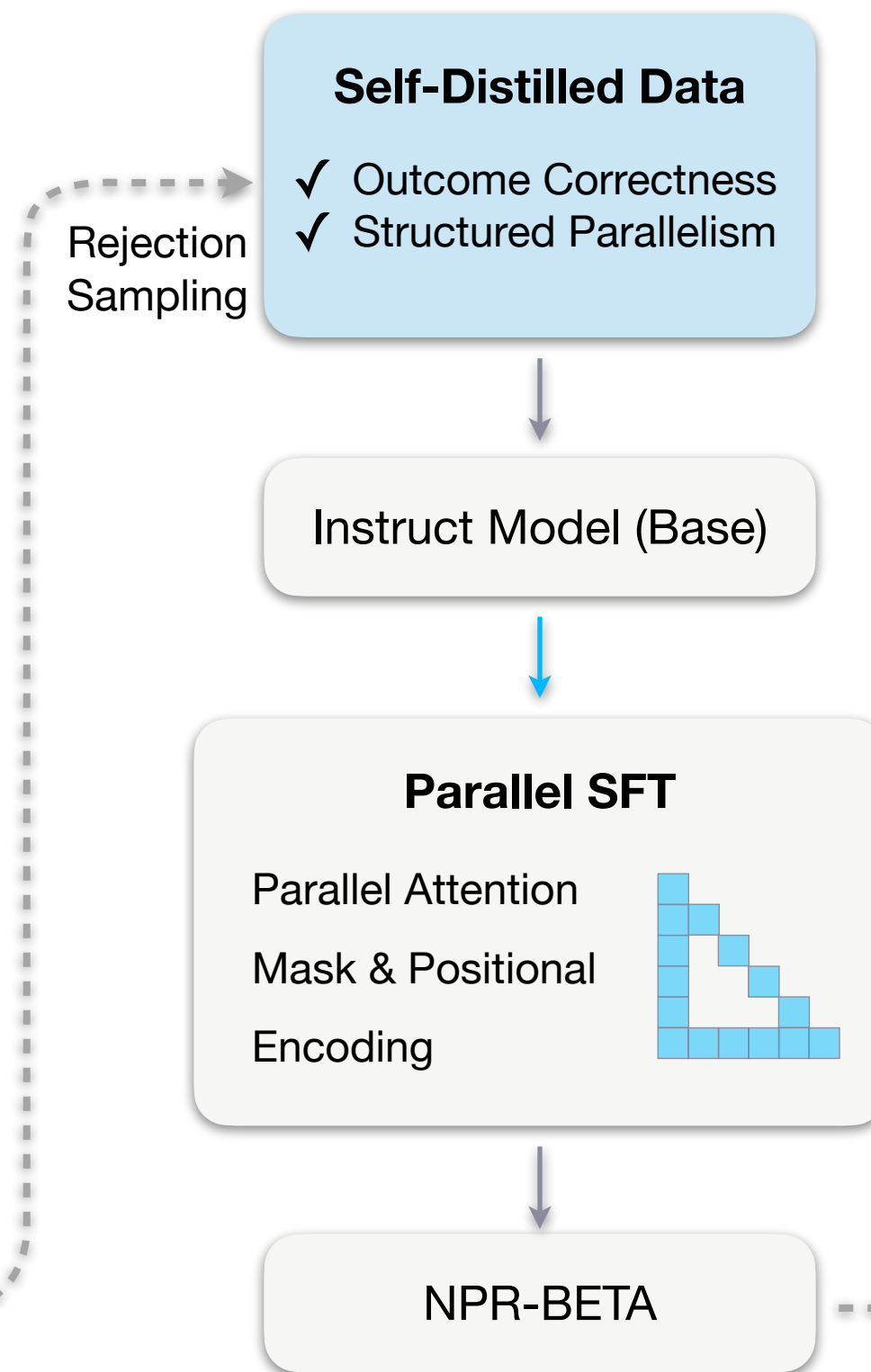
Stage 1

Using RL to induce structured parallel format without annotations.



Stage 2

Supervised fine-tuning on self-distilled parallel trajectories.



Stage 3

Using parallel-aware RL to directly enhance native parallel reasoning.

