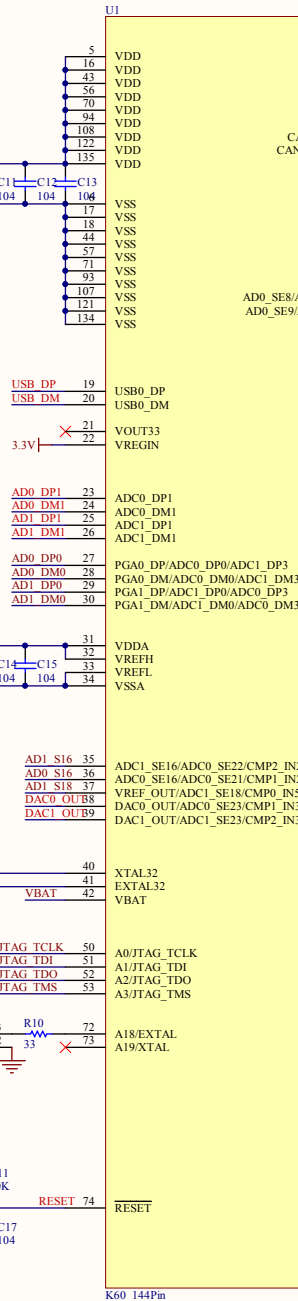
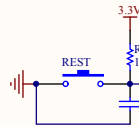
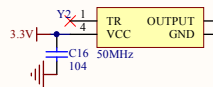
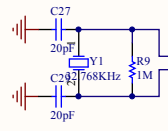
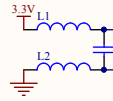
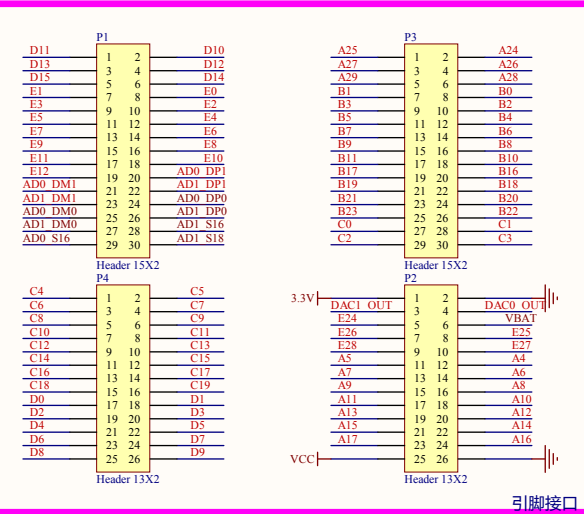
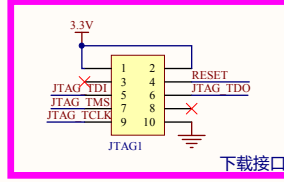
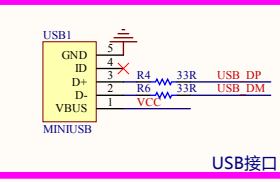
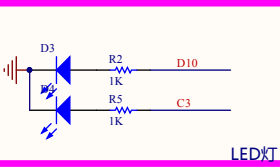
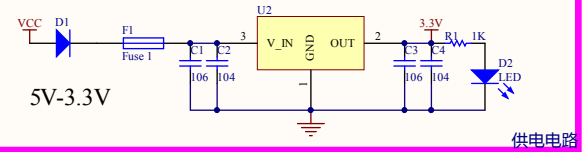


超核电子科技



K60 144Pin

	TS0 CH5/FTM0_CH1/A4	54	A4
	CMP2_OUT/TS0_RX_BCLK/RMIO_RX/RXTM0_CH2/A5	55	A5
	FTM0_CH3/A6	58	A6
	ADC0_SE10/FTM0_CH4/A7	59	A7
	ADC0_SE11/FTM1_CH0/FTM1_QD_PHA/A8	60	A8
	FTM1_CH1/FTM1_QD_PHB/A8	61	A9
	FTM2_CH0/FTM2_QD_PHA/A10	62	A10
	FTM2_CH1/FTM2_QD_PHB/A11	63	A11
	CAN0_TX/FTM1_CH0/CMP2_IN0/RMIO_RX/D1/TS0_TX/FTM1_QD_PHA/A12	64	A12
	CAN0_RX/FTM1_CH1/CMP2_IN1/RMIO_RX/D0/TS0_TX_FS/FTM1_QD_PHB/A13	65	A13
	SP10_SCK/UART0_RX/RMIO_TXEN/A15	66	A14
	SP10_SOUT/RMIO_TX/D0/A16	67	A15
	ADC1_SE17/SP10_SIN/RMIO_TX/D0/A17	68	A16
	FB_A29/A24	75	A24
	FB_A28/A25	76	A25
	FB_A27/A26	77	A26
	FB_A26/A27	78	A27
	FB_A25/A28	79	A28
	FB_A24/A29	80	A29
	AD0_SE8/ADC1_SE8/TS10_CH0/I2C0_SCL/FTM1_CH0/FTM1_QD_PHA/RMIO_MD0/B1	81	B0
	AD0_SE9/ADC1_SE9/TS10_CH1/I2C0_SDA/FTM1_CH1/FTM1_QD_PHB/RMIO_MD0/B1	82	B1
	ADC0_SE12/TS10_CH7/I2C0_SCL/B2	83	B2
	ADC0_SE13/TS10_CH8/I2C0_SDA/B3	84	B3
	ADC1_SE10/B4	85	B4
	ADC1_SE11/B5	86	B5
	ADC1_SE12/FB_AD23/B6	87	B6
	ADC1_SE13/FB_AD22/B7	88	B7
	FB_AD21/B8	89	B8
	SP11_PCS1/FB_AD20/B9	90	B9
	ADC1_SE14/SP11_PCS0/UART3_RX/FB_AD19/B10	91	B10
	ADC1_SE15/SP11_SCK/UART3_TX/FB_AD18/B11	92	B11
	TS10_CH9/SP11_SOUT/UART0_RX/FB_AD17/B16	95	B16
	TS10_CH10/SP11_SIN/UART0_TX/FB_AD16/B17	96	B17
	TS10_CH11/CAN0_TX/FTM2_CH0/FTM2_QD_PHA/FA5/B18	97	B18
	TS10_CH12/CAN0_RX/FTM2_CH1/FTM2_QD_PHB/FA5/B19	98	B19
	SP12_SCK/FB_AD31/CMP0_OUT/B20	99	B20
	SP12_SCK/FB_AD30/CMP1_OUT/B21	100	B21
	SP12_SOUT/FB_AD29/CMP2_OUT/B22	101	B22
	SP12_SIN/SP10_PCS5/FB_AD28/B23	102	B23
	AD0_SE14/TS10_CH3/SP10_PCS4/TS20_TX/PDB0_EXTRG/FA6/C0	103	C0
	AD0_SE15/TS10_CH4/SP10_PCS3/FTM0_CH0/FA7/C1	104	C1
	AD0_SE4b/TS10_CH15/SP10_PCS2/FTM0_CH1/FA7/C12/C2	105	C2
	SP10_PCS1/UART1_RX/FTM0_CH2/FA7/CLKOUT/C3	106	C3
	SP10_PCS0/UART1_TX/FTM0_CH3/FA7/CLKOUT/C4	109	C4
	SP10_SCK/LPT0_AL12/FA7/AD10/CMP0_OUT/C5	111	C6
	SP10_SOUT/PDB0_EXTRG/FA7/C6	112	C7
	SP10_SIN/FA8/C7	113	C8
	ADC1_SE4b/FA7/C8	114	C9
	ADC1_SE5b/FA8/C9	115	C10
	ADC1_SE6b/I2C1_SCL/FA5/C10	116	C11
	AD0_SE7b/TS20_RX/D2C1_SDA/FA7/RW/C11	117	C12
	FB_AD27/C12	118	C13
	FB_AD26/C13	119	C14
	UART4_RX/FA7/AD25/C14	120	C15
	UART4_TX/FA7/AD24/C15	123	C16
	CAN1_RX/UART3_RX/FA7/C5/C16	124	C17
	CAN1_TX/UART3_TX/FA7/C5/C17	125	C18
	FB_CS3_bFB_TA_bC19	126	C19
	SP10_PCS0/FA7/CS1_bD0	127	D0
	AD0_SE5b/SP10_SCK/FA7/C0_bD1	128	D1
	SP10_SOUT/UART2_RX/FA7/AD3/D2	129	D2
	SP10_SIN/UART2_TX/FA7/AD3/D3	130	D3
	SP10_PCS1/FTM0_CH4/FA7/AD2/D4	131	D4
	AD0_SE6b/SP10_PCS2/FTM0_CH5/FA7/AD1/D5	132	D5
	AD0_SE7b/SP10_PCS3/UART0_RX/FTM0_CH6/FA7/AD0/D6	133	D6
	UART0_TX/FTM0_CH7/D7	136	D7
	UART5_RX/I2C0_SCL/FA7/AD16/D8	137	D8
	UART5_TX/I2C0_SDA/FA7/AD19/D9	138	D9
	FB_A18/D10	139	D10
	SP12_PCS0/SDHC0_CLKIN/FA7/A19/D11	140	D11
	SP12_SCK/SDHC0_D4/FA7/A20/D12	141	D12
	SP12_SOUT/SDHC0_D5/FA7/A21/D13	142	D13
	SP12_SIN/SDHC0_D6/FA7/A22/D14	143	D14
	SP12_PCS1/SDHC0_D7/FA7/A23/D15	144	D15
	ADC1_SE4a/SP11_PCS1/UART1_TX/SDHC0_D1/D2C1_SDA/E0	1	E0
	ADC1_SE5a/SP11_SOUT/UART1_RX/SDHC0_D0/D2C1_SCL/E1	2	E1
	ADC1_SE6a/SP11_SCK/SDHC0_D2/D2C1_SCL/E2	3	E2
	ADC1_SE7a/SP11_SIN/SDHC0_CMD/E3	4	E3
	SP11_PCS0/UART3_TX/SDHC0_D3/E4	7	E4
	SP11_PCS2/UART3_RX/SDHC0_D2/E5	8	E5
	SP11_PCS3/E6	9	E6
	UART5_TX/E7	10	E7
	UART5_TX/E8	17	E8
	E11	12	E9
	E12	13	E10
	E10	14	E11
	E11	15	E12
	AD0_SE17/CAN1_TX/UART4_TX/E24	45	E24
	AD0_SE18/CAN1_RX/UART4_RX/E25	46	E25
	E26/RTC_CLKOUT/ENET_1588_CLKIN	47	E26
	E27	48	E27
		49	E28



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Title				K60-144核心板原理图			
Size		Number			Revision		
A3		超核电子科技			V2.1		
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