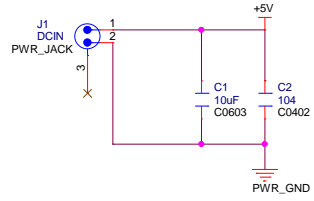
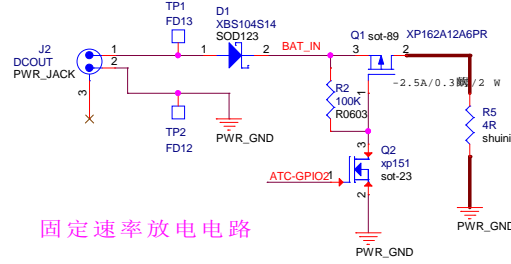


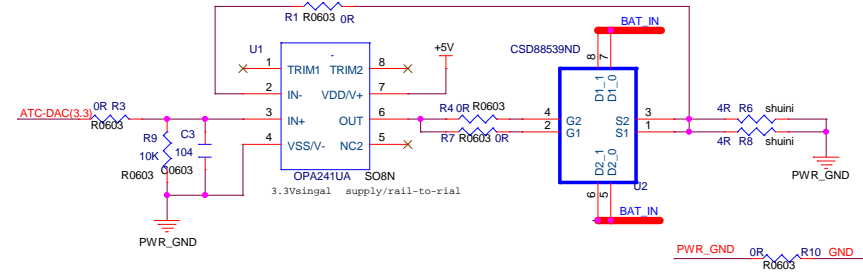
供电



放电电路



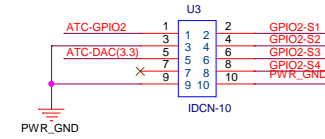
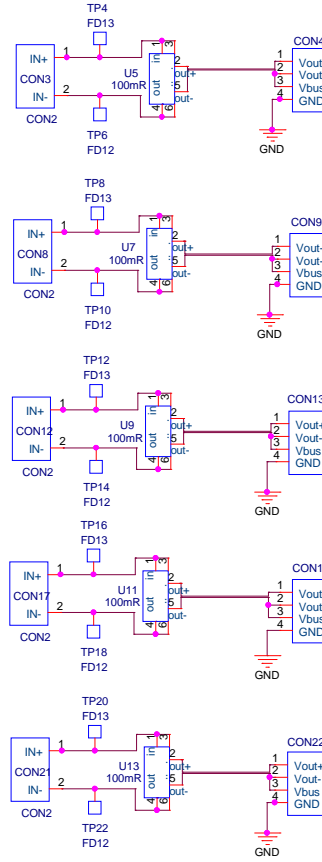
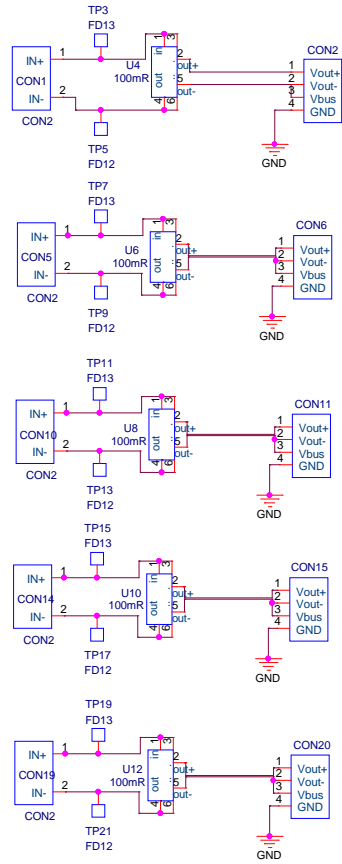
放电速率调节电路



I-V

sheild GND

I-V



GPIO2-S1 JP1 GPIO
GPIO2-S3 JP2 GPIO
PWR_GND JP3 GND

120mil
100mil

Title			<Title>
Size	Document	Number	Rev
A3	<Doc>		<RevCode>
Date:	Tuesday, August 01, 2017		Sheet 1 of 1